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TUNING APPARATUS**Abstract**

A Tuner for receiving a video broadcast signal comprises commandable
5 circuits (6, 7, 8, 17) to select a tuned frequency, the tuner comprising a bus interface
(21) receiving data bytes and among those data bytes, bytes representing a divider
ratio N, and address bytes. Bytes received by the bus interface (21) are processed
through a N divider comparator (23) comparing the value of divider ratio N received
from the bus interface (21) to values stored in a register (24), the comparator (23)
10 outputting control bytes and data bytes to be sent to the commandable circuit (6, 7,
8, 17) of the tuner to select a band and a tuned frequency. The tuner may be
implemented on a chassis of a receiver without modification of an existing software
included with a microprocessor of the chassis. The values stored in the register (24)
are customised values of the tuner.

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Figure 2



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