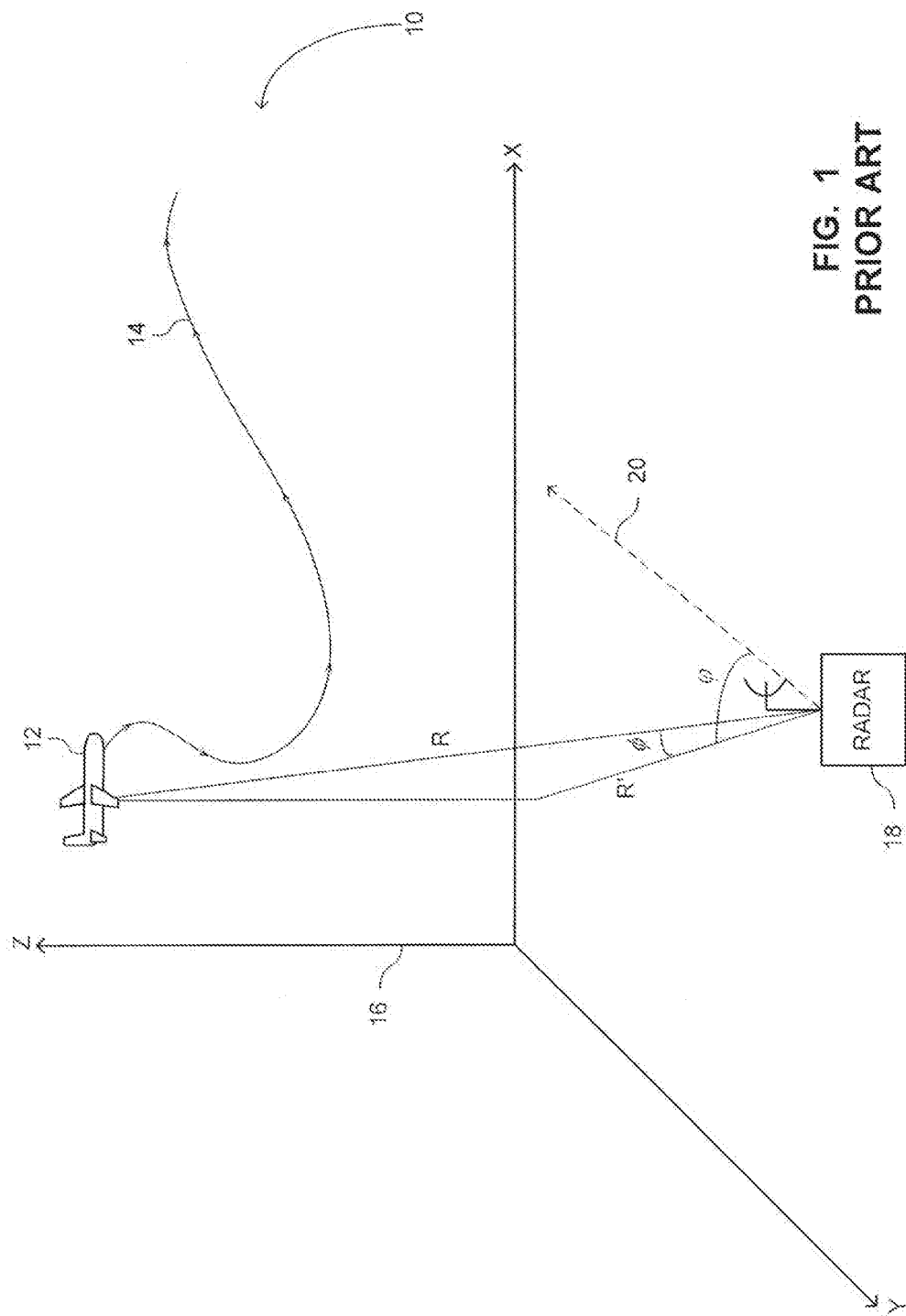




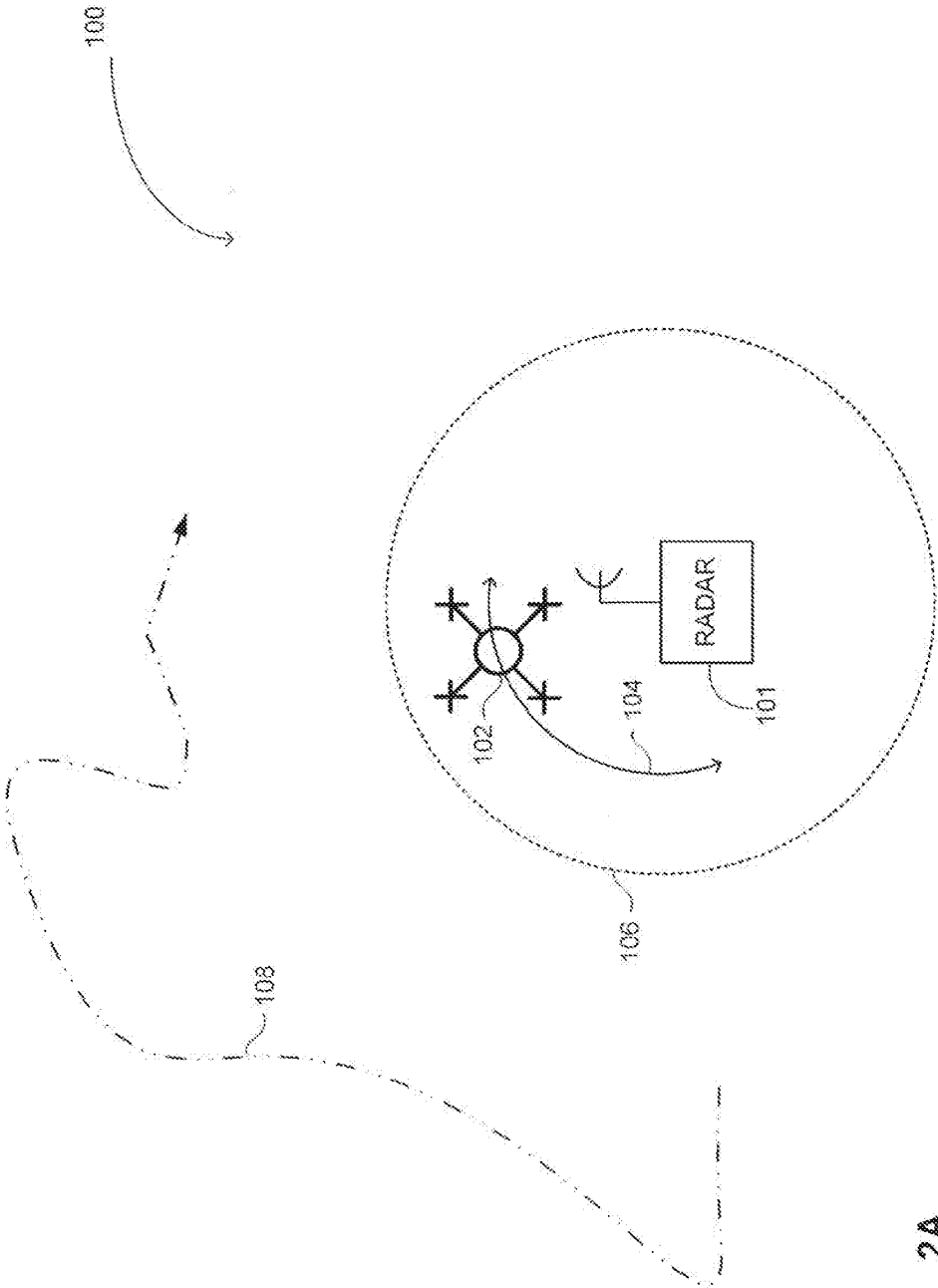


FIG. 2A

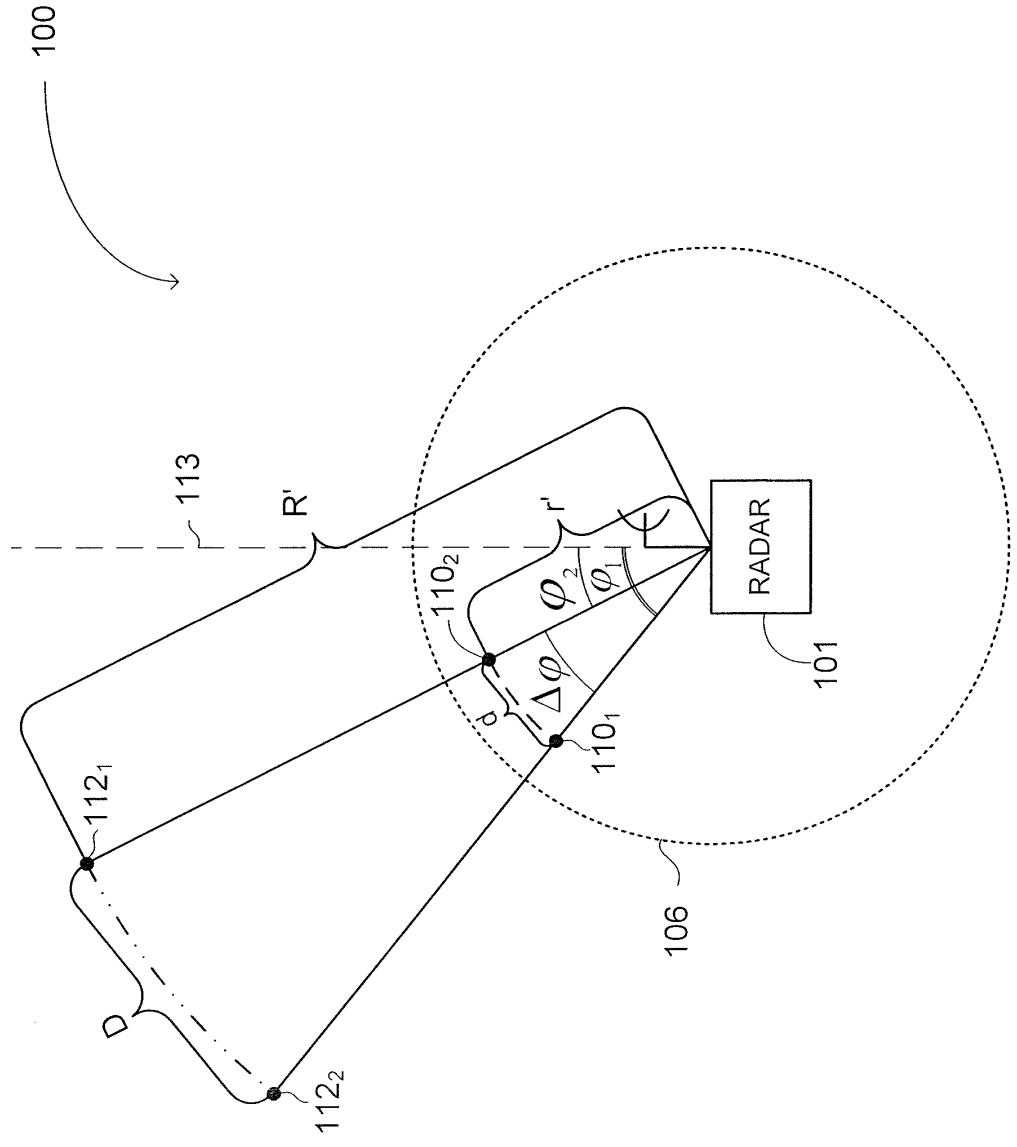


FIG. 2B

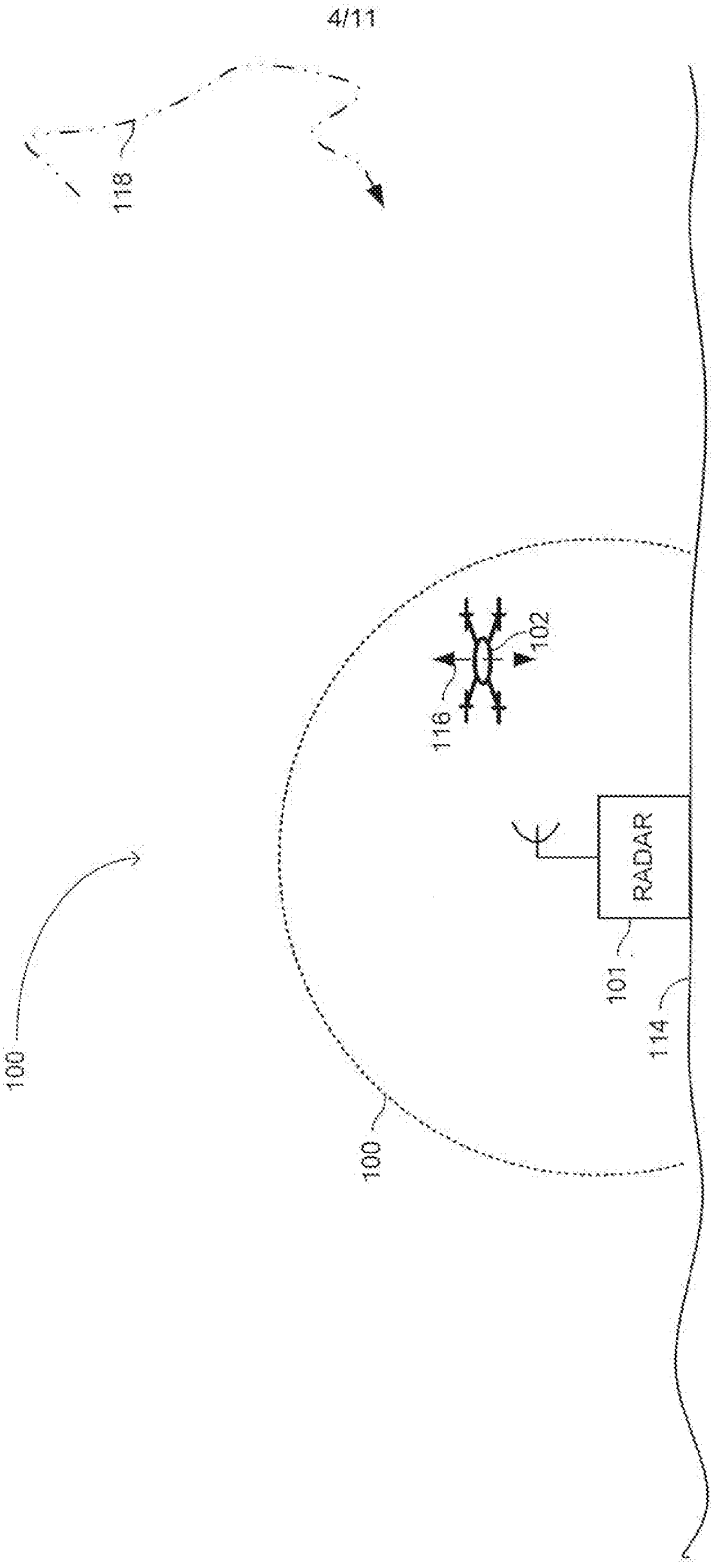


FIG. 2C

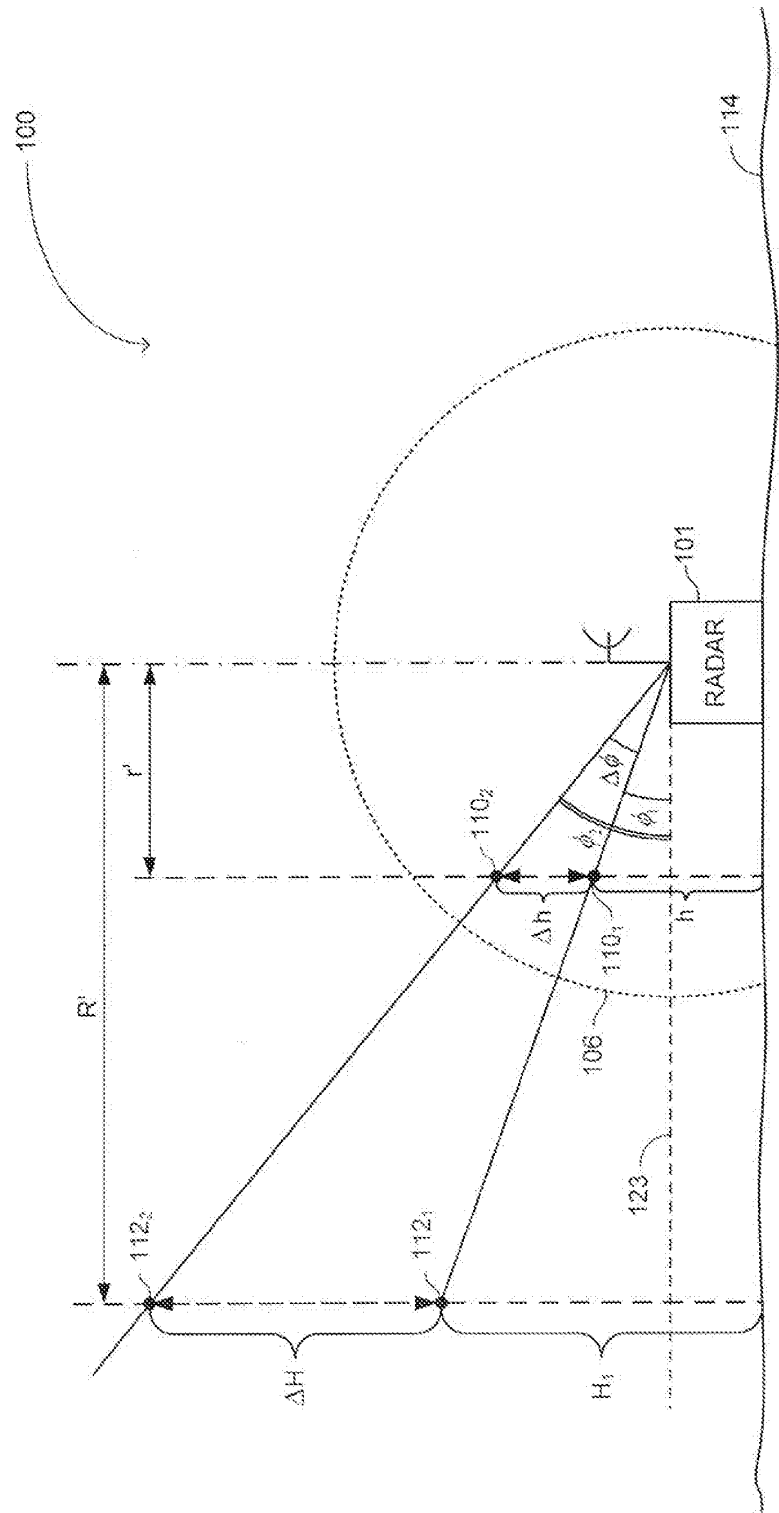


FIG. 2D

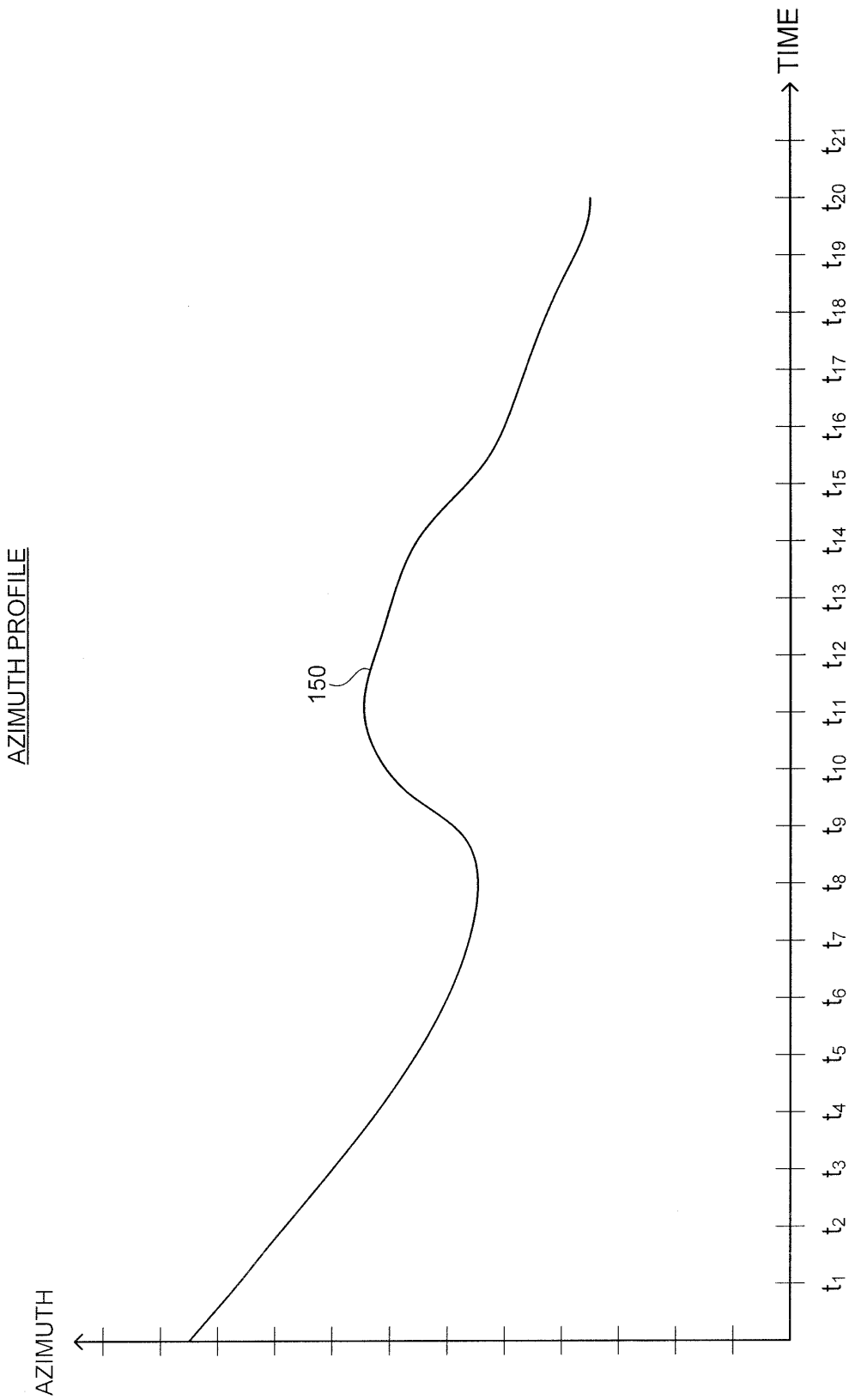


FIG. 3A

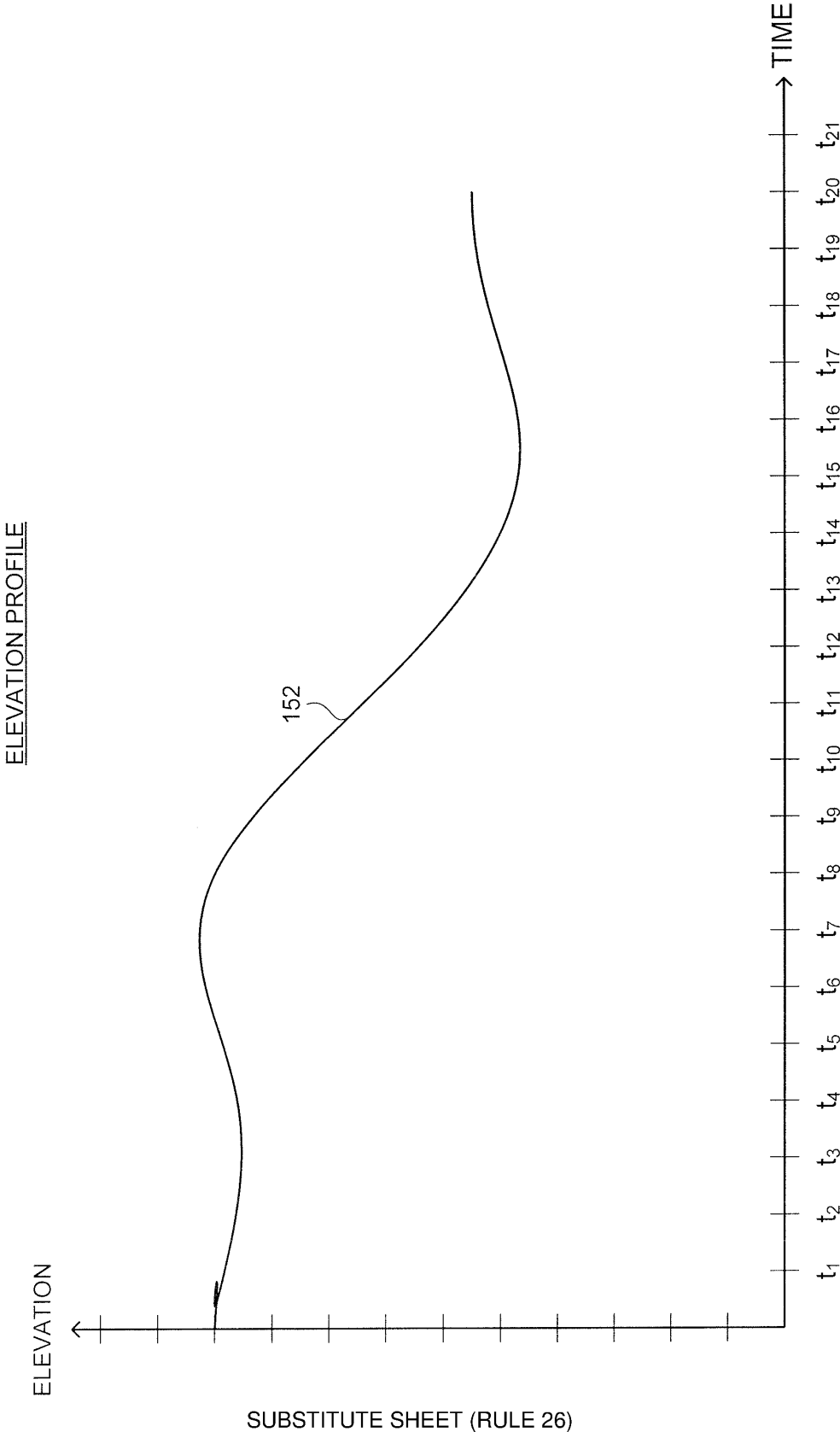


FIG. 3B

DELAY PROFILE

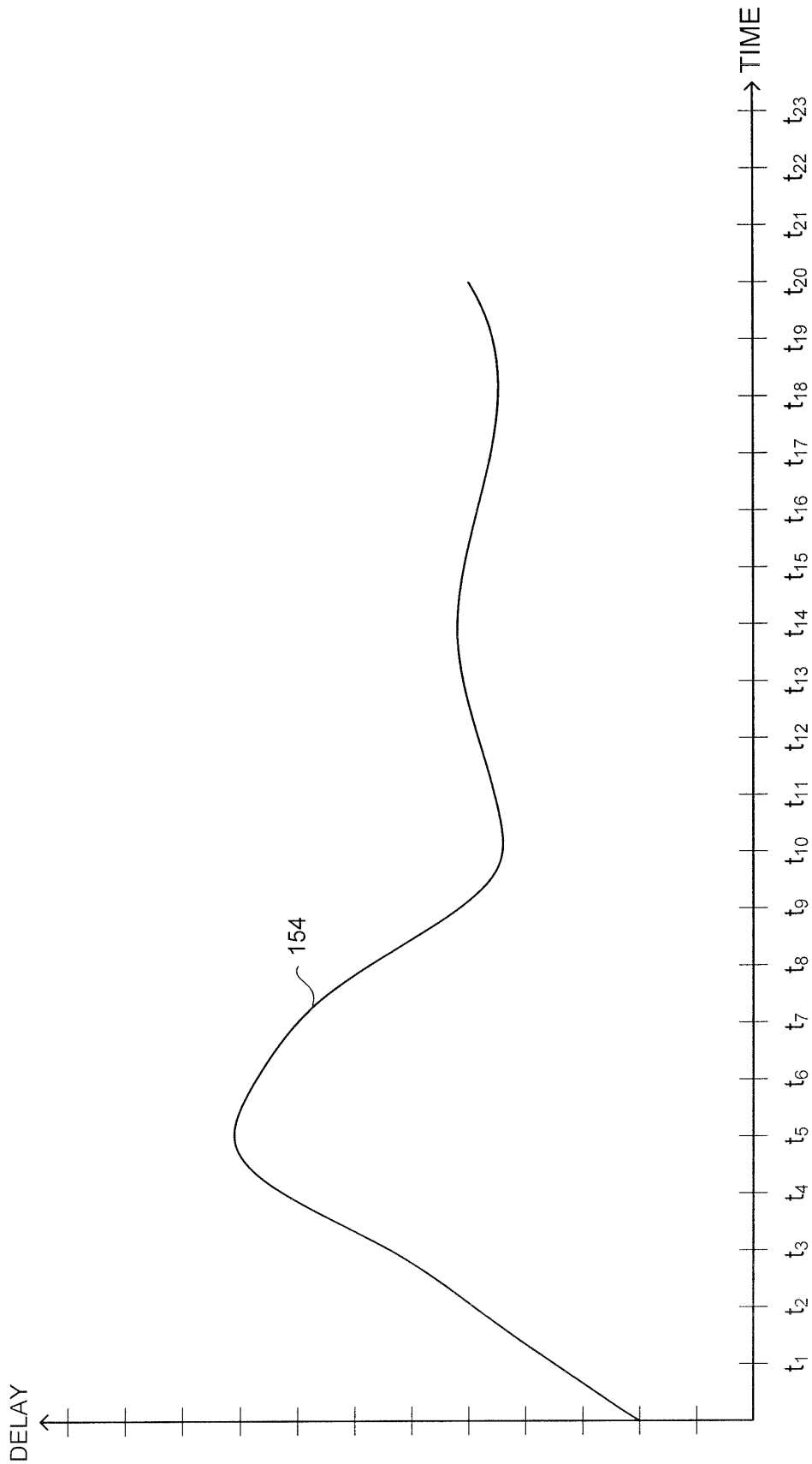


FIG. 3C

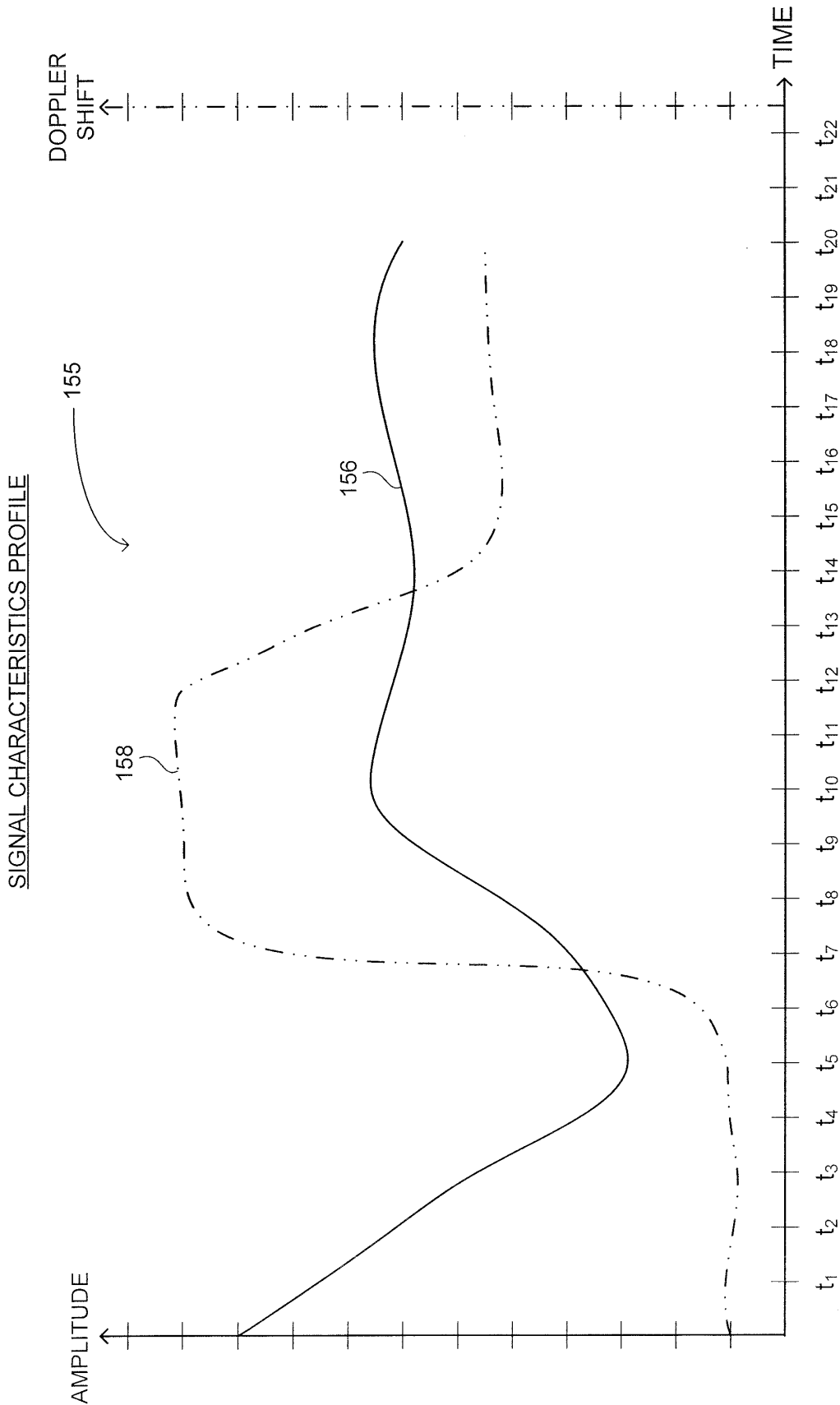


FIG. 3D

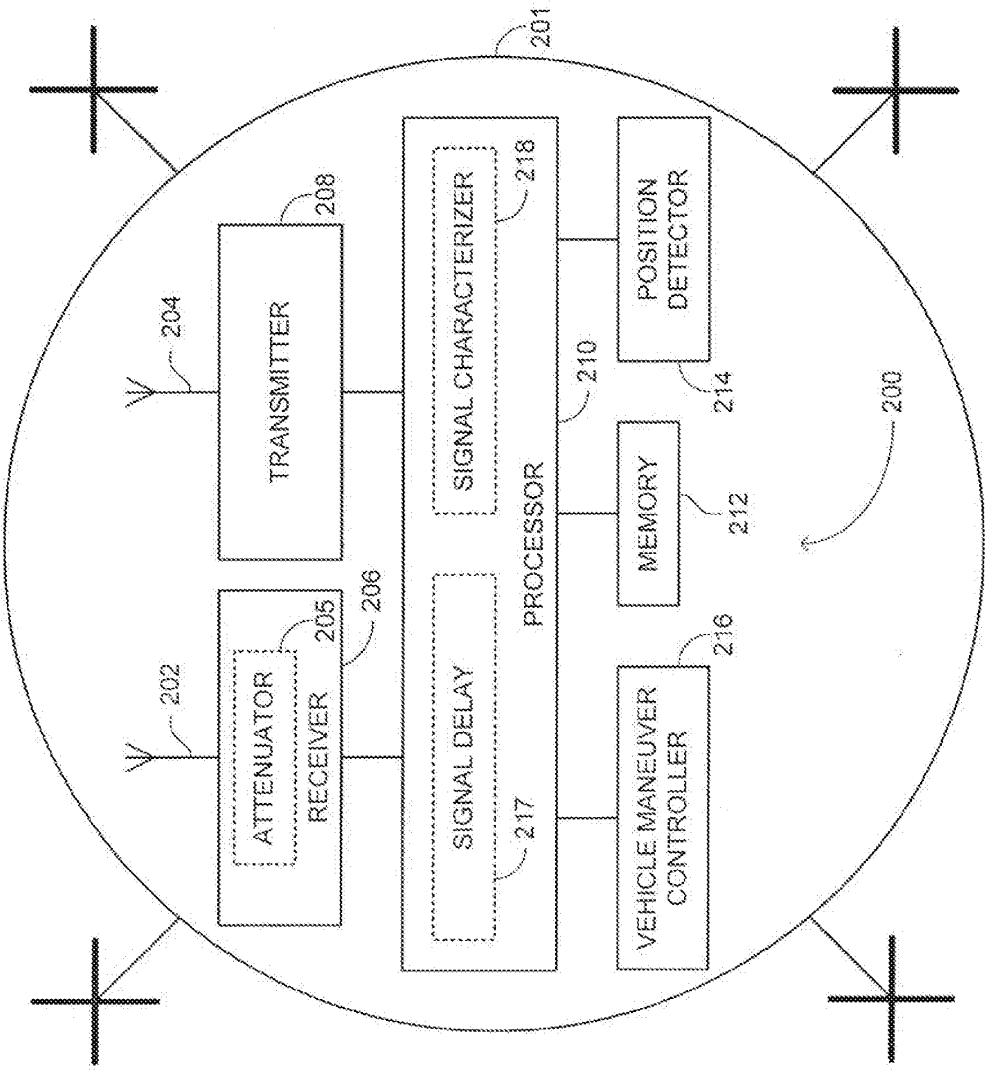


FIG. 4

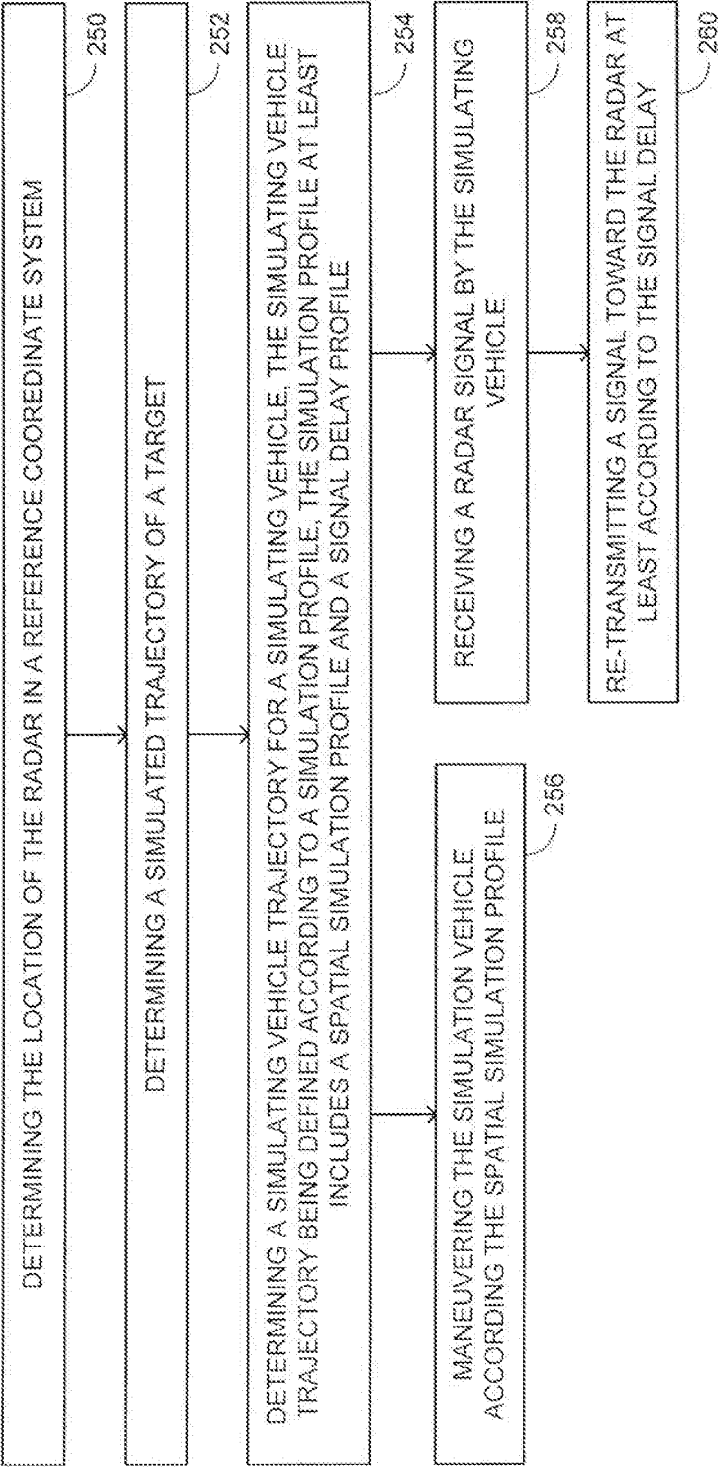


FIG. 5