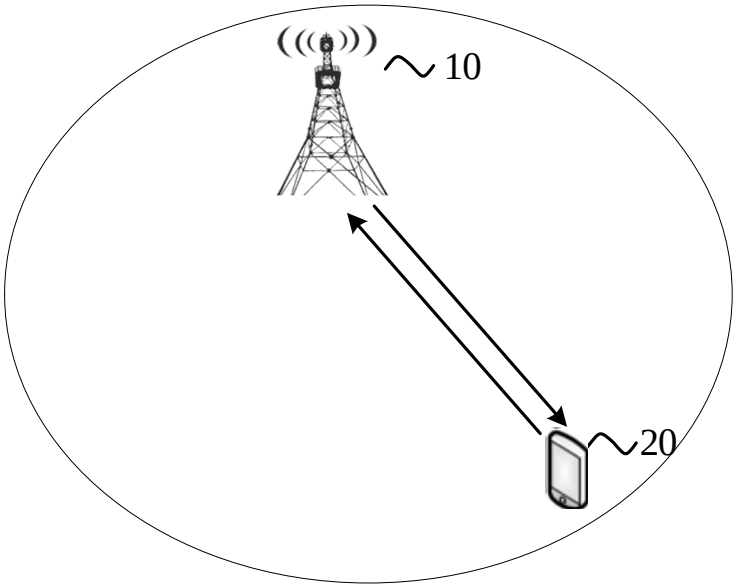
10
20

FIG. 1

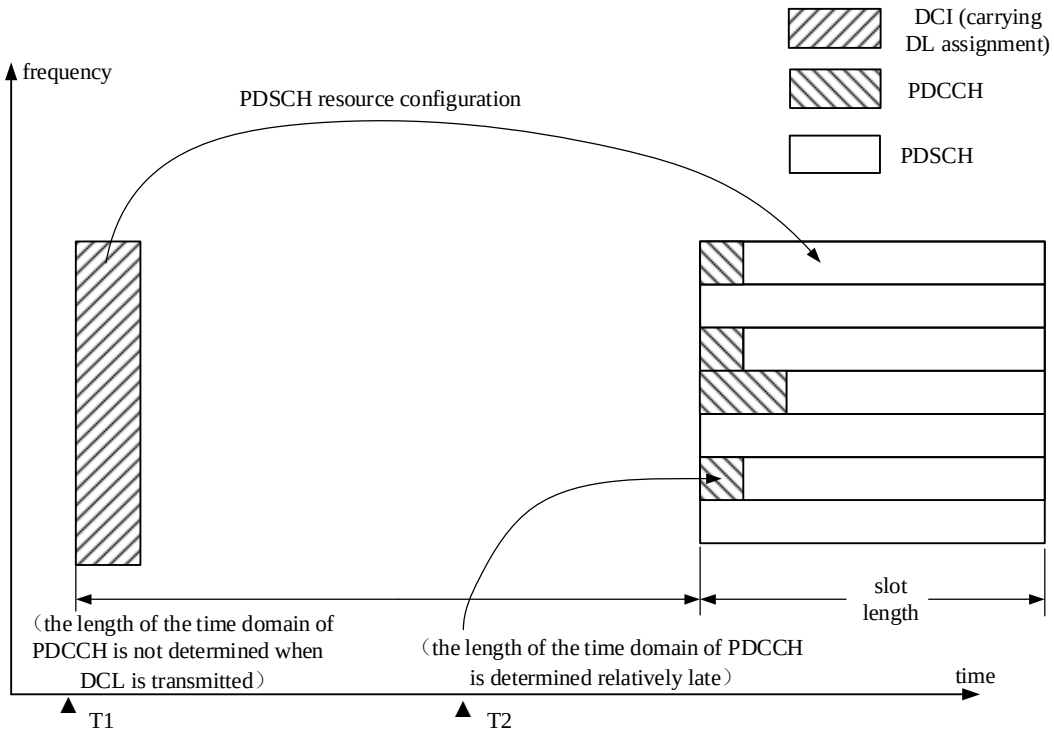
DCI (carrying DL assignment)
PDCCH
PDSCH
frequency
PDSCH resource configuration
slot length
(the length of the time domain of PDCCH is not determined when DCL is transmitted)
(the length of the time domain of PDCCH is determined relatively late)
time
T1
T2

FIG. 2

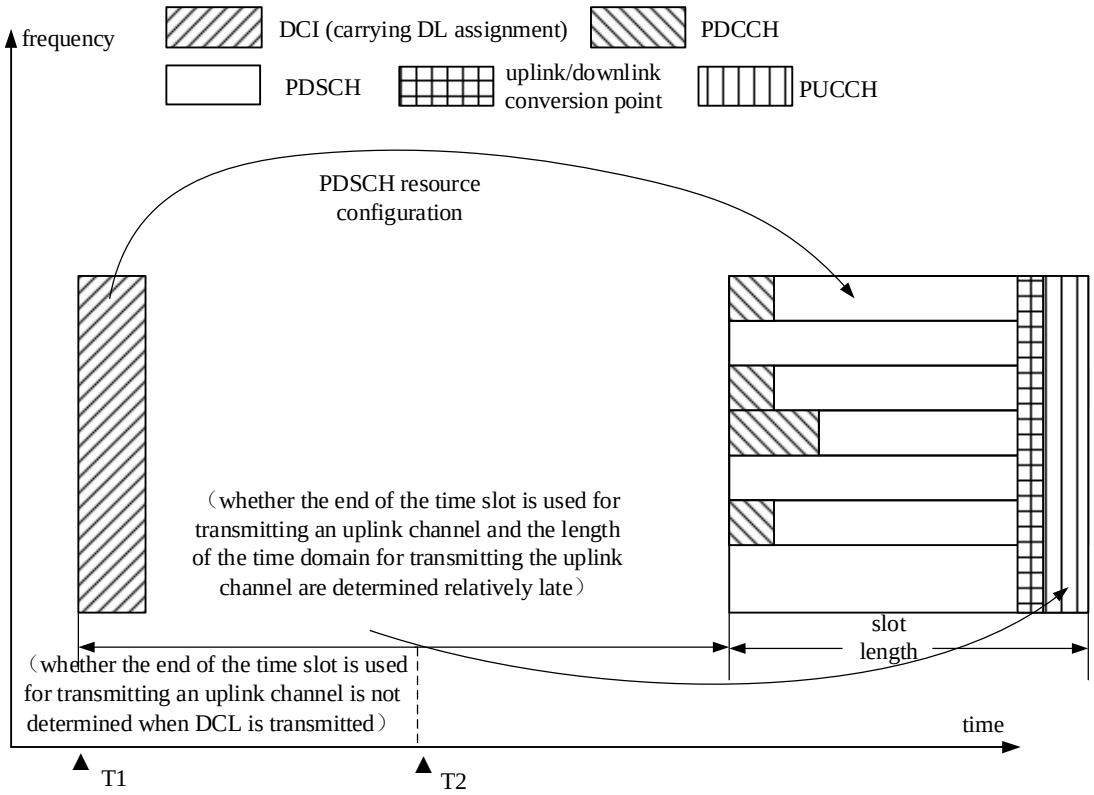
frequency
DCI (carrying DL assignment)
PDCCH
PDSCH
uplink/downlink conversion point
PUCCH
PDSCH resource configuration
（whether the end of the time slot is used for transmitting an uplink channel and the length of the time domain for transmitting the uplink channel are determined relatively late）
slot length
（whether the end of the time slot is used for transmitting an uplink channel is not determined when DCL is transmitted）
time
T1
T2

FIG. 3

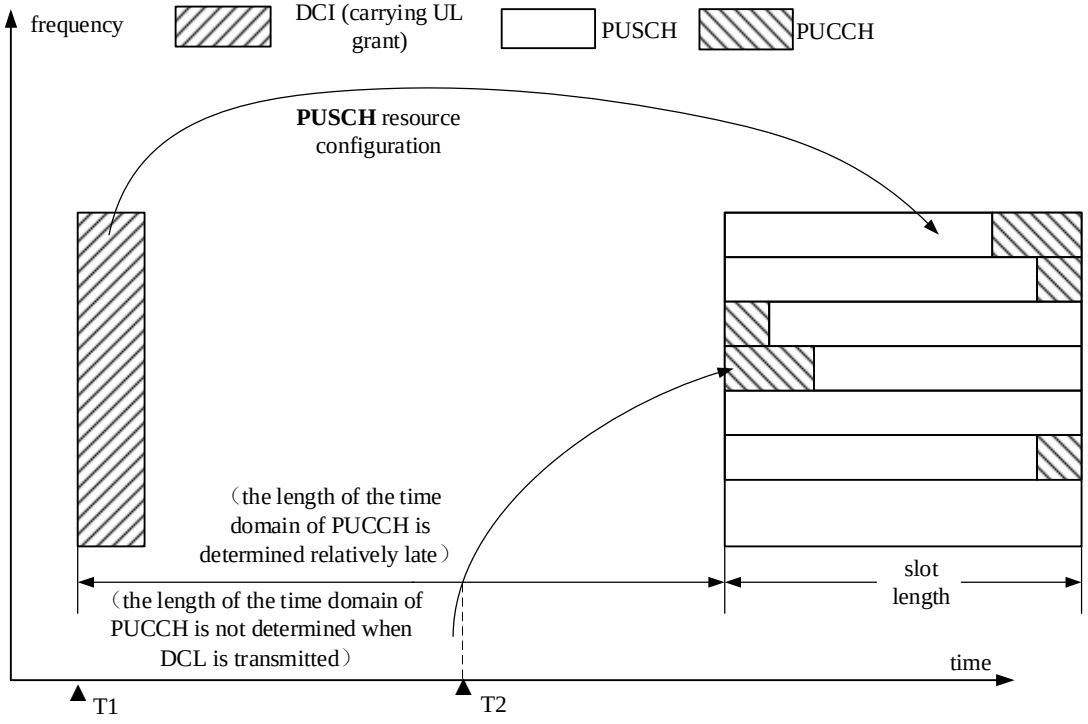
frequency
DCI (carrying UL grant)
PUSCH
PUCCH
PUSCH resource configuration
（the length of the time domain of PUCCH is determined relatively late）
slot length
（the length of the time domain of PUCCH is not determined when DCL is transmitted）
time
T1
T2

FIG. 4

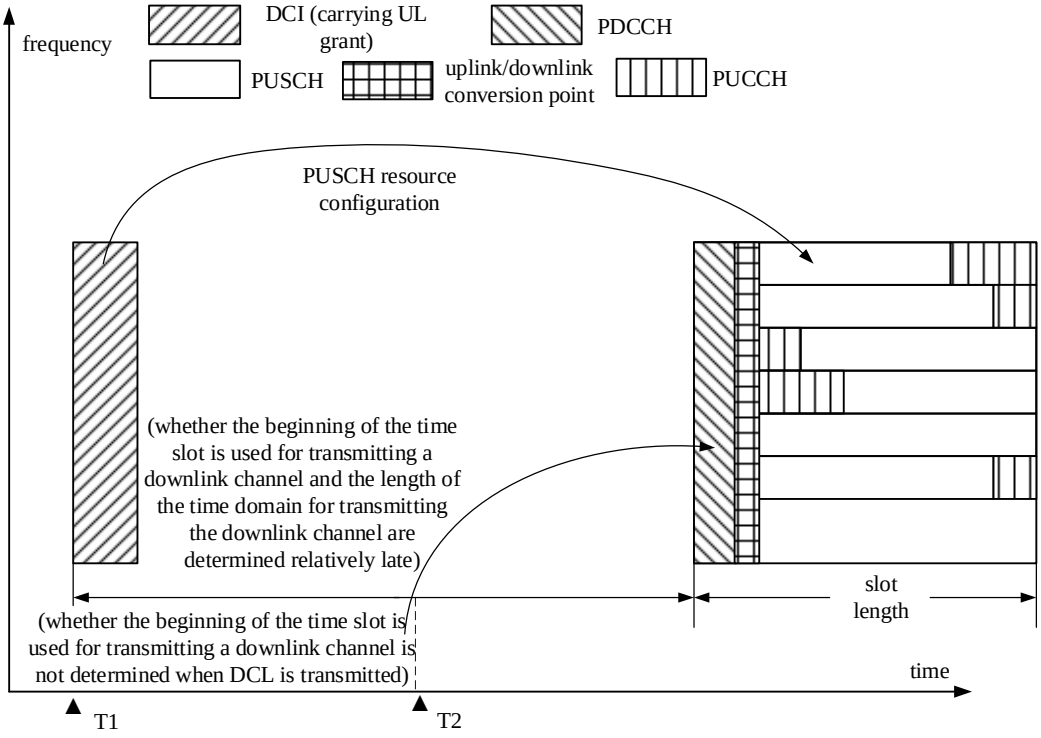
frequency
DCI (carrying UL grant)
PDCCH
PUSCH
uplink/downlink conversion point
PUCCH
PUSCH resource configuration
(whether the beginning of the time slot is used for transmitting a downlink channel and the length of the time domain for transmitting the downlink channel are determined relatively late)
slot length
(whether the beginning of the time slot is used for transmitting a downlink channel is not determined when DCL is transmitted)
time
T1
T2

FIG. 5

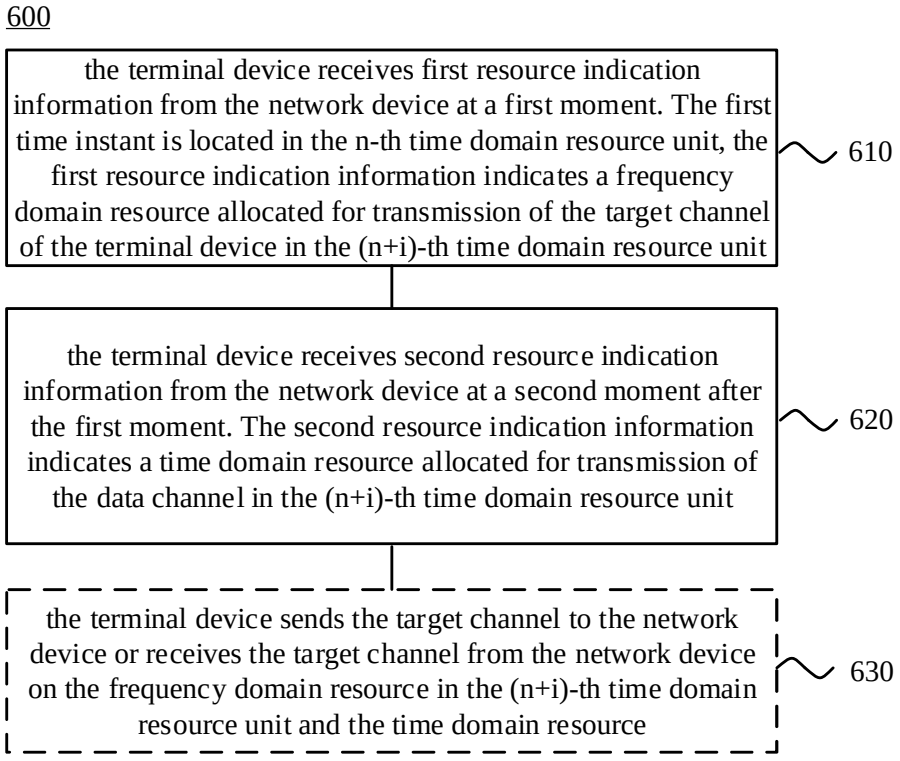
600
the terminal device receives first resource indication information from the network device at a first moment. The first time instant is located in the n-th time domain resource unit, the first resource indication information indicates a frequency domain resource allocated for transmission of the target channel of the terminal device in the (n+i)-th time domain resource unit
610
the terminal device receives second resource indication information from the network device at a second moment after the first moment. The second resource indication information indicates a time domain resource allocated for transmission of the data channel in the (n+i)-th time domain resource unit
620
the terminal device sends the target channel to the network device or receives the target channel from the network device on the frequency domain resource in the (n+i)-th time domain resource unit and the time domain resource
630

FIG. 6

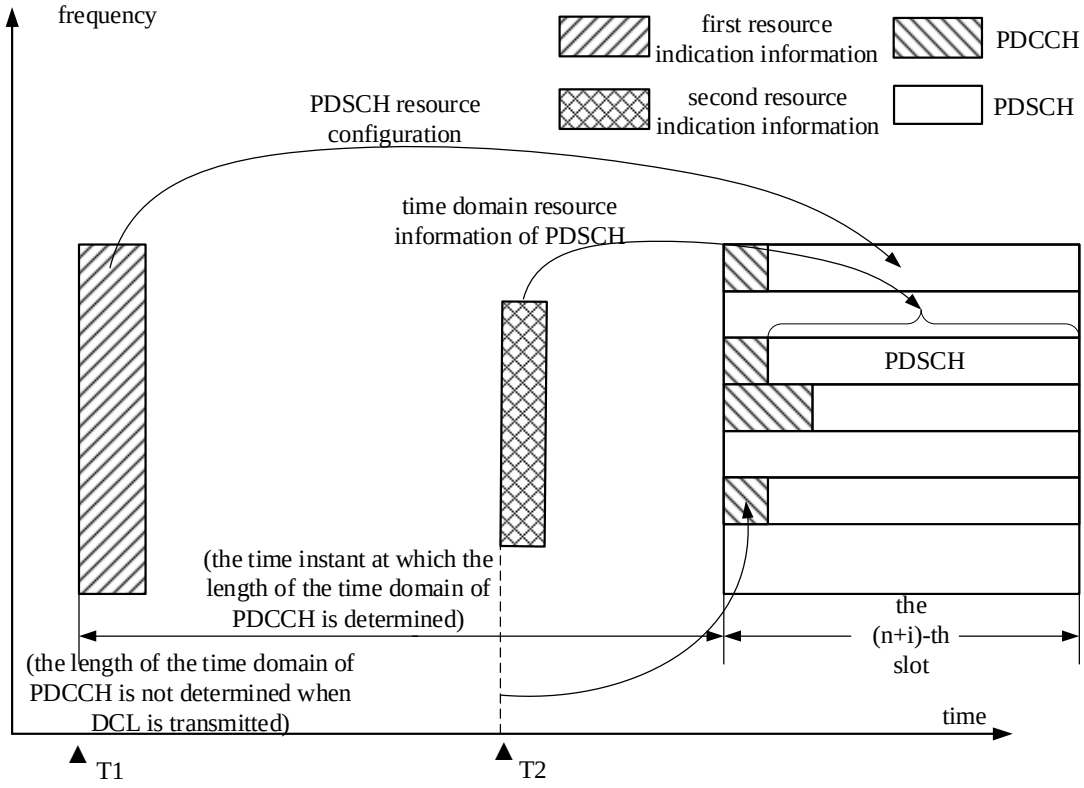
frequency
first resource indication information
PDCCH
second resource indication information
PDSCH
PDSCH resource configuration
time domain resource information of PDSCH
PDSCH
(the time instant at which the length of the time domain of PDCCH is determined)
the (n+i)-th slot
(the length of the time domain of PDCCH is not determined when DCL is transmitted)
time
T1
T2

FIG. 7

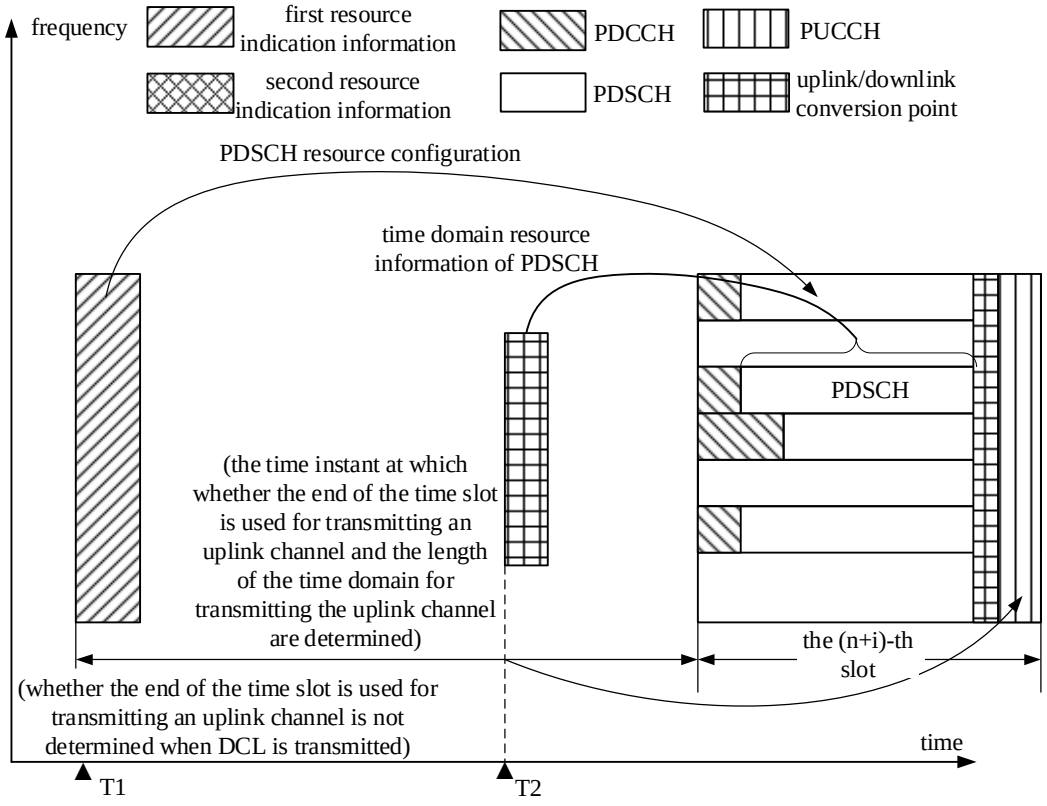
frequency
first resource indication information
PDCCH
PUCCH
second resource indication information
PDSCH
uplink/downlink conversion point
PDSCH resource configuration
time domain resource information of PDSCH
PDSCH
(the time instant at which whether the end of the time slot is used for transmitting an uplink channel and the length of the time domain for transmitting the uplink channel are determined)
the (n+i)-th slot
(whether the end of the time slot is used for transmitting an uplink channel is not determined when DCL is transmitted)
time
T1
T2

FIG. 8

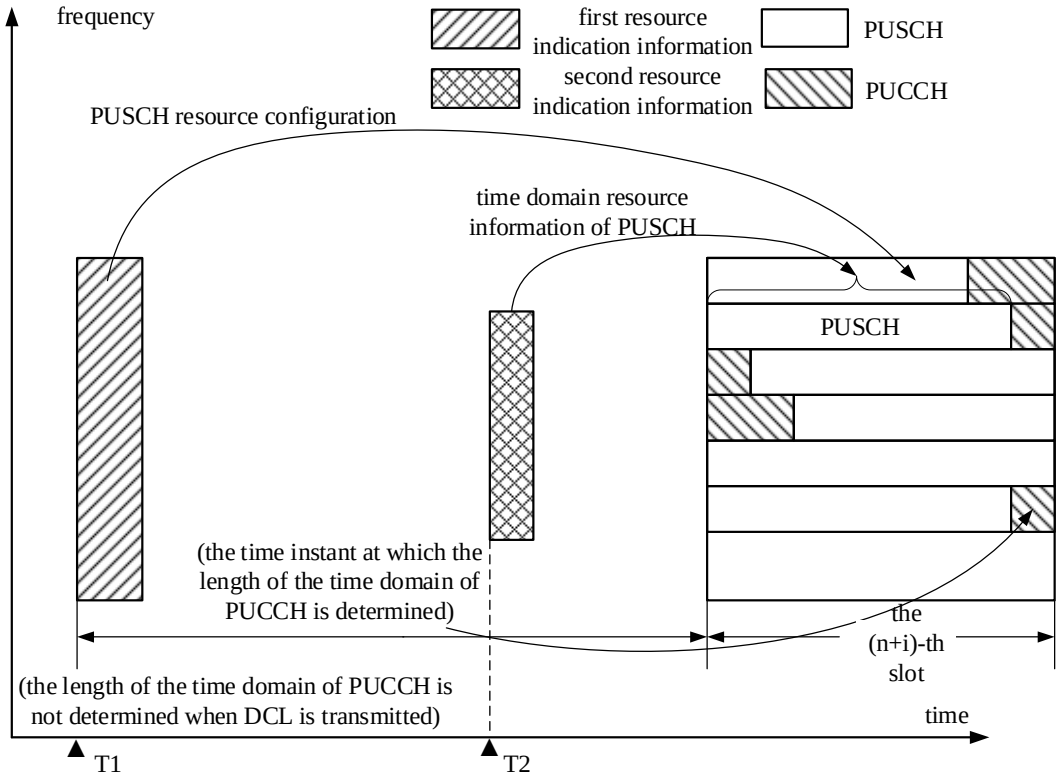
frequency
first resource indication information
PUSCH
second resource indication information
PUCCH
PUSCH resource configuration
time domain resource information of PUSCH
PUSCH
(the time instant at which the length of the time domain of PUCCH is determined)
the (n+i)-th slot
(the length of the time domain of PUCCH is not determined when DCL is transmitted)
time
T1
T2

FIG. 9

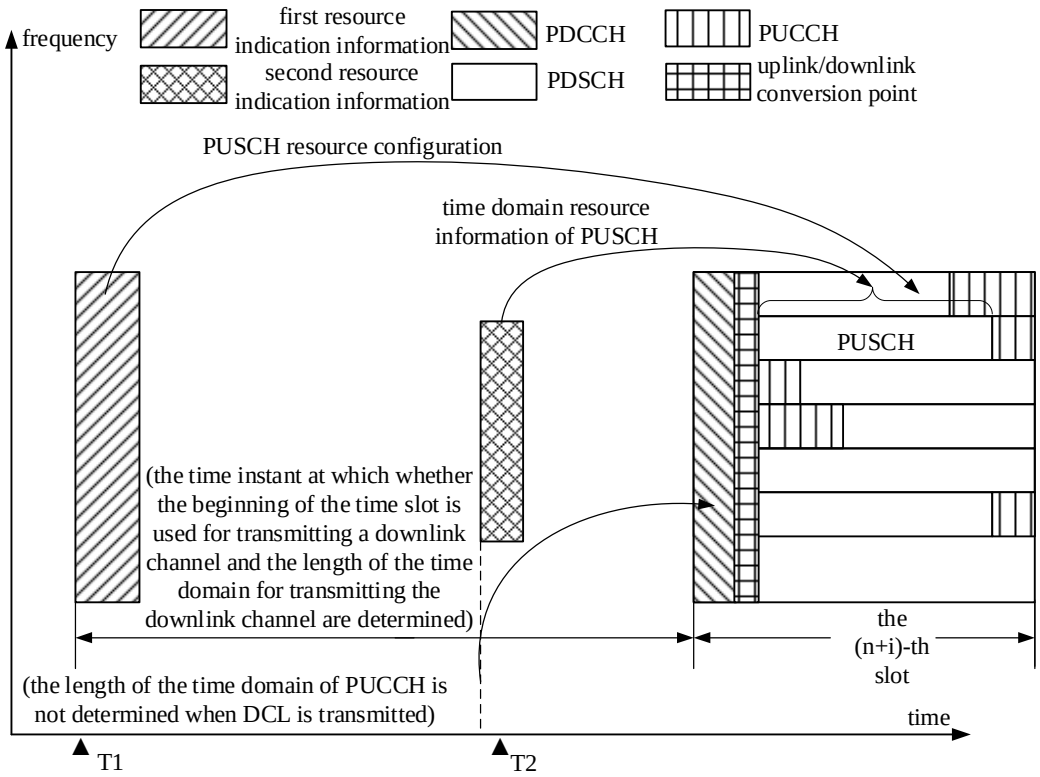
frequency
first resource indication information
PDCCH
PUCCH
second resource indication information
PDSCH
uplink/downlink conversion point
PUSCH resource configuration
time domain resource information of PUSCH
PUSCH
(the time instant at which whether the beginning of the time slot is used for transmitting a downlink channel and the length of the time domain for transmitting the downlink channel are determined)
the (n+i)-th slot
(the length of the time domain of PUCCH is not determined when DCL is transmitted)
time
T1
T2

FIG. 10

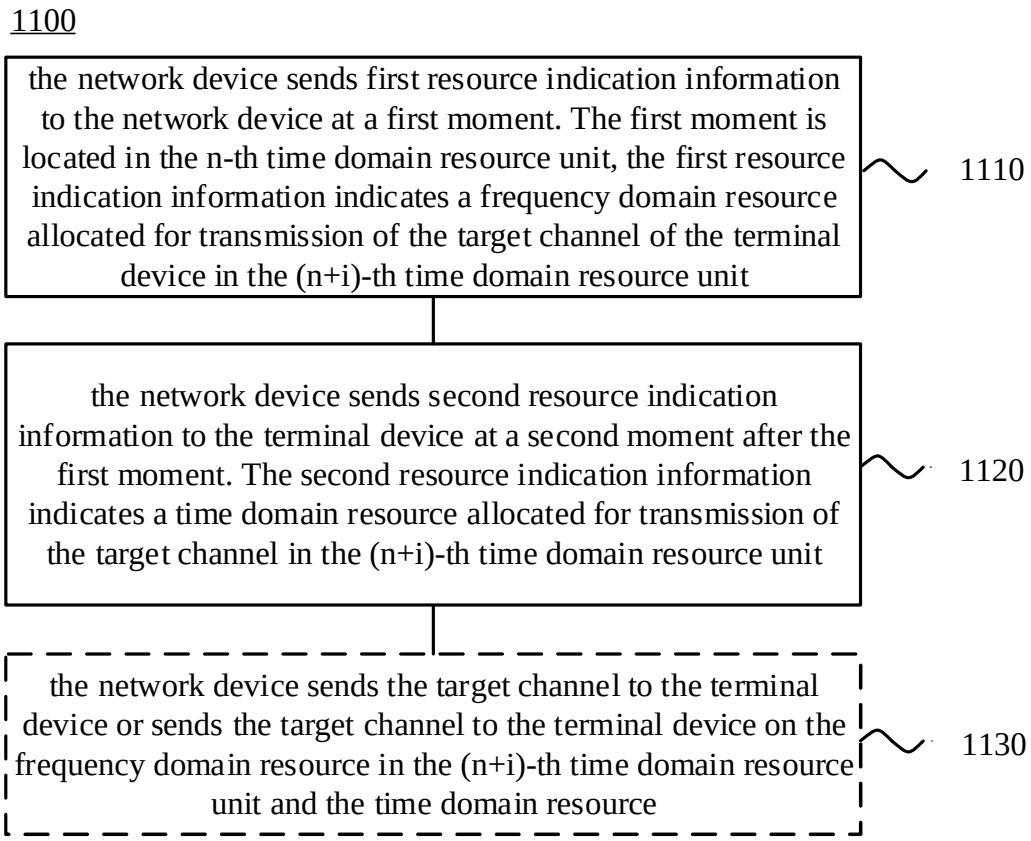
1100
the network device sends first resource indication information to the network device at a first moment. The first moment is located in the n-th time domain resource unit, the first resource indication information indicates a frequency domain resource allocated for transmission of the target channel of the terminal device in the (n+i)-th time domain resource unit
1110
the network device sends second resource indication information to the terminal device at a second moment after the first moment. The second resource indication information indicates a time domain resource allocated for transmission of the target channel in the (n+i)-th time domain resource unit
1120
the network device sends the target channel to the terminal device or sends the target channel to the terminal device on the frequency domain resource in the (n+i)-th time domain resource unit and the time domain resource
1130

FIG. 11

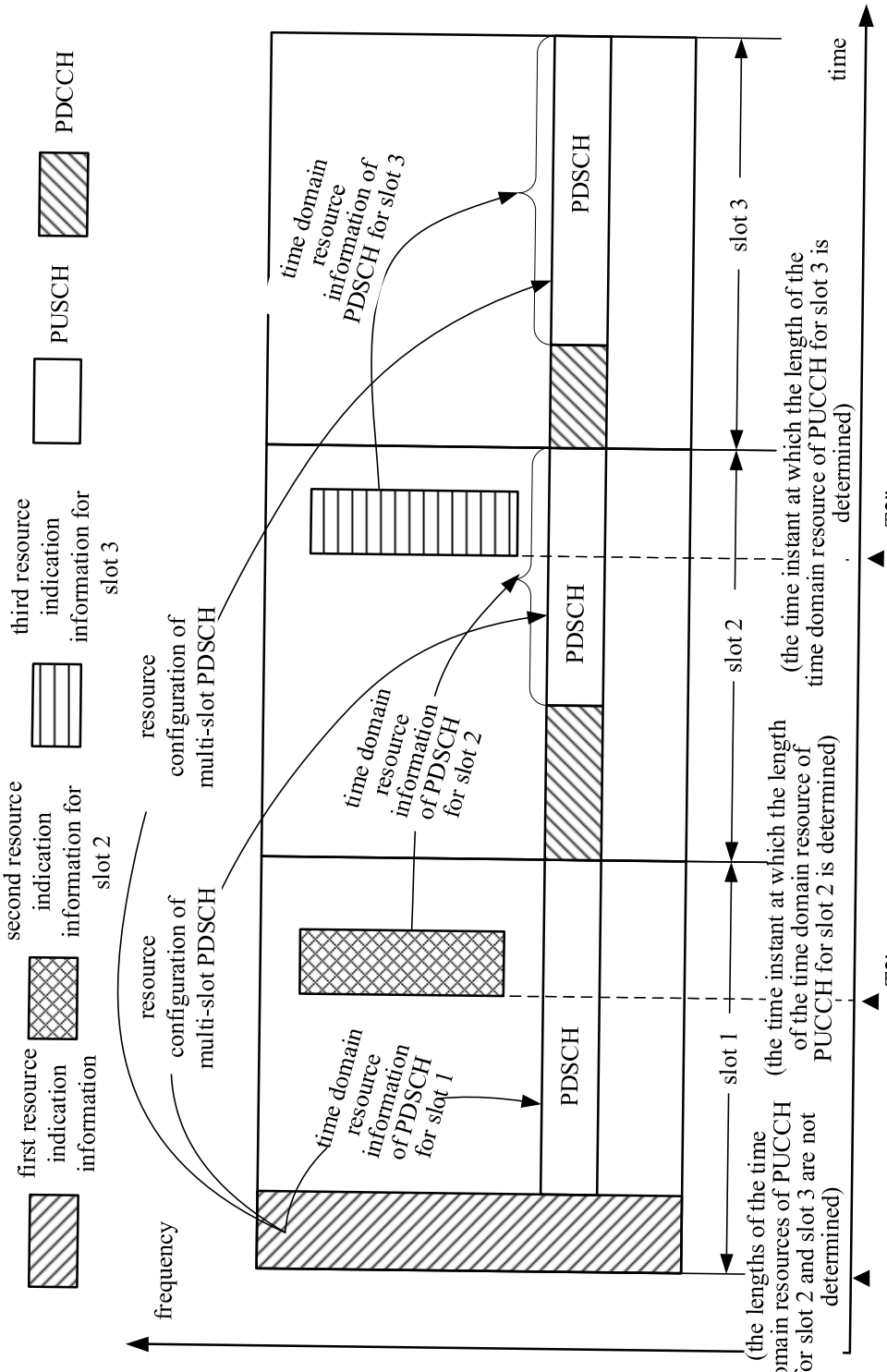
first resource indication information
second resource indication information for slot 2
third resource indication information for slot 3
PUSCH
PDCCH
resource configuration of multi-slot PDSCH
resource configuration of multi-slot PDSCH
time domain resource information of PDSCH for slot 1
time domain resource information of PDSCH for slot 2
time domain resource information of PDSCH for slot 3
PDSCH
PDSCH
PDSCH
slot 1
slot 2
slot 3
(the lengths of the time domain resources of PUCCH for slot 2 and slot 3 are not determined)
(the time instant at which the length of the time domain resource of PUCCH for slot 2 is determined)
(the time instant at which the length of the time domain resource of PUCCH for slot 3 is determined)
T1
T2'
T2"
frequency
time

FIG. 12

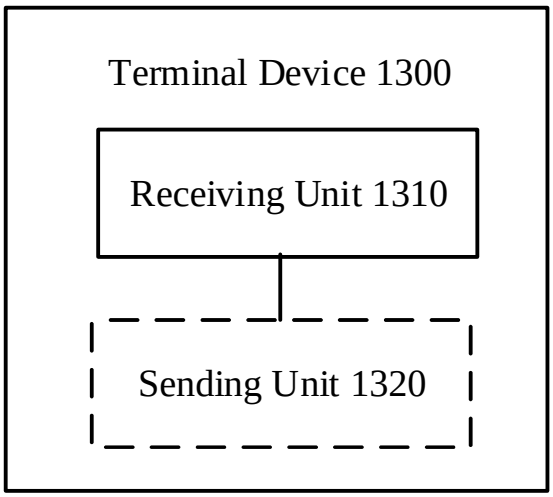
Terminal Device 1300
Receiving Unit 1310
Sending Unit 1320

FIG. 13

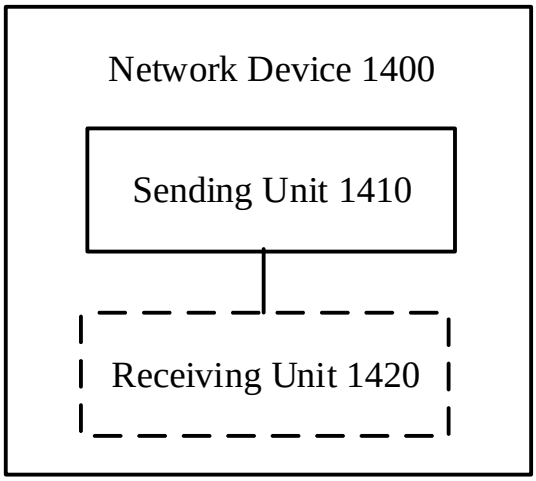
Network Device 1400
Sending Unit 1410
Receiving Unit 1420

FIG. 14

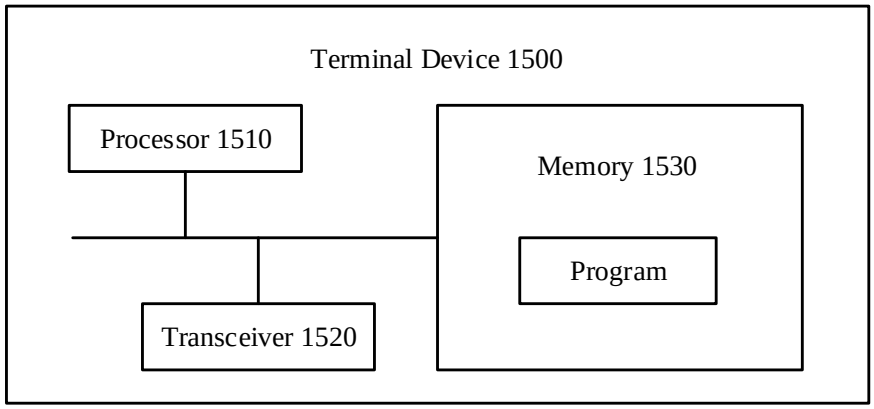
Terminal Device 1500
Processor 1510
Memory 1530
Program
Transceiver 1520

FIG. 15

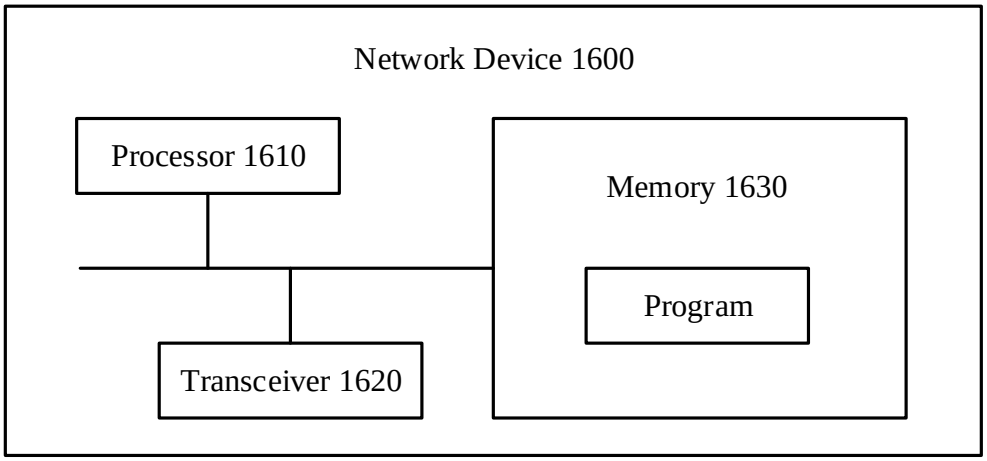
Network Device 1600
Processor 1610
Memory 1630
Program
Transceiver 1620

FIG. 16

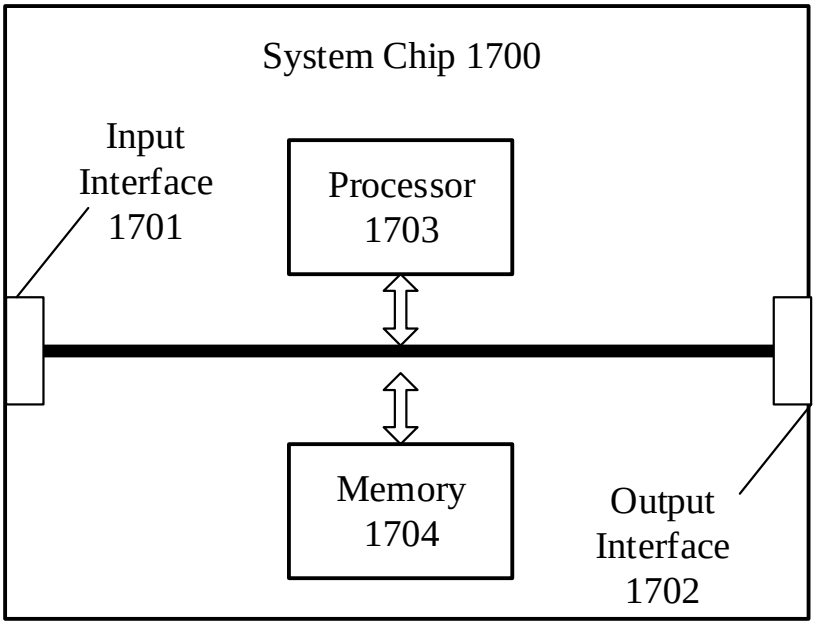
System Chip 1700
Input Interface 1701
Processor 1703
Memory 1704
Output Interface 1702

FIG. 17