

METHOD AND APPARATUS FOR PROTECTING PORTIONS OF MEMORY

ABSTRACT

A memory (20) in a computer system includes a visible portion (30) and a hidden portion (60). The visible portion of the memory is addressable by a processor (15a-15n) and the operating system (40) operating within the computer system. Addressability by either the processor (15a-15n) or the operating system (40) is excluded to the hidden portion of the memory (60). The hidden portion of the memory is used for storing data transmitted by either the processor or the operating system. A communications area (80), located in the visible portion of memory receives requests for data access from either the processor or the operating system. A hidden server (70) which addresses both the visible (30) portion of memory and the hidden (60) portion of memory receives requests for data access from the communications area (80) and initiates data access from the hidden memory.

Fig. 1