

TITLE:
DISPLAY APPARATUS

ABSTRACT

5 A display apparatus is provided. The display apparatus includes a display module
and multiple light-emitting driving circuits. Each of the light-emitting driving circuits
includes a timing control circuit and a driving circuit. The timing control circuit receives
multiple clock signals and a previous light-emitting timing signal to provide a light-emitting
timing signal and an internal voltage. The driving circuit receives a first phase signal
10 among multiple phase signals and the internal voltage to provide a light-emitting driving
signal to the display module based on the first phase signal and the internal voltage. The
phase signals all present disabled levels during a vertical blank period.

FIG. 1