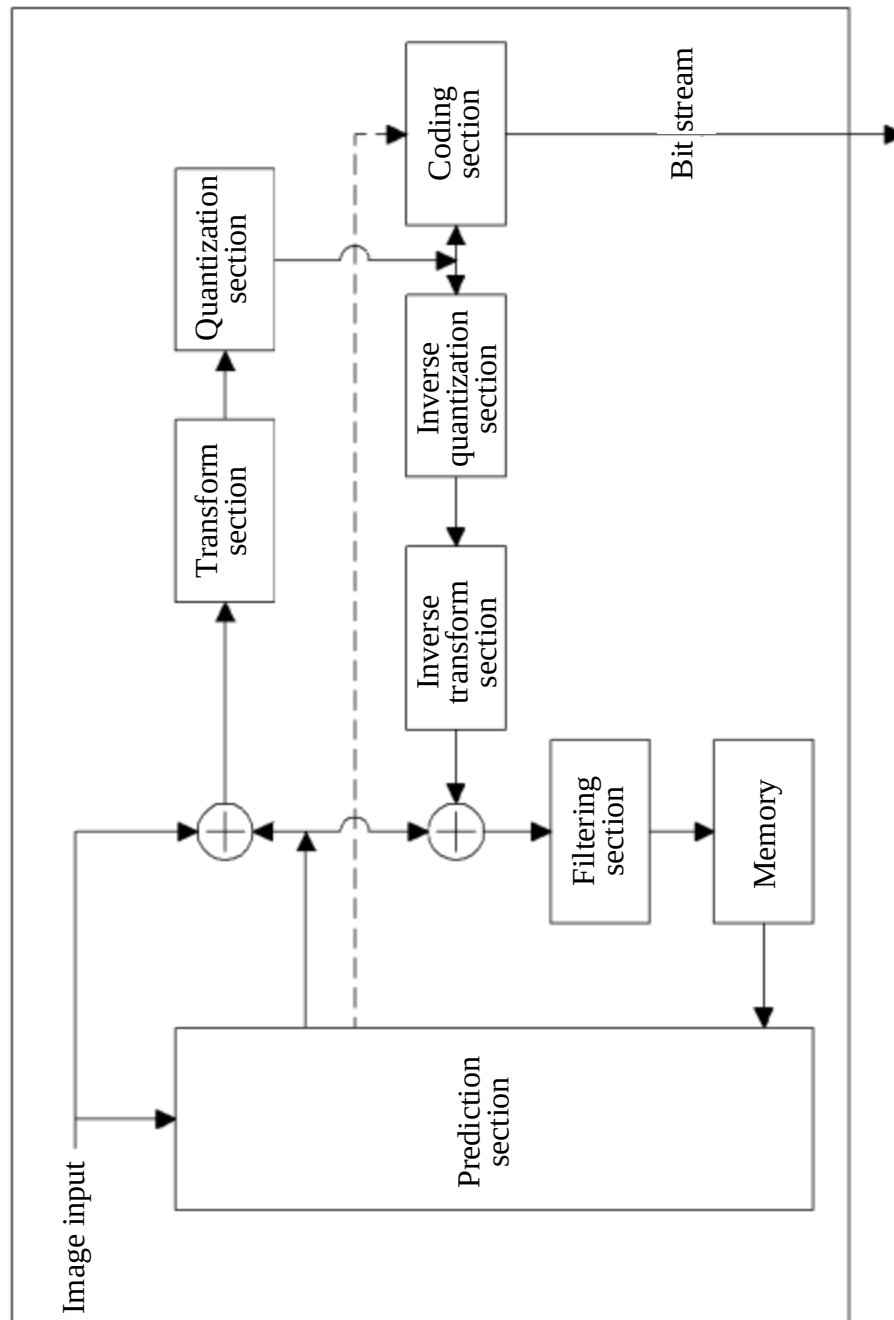
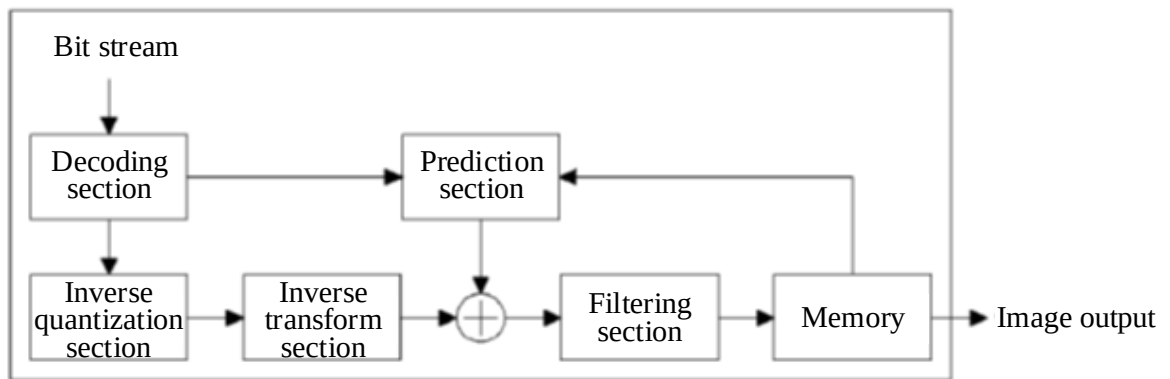
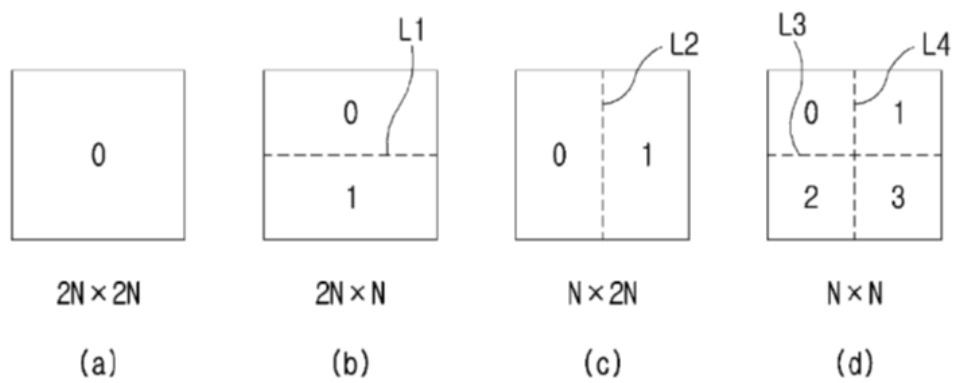


**FIG. 1**

**FIG. 2****FIG. 3**

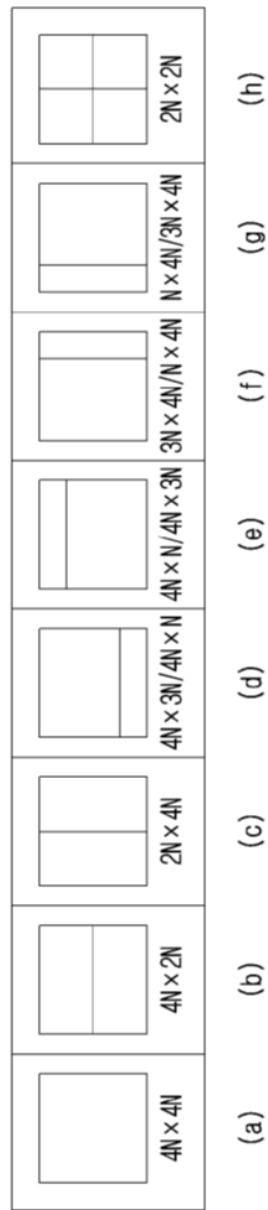


FIG. 4

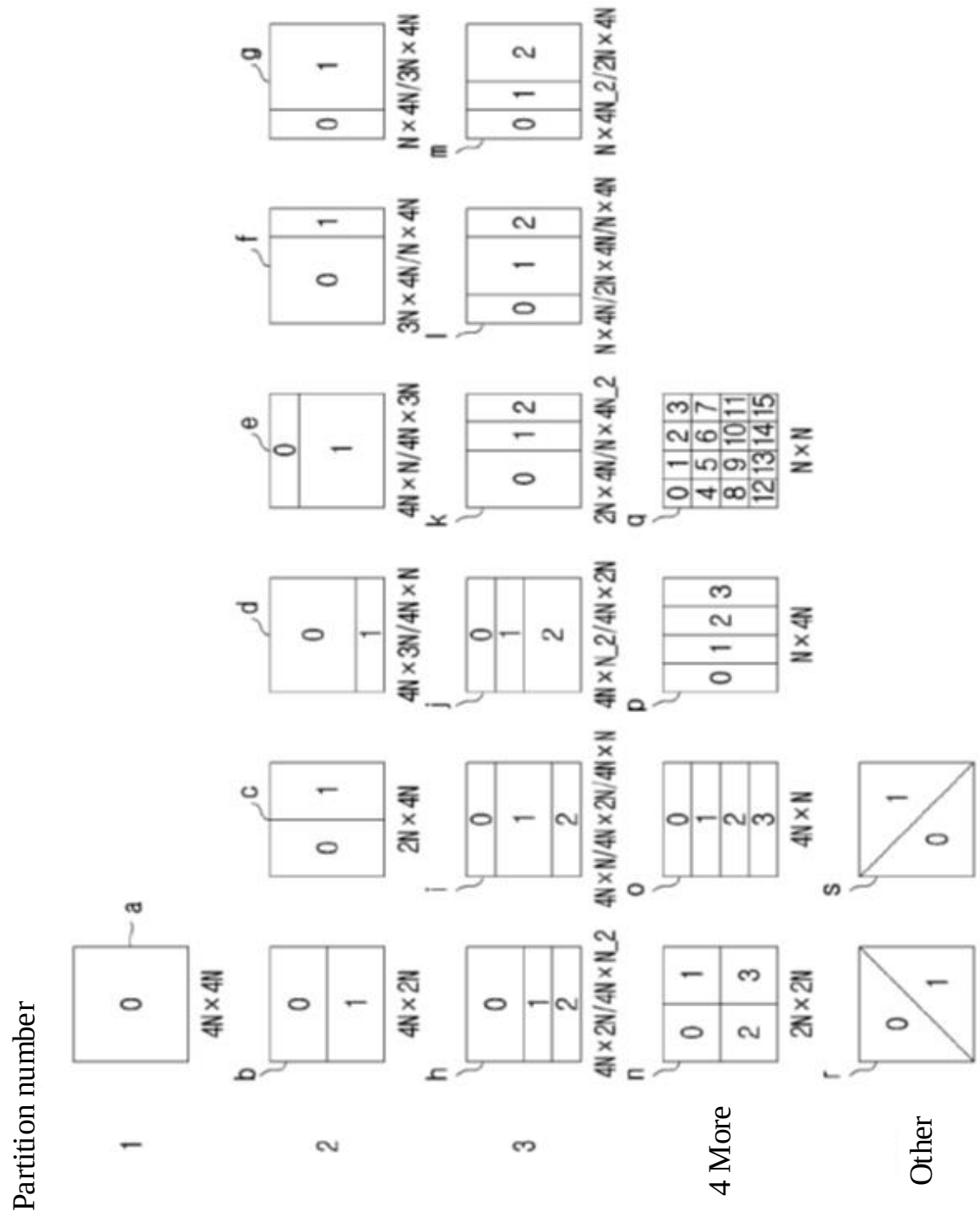
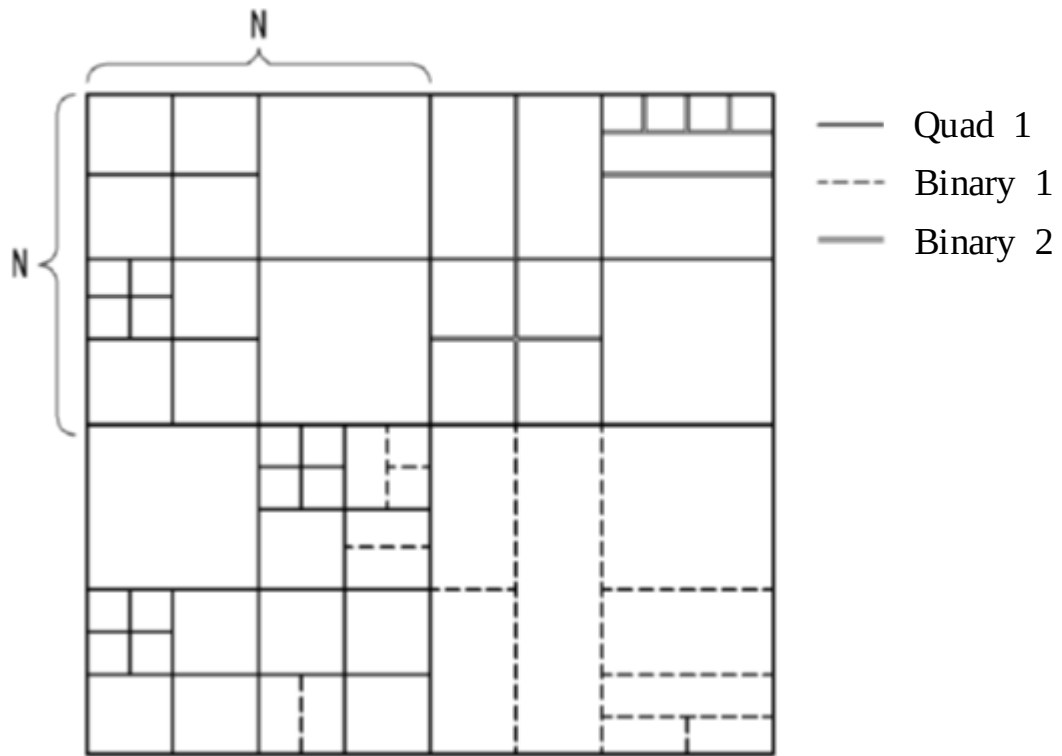


FIG. 5

**FIG. 6**

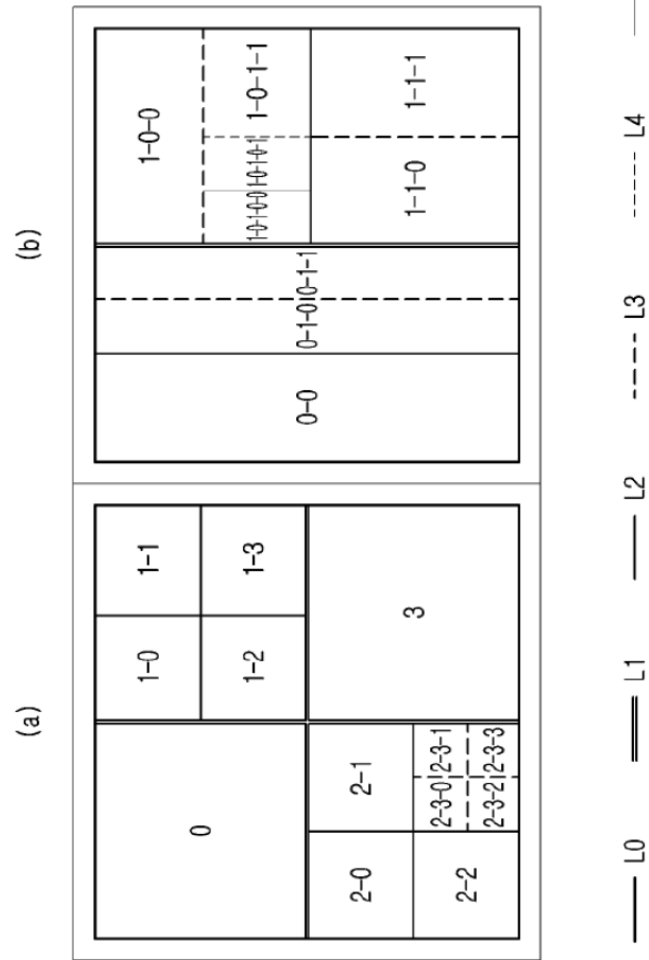
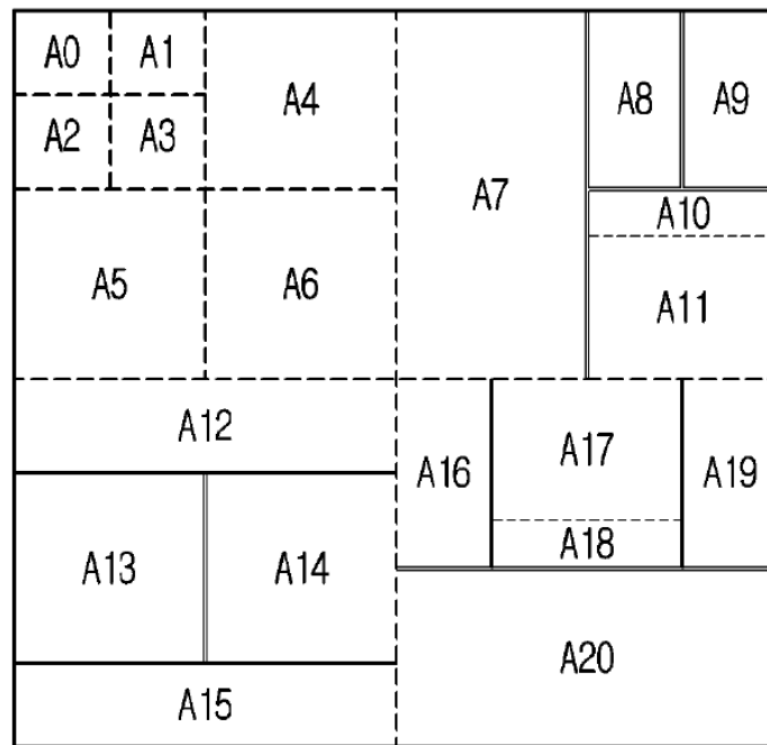


FIG. 7

**FIG. 8**

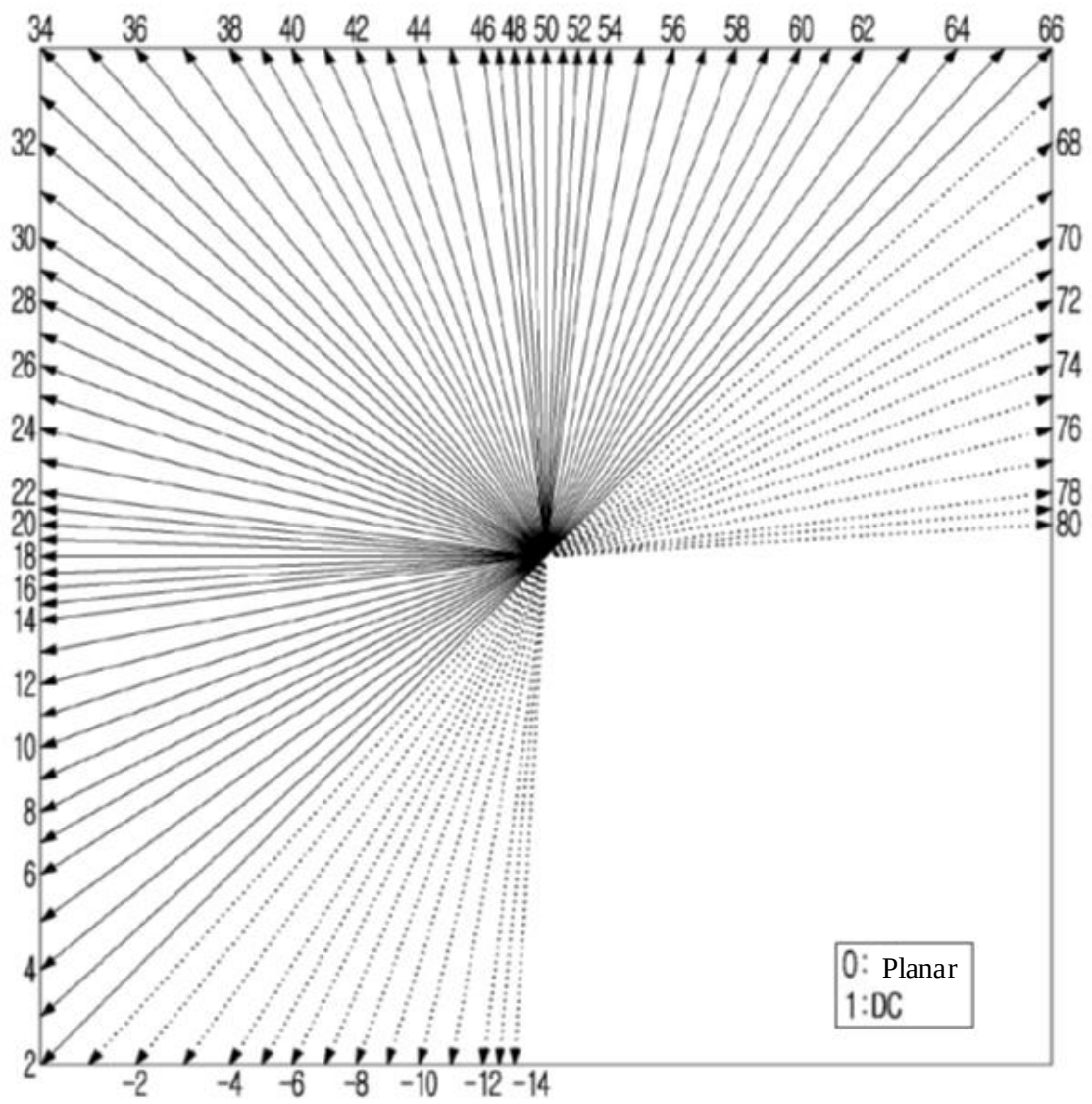


FIG. 9

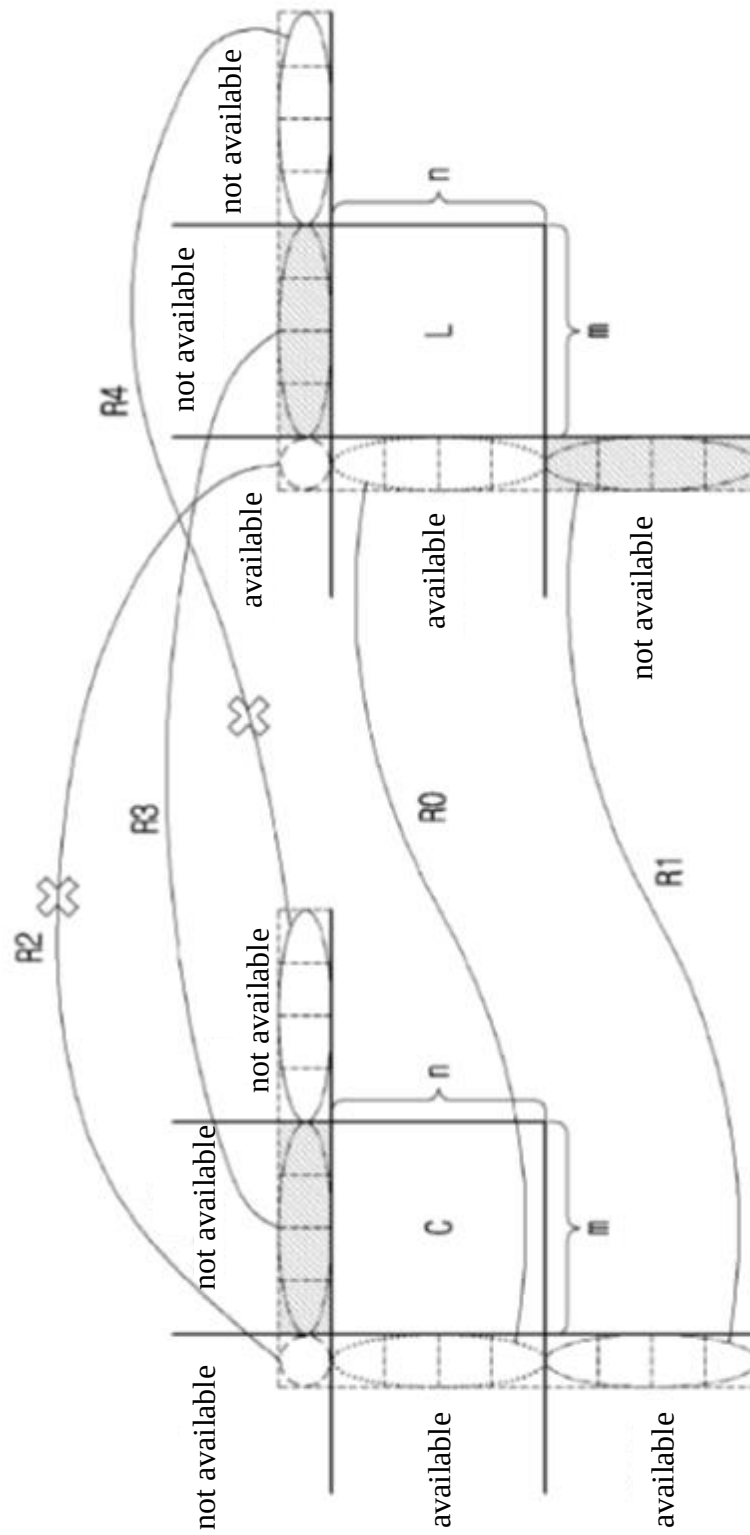


FIG. 10

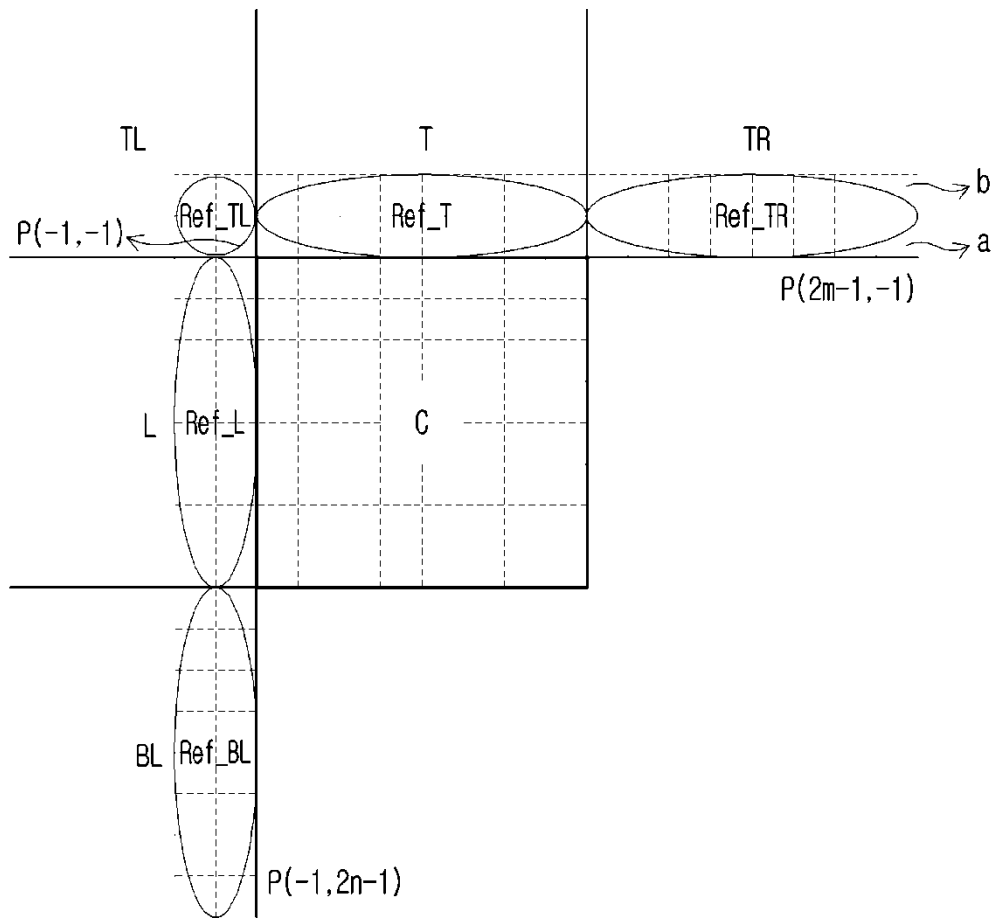


FIG. 11

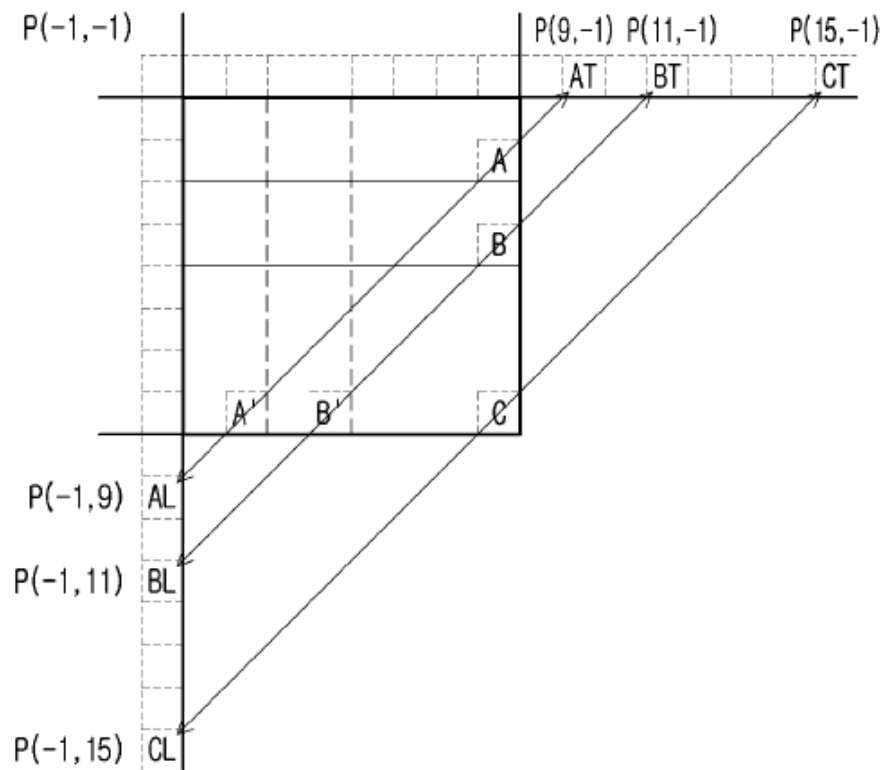


FIG. 12

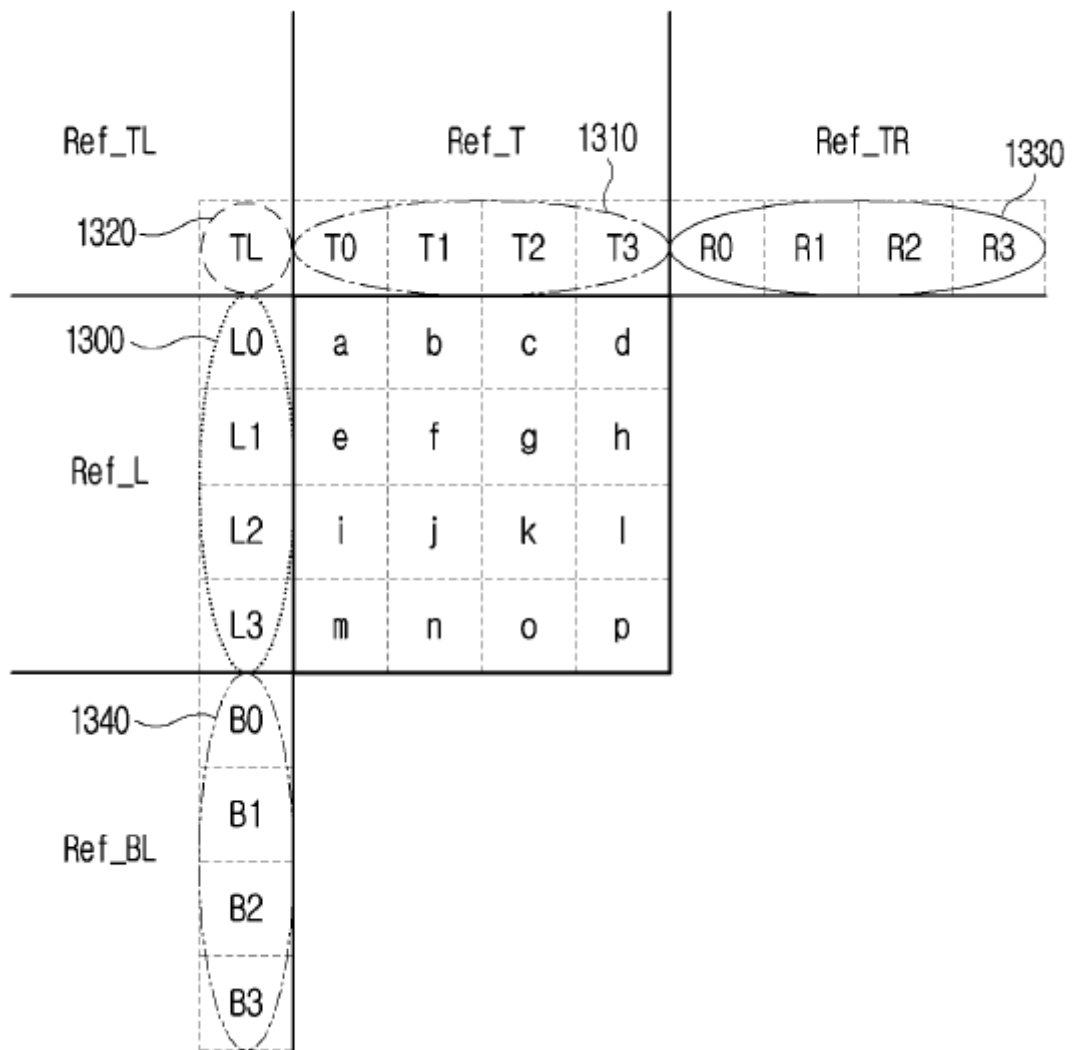
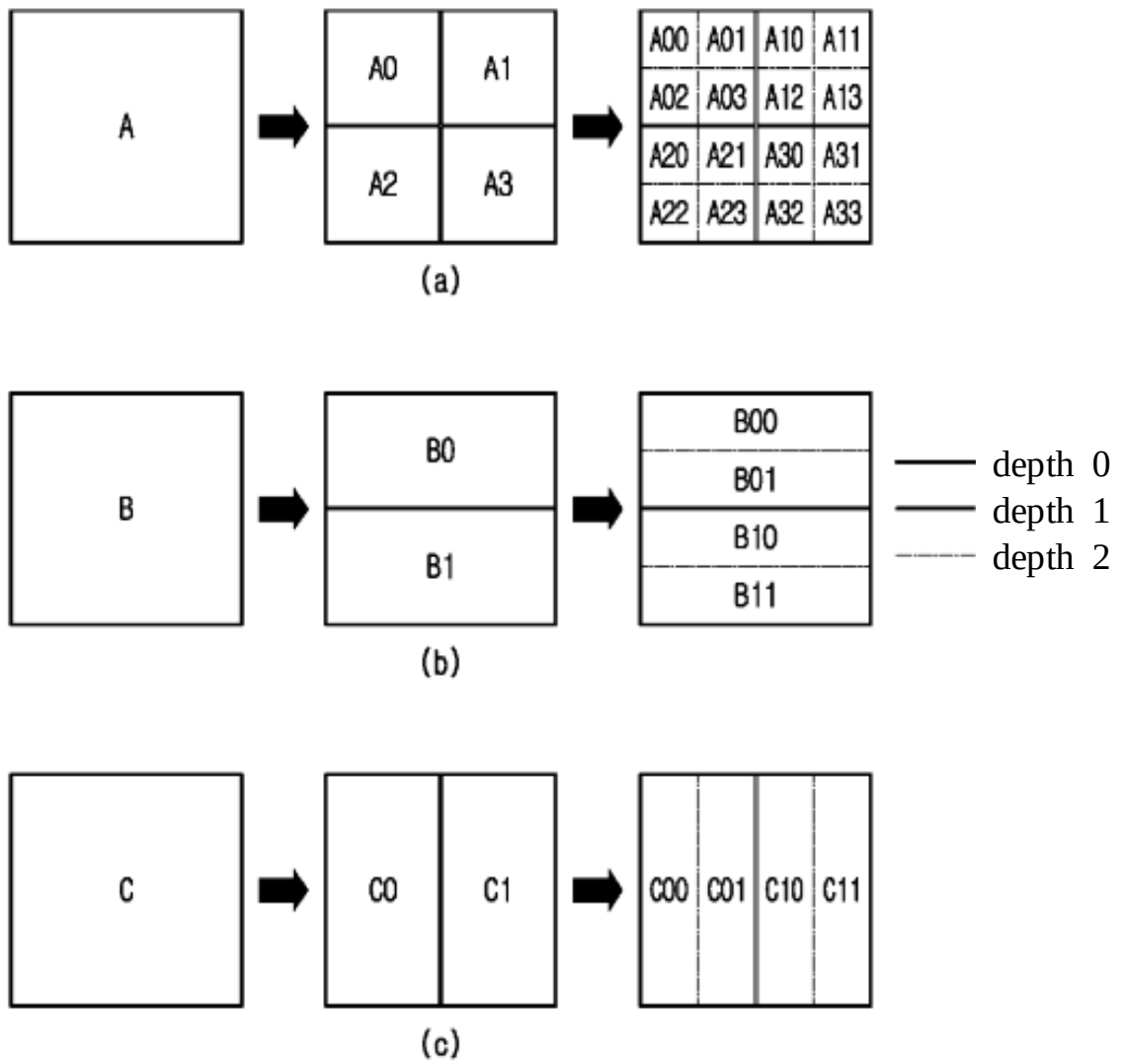


FIG. 13

**FIG. 14**

A0	A1A0	A1A1B0
		A1A1B1
	A1A2	A1A3
A2B0B0	A3B0	
A2B0B1		
A2B1	A3B1C0	A3B1C1

FIG. 15

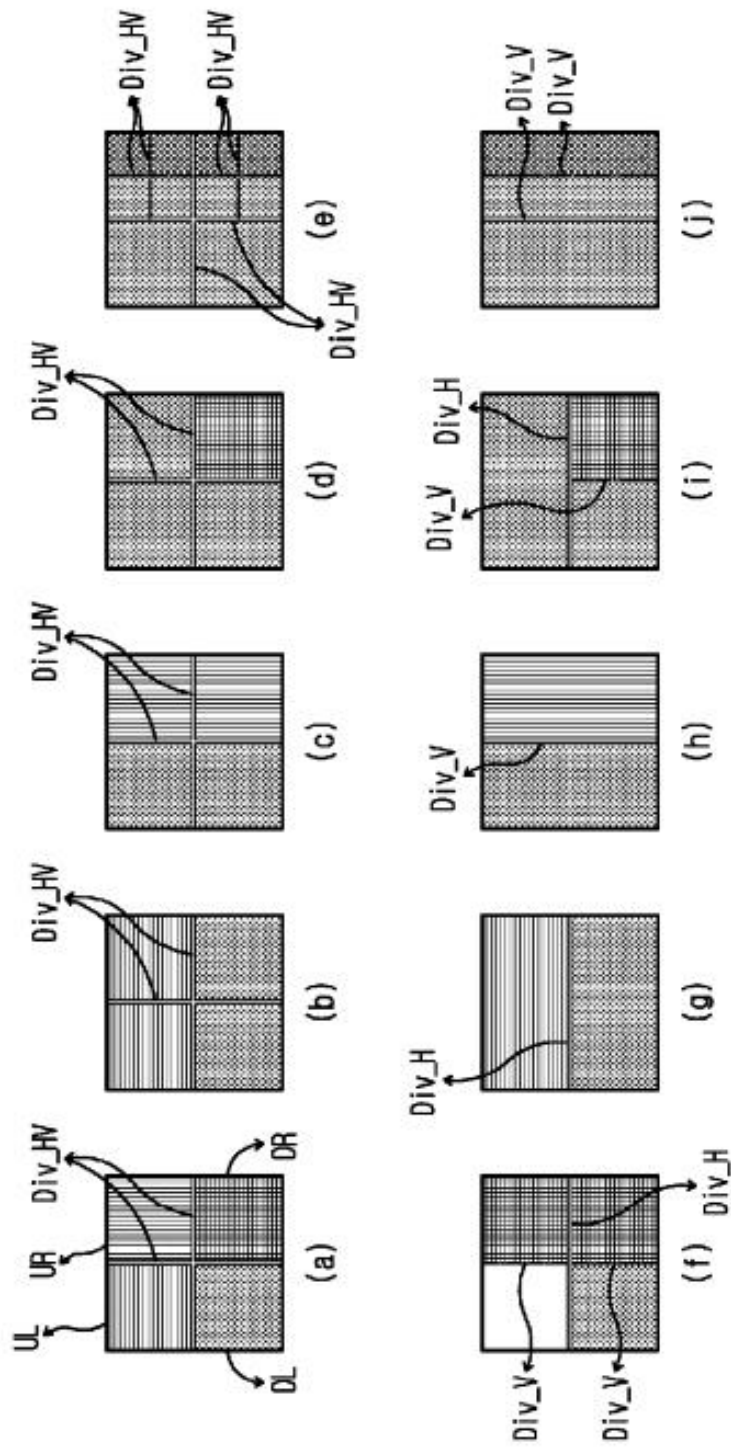


FIG. 16

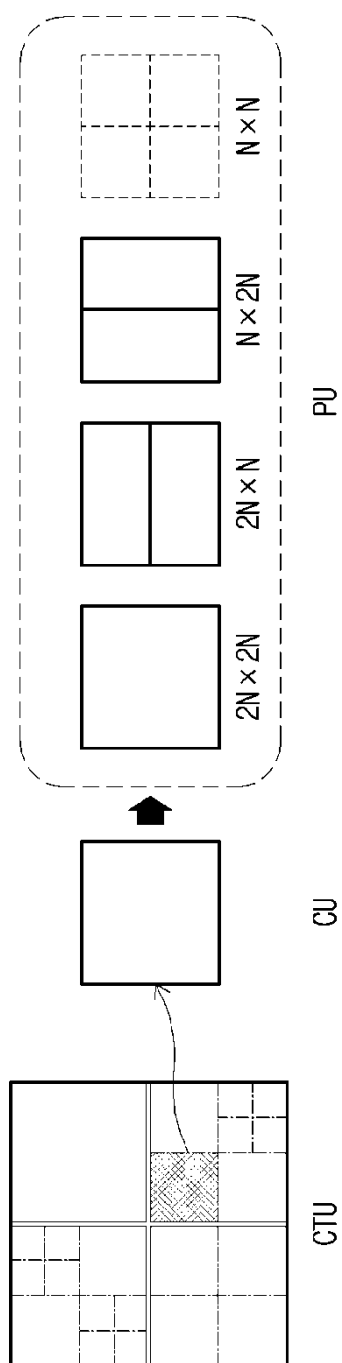


FIG. 17

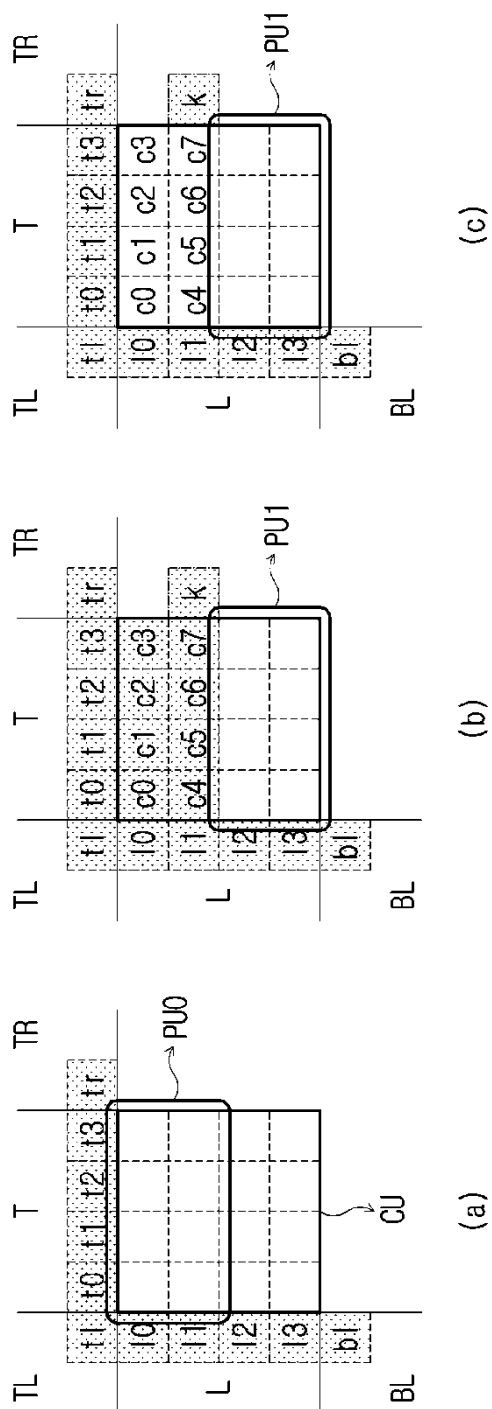


FIG. 18

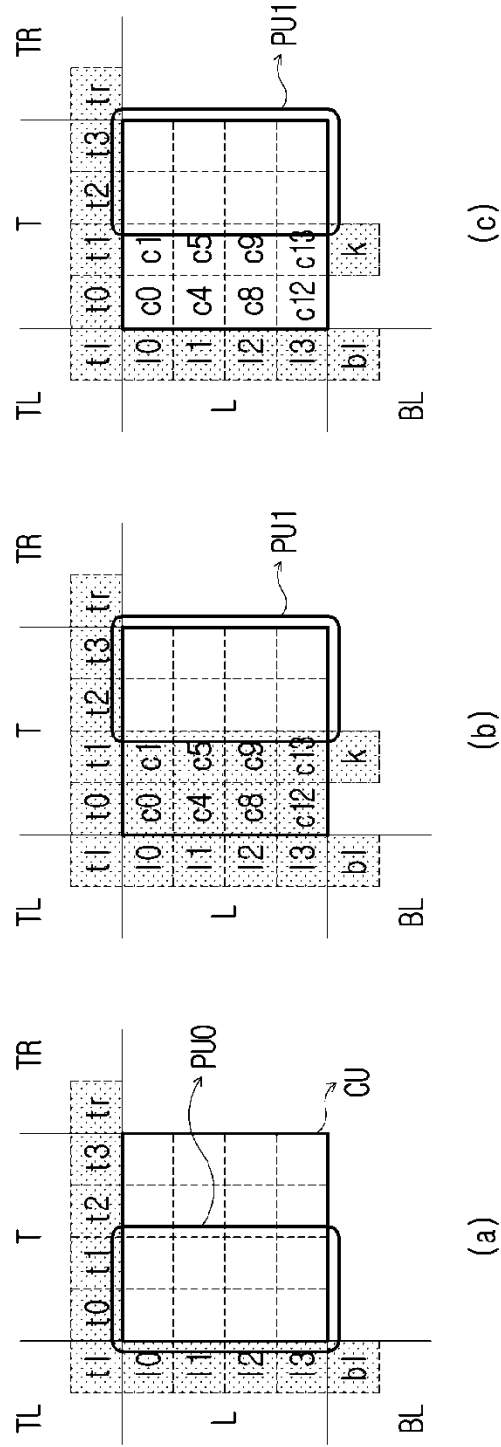


FIG. 19

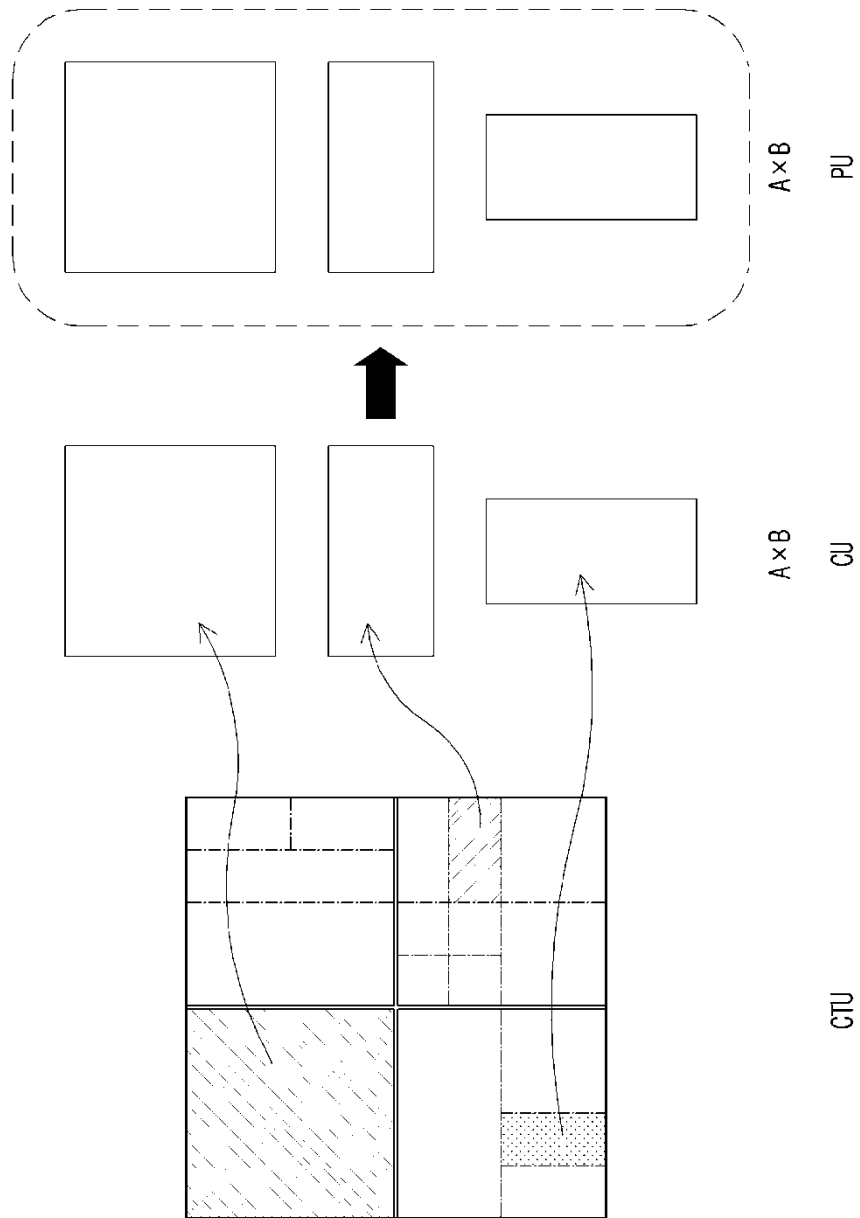


FIG. 20

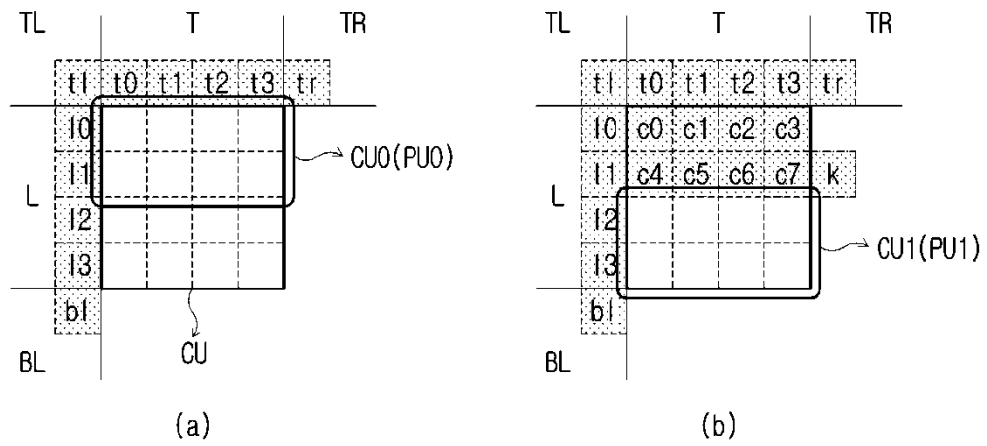


FIG. 21

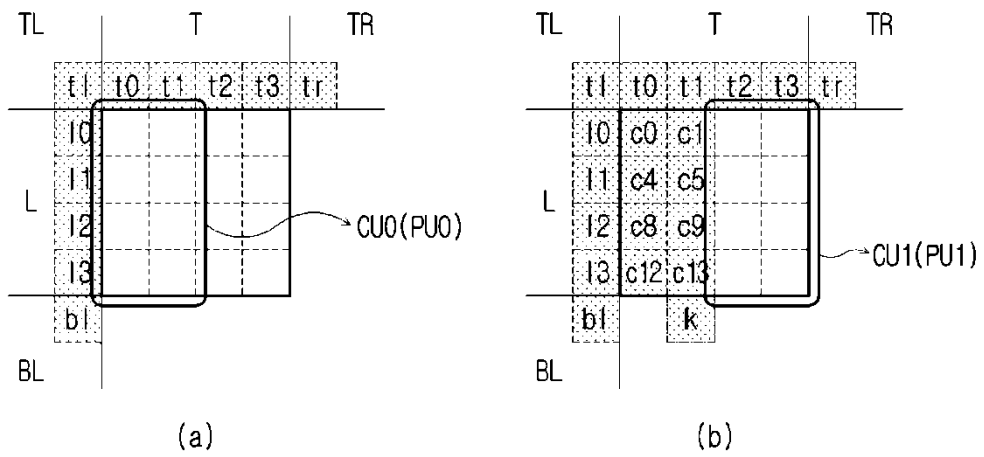


FIG. 22

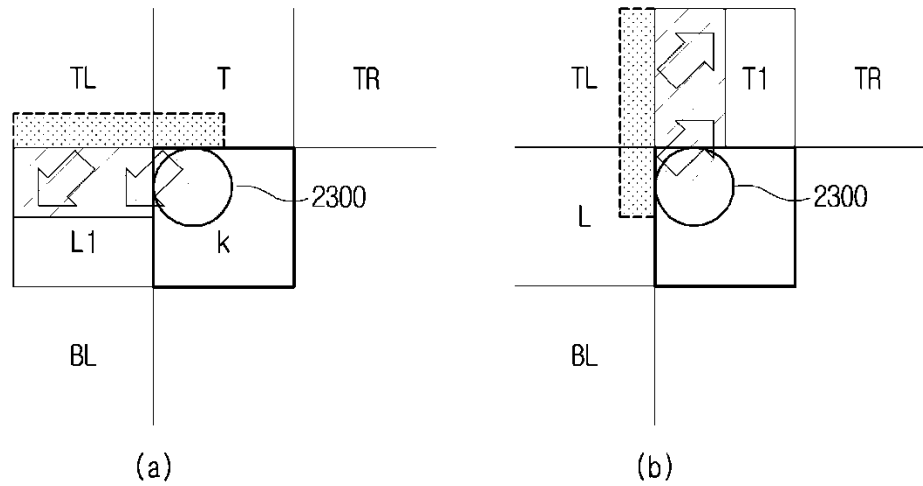


FIG. 23

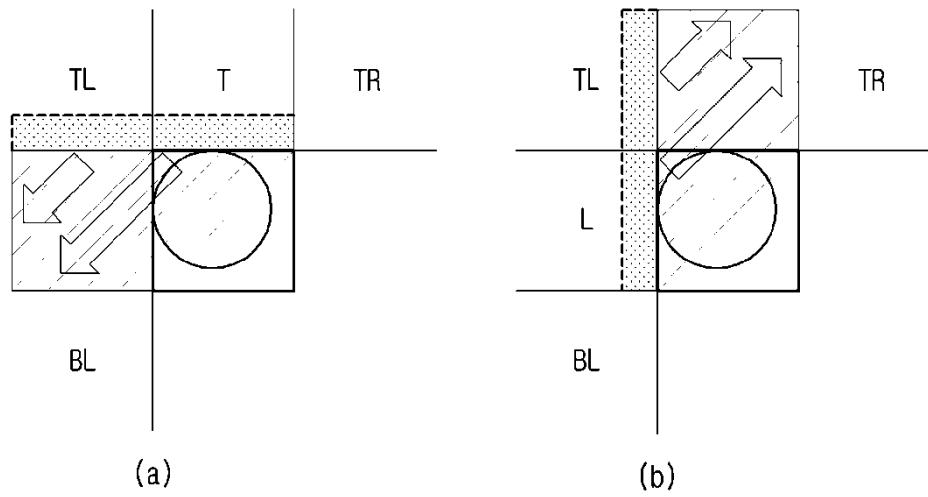


FIG. 24

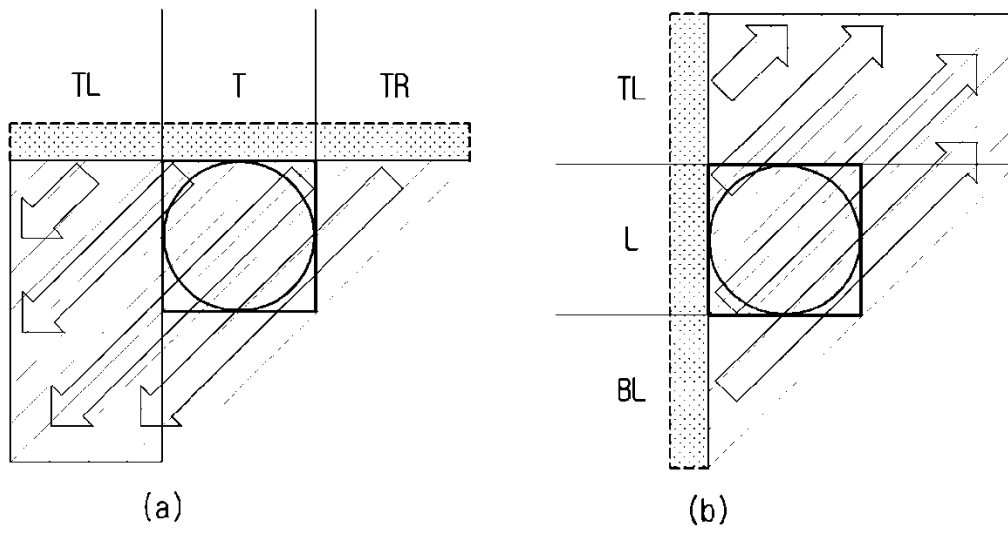


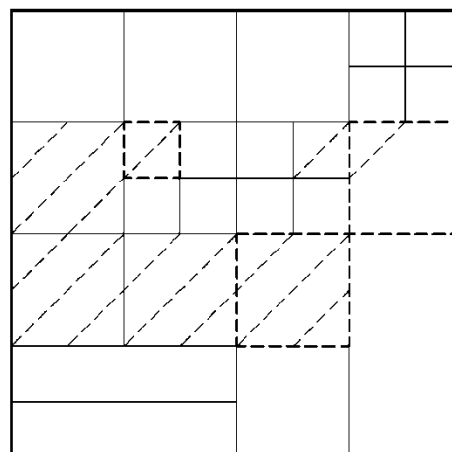
FIG. 25

QT

BT

0	1	7	8	9
			10	11
2	3	4	12	13
	5	6	14	15
16				
17		20		
18				
19		21		

(a)



(b)

FIG. 26

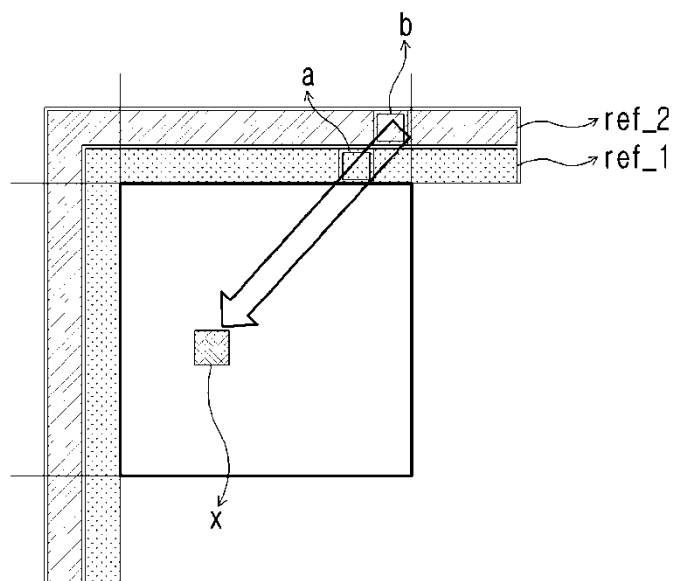


FIG. 27

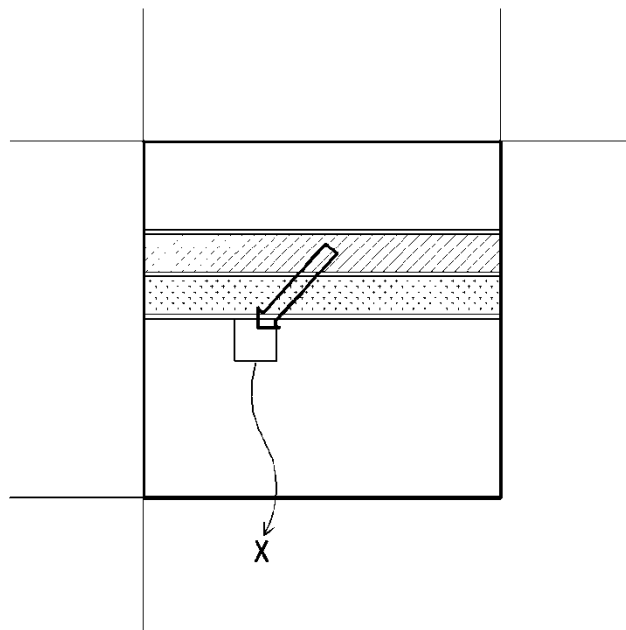


FIG. 28

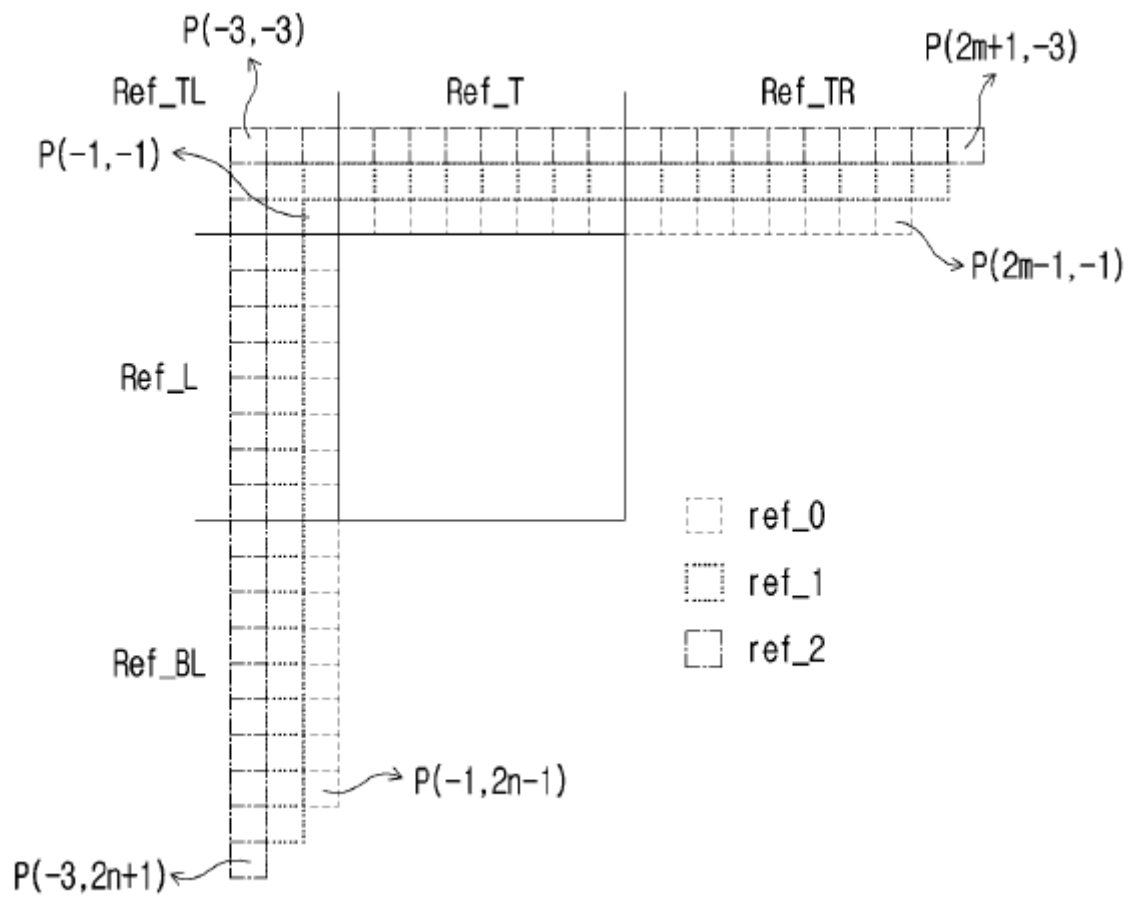


FIG. 29

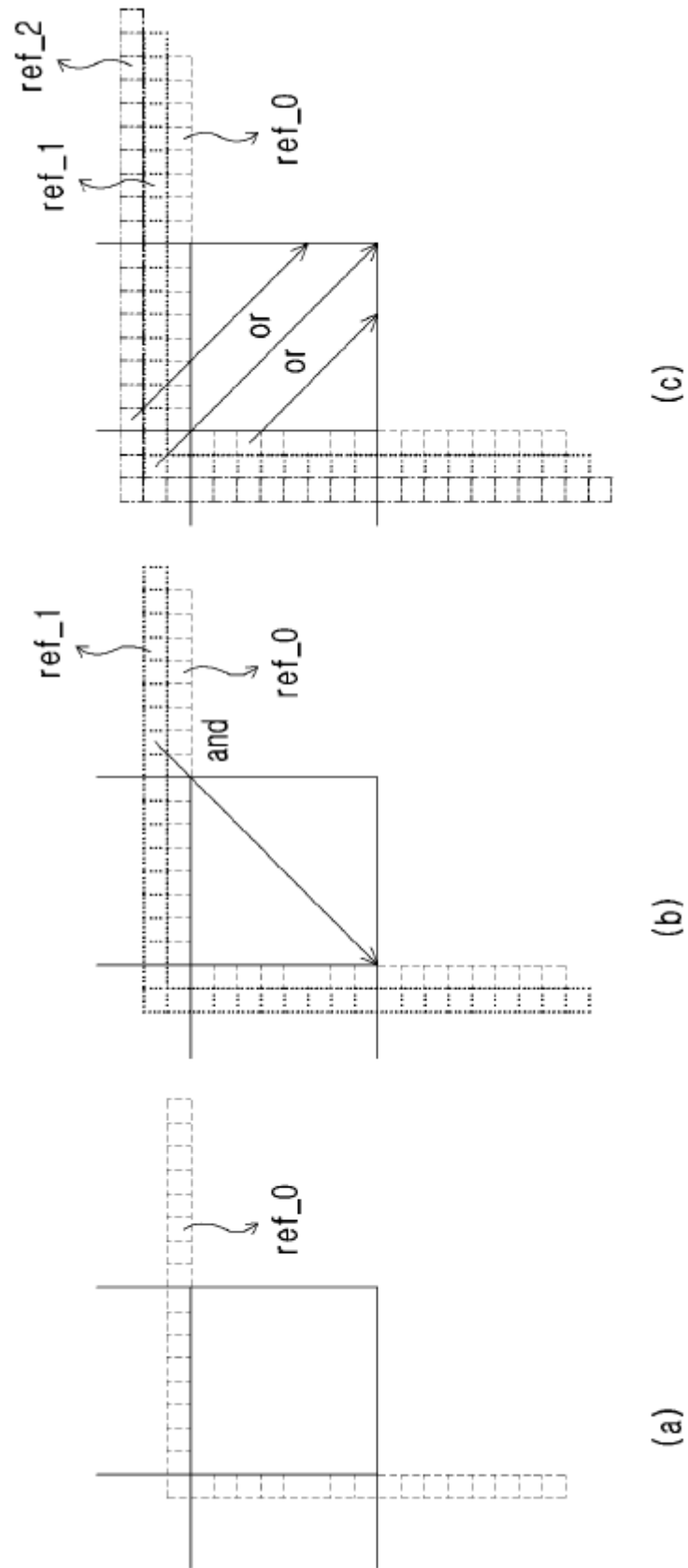
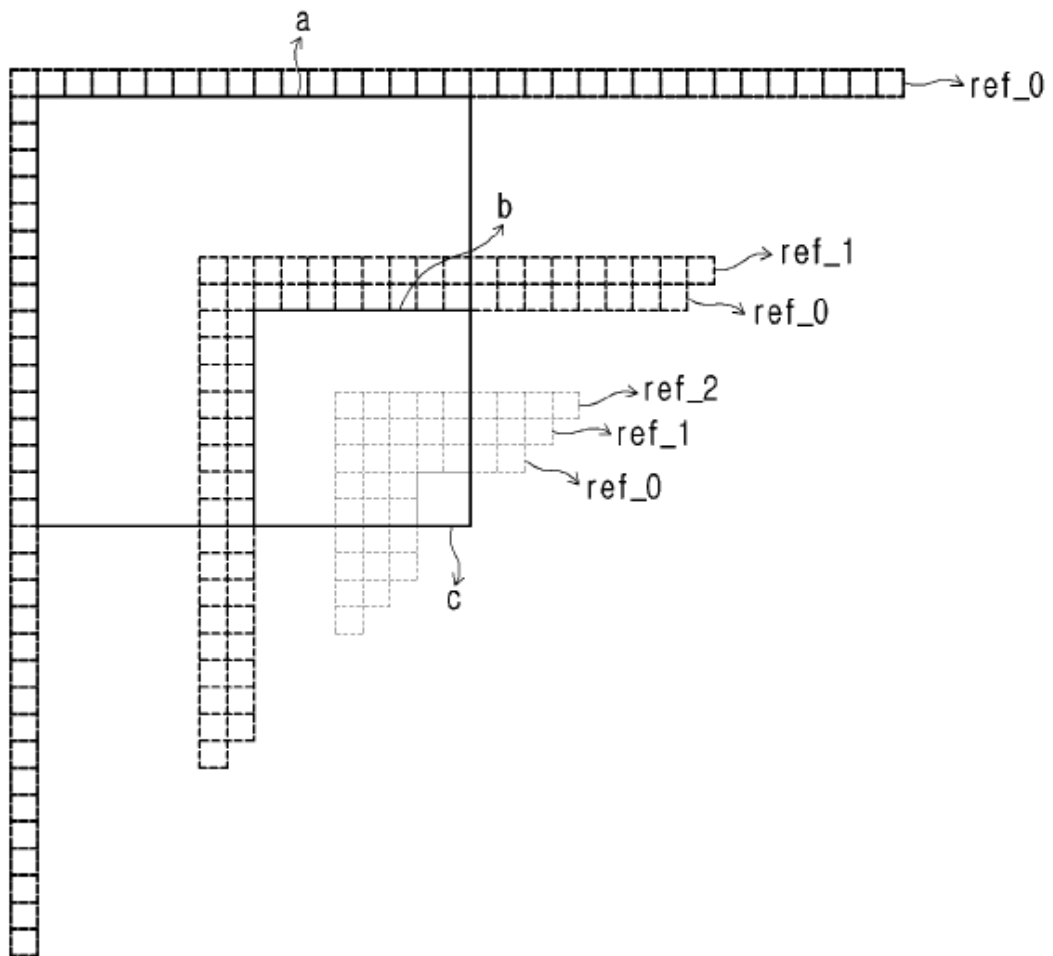
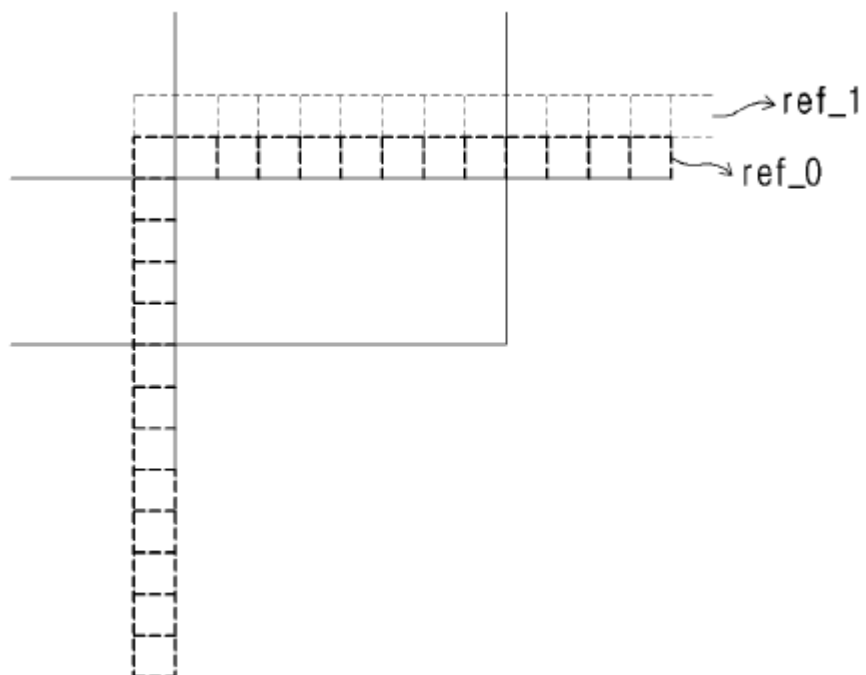
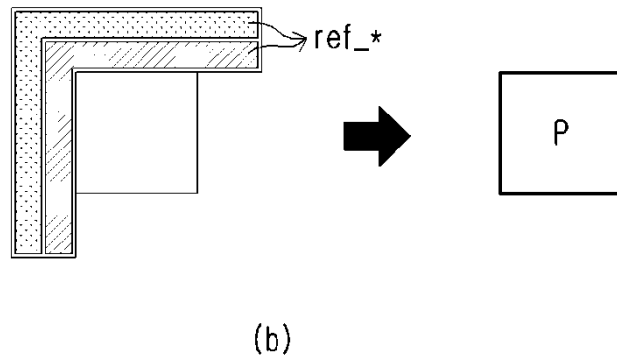
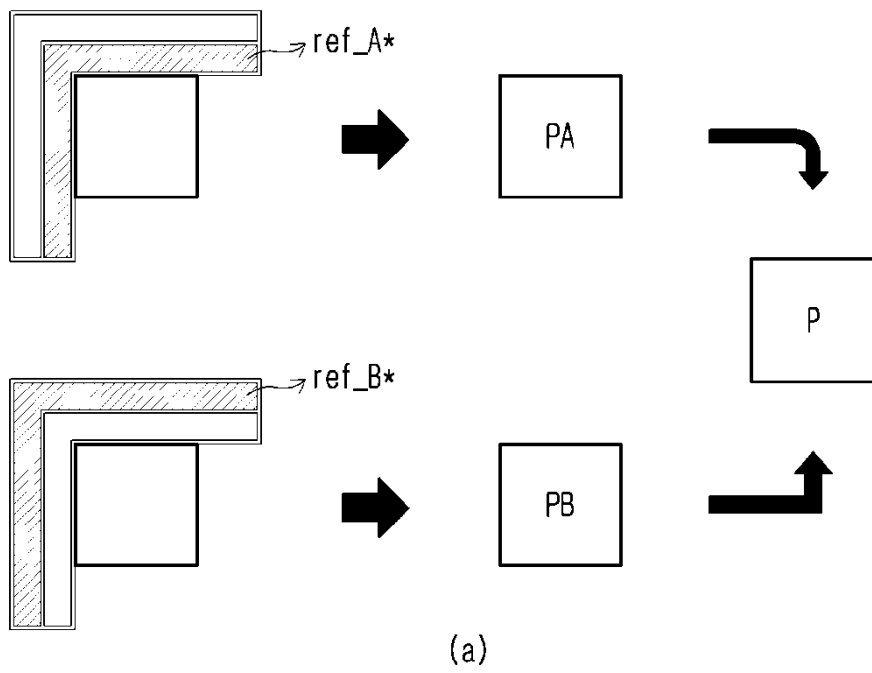
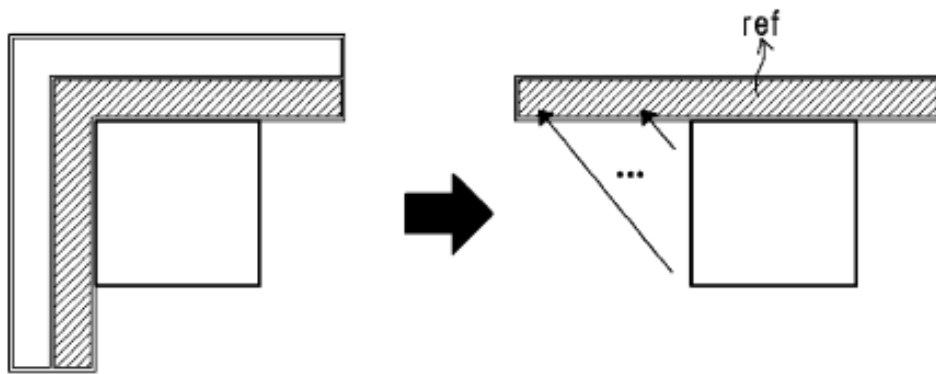


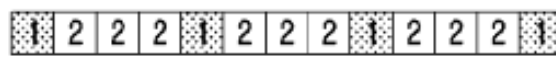
FIG. 30

**FIG. 31****FIG. 32**

**FIG. 33**



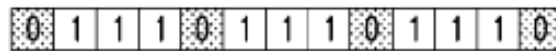
(a)



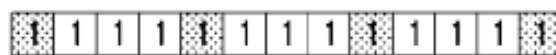
Integer
Decimal

Number of digital filtering

(b)

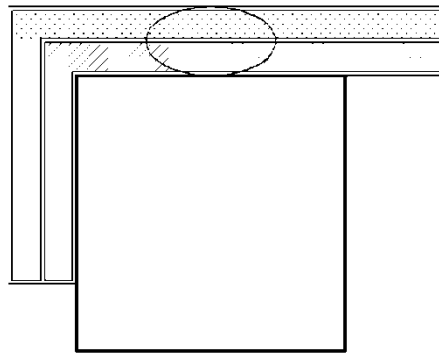


(c)

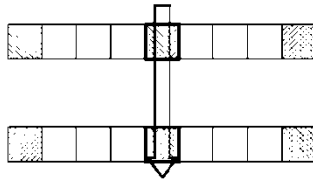


(d)

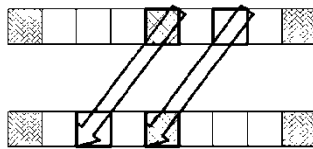
FIG. 34



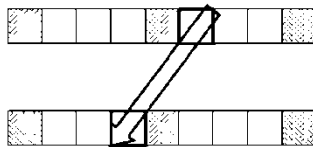
(a)



(b)



(c)



(d)

FIG. 35