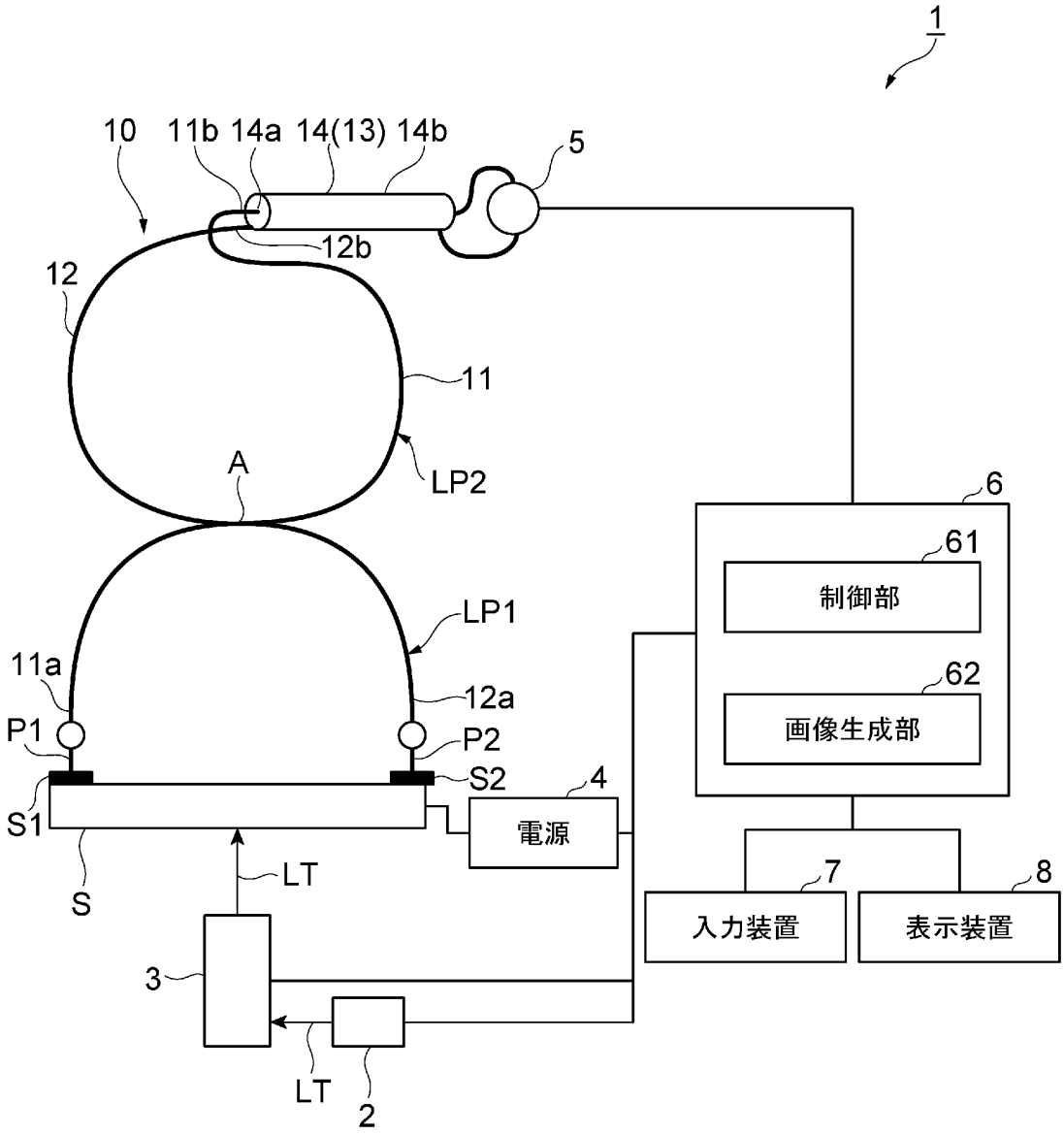
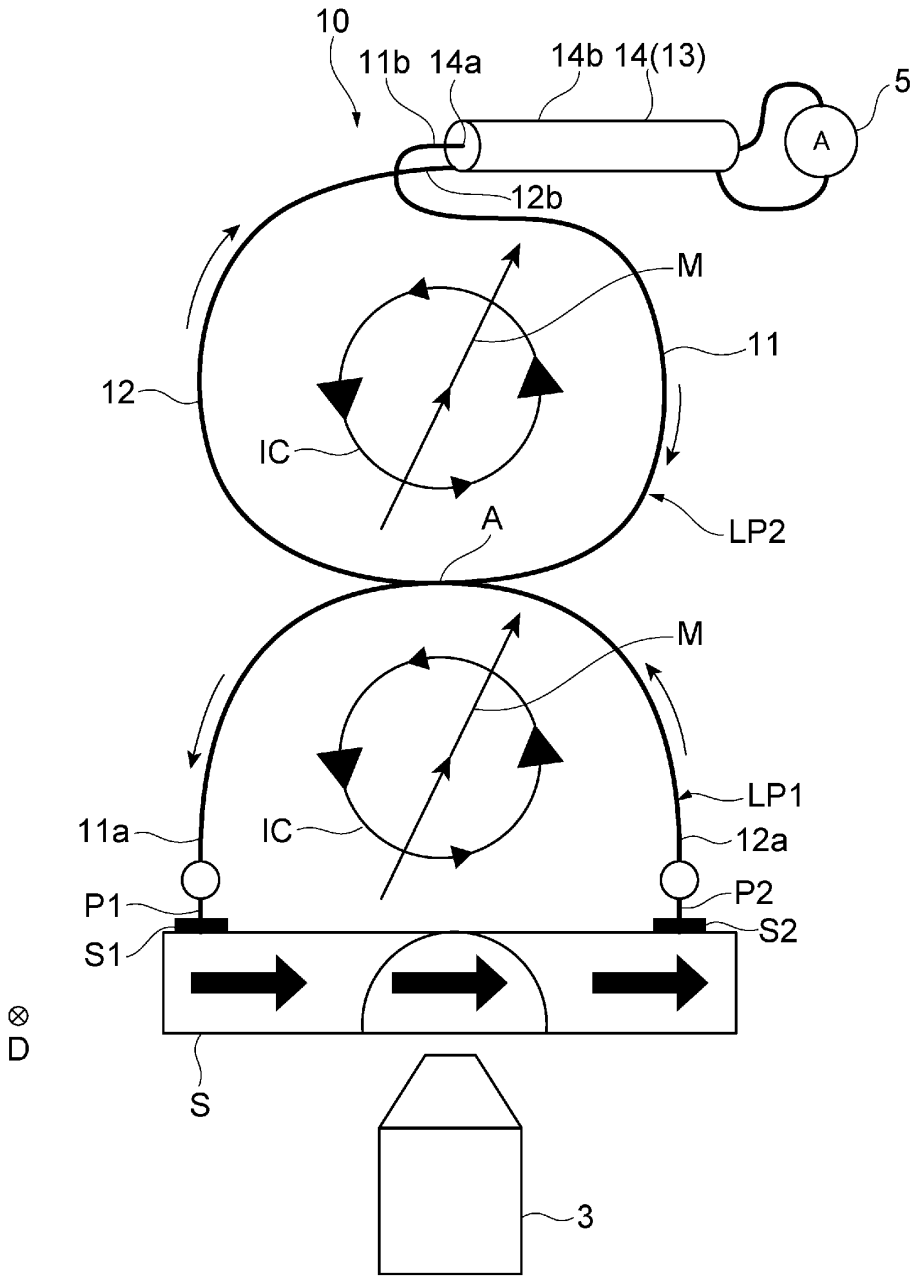
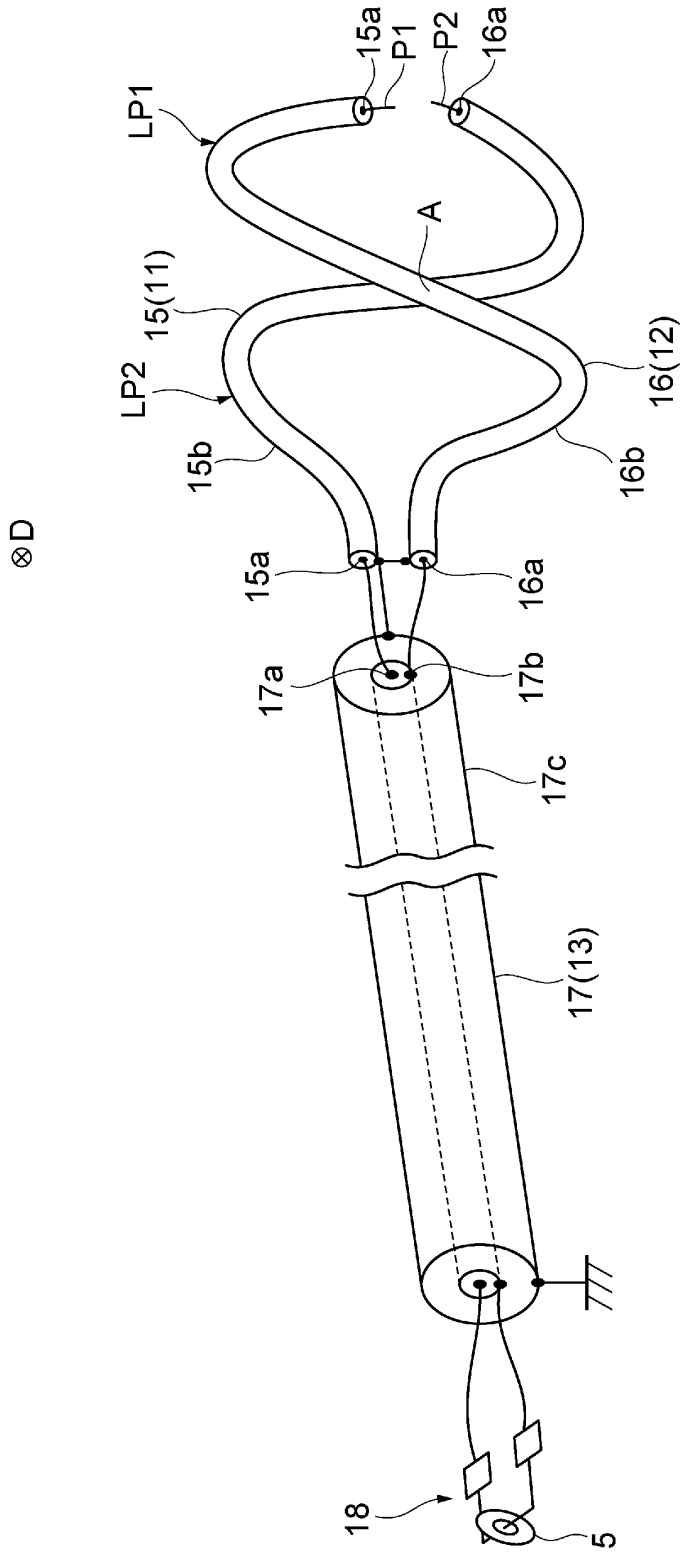
[図1]


1
10
11b
14a
14(13)
14b
5
12b
12
11
LP2
A
6
61
制御部
62
画像生成部
LP1
11a
12a
P1
P2
S2
4
S1
電源
S
LT
7
8
入力装置
表示装置
3
LT
2

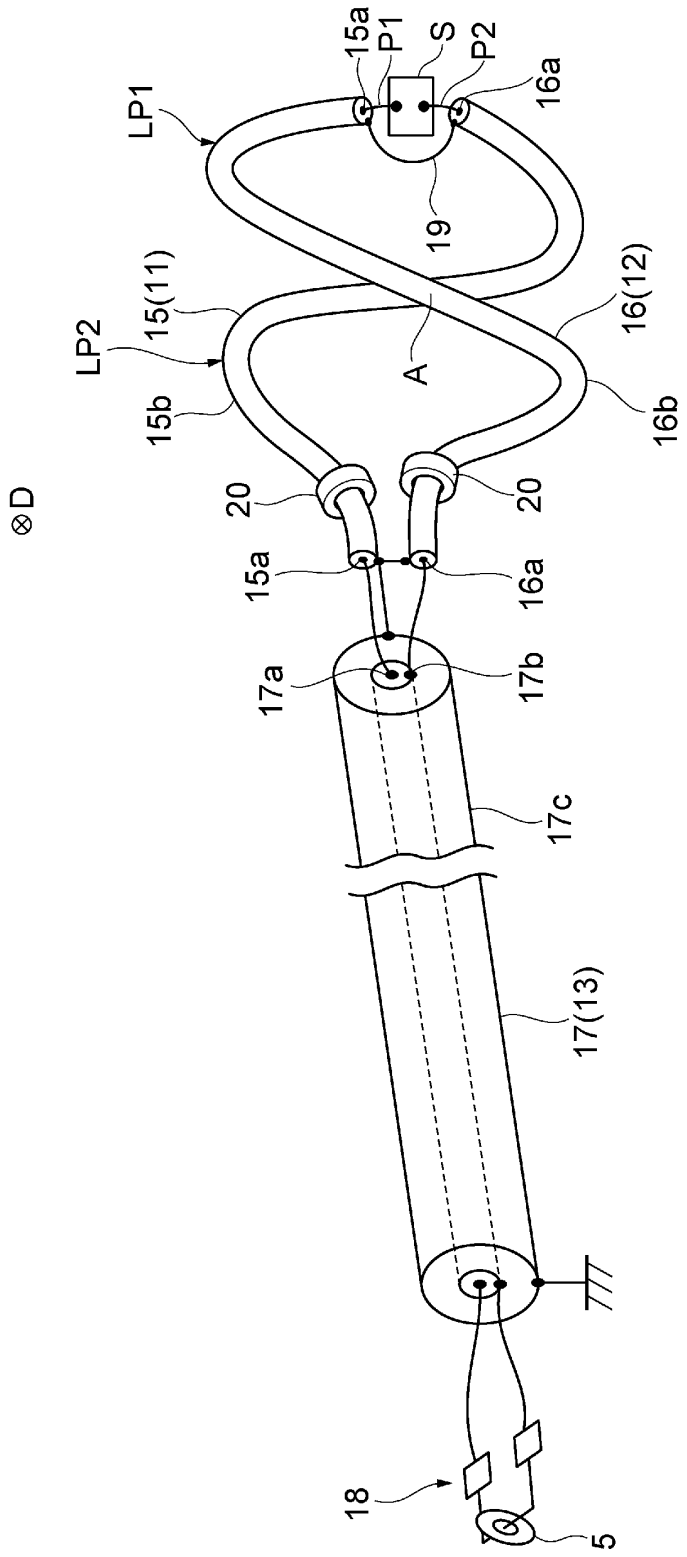
[図2]


10
11b
14a
14b
14(13)
5
A
12b
M
11
12
IC
A
LP2
M
LP1
11a
IC
12a
P1
P2
S1
S2
⊗
D
S
3

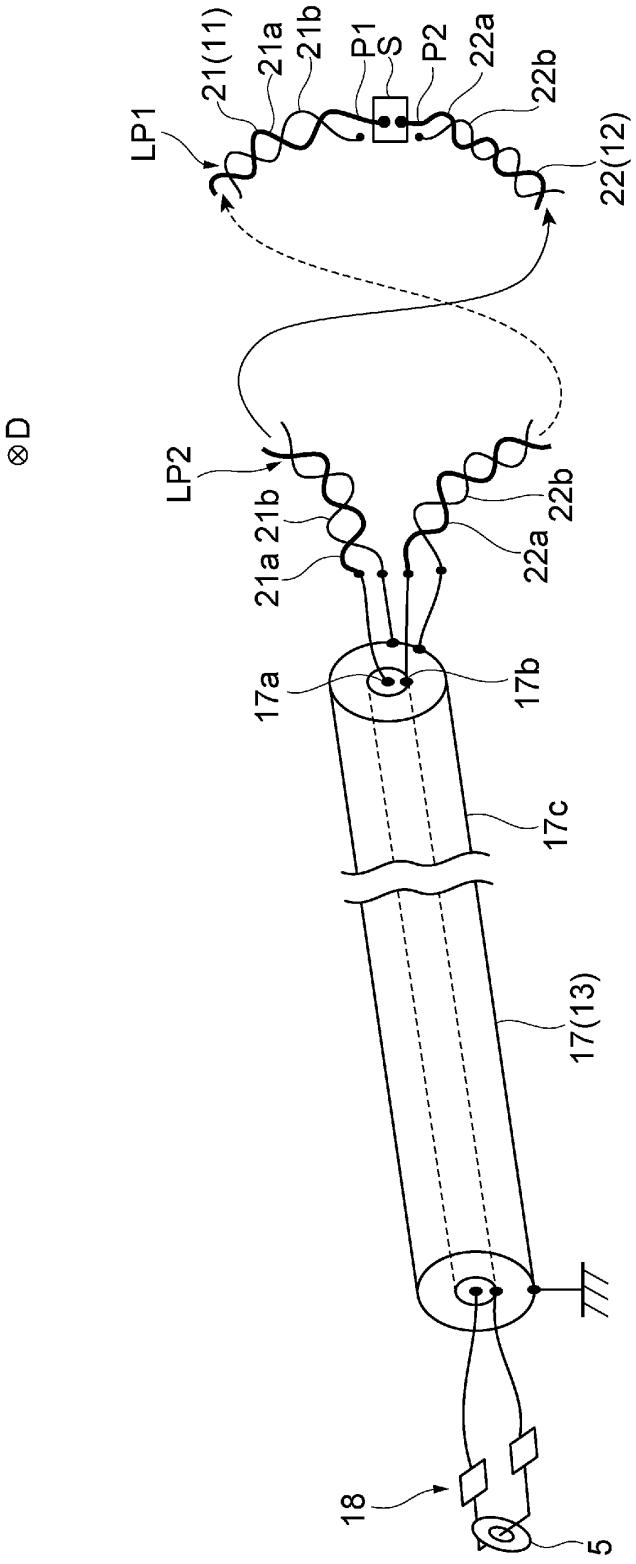
[図3]


15a
P1
P2
16a
LP1
A
15(11)
LP2
16(12)
15b
16b
⊗D
15a
16a
17a
17b
17c
17(13)
18
5

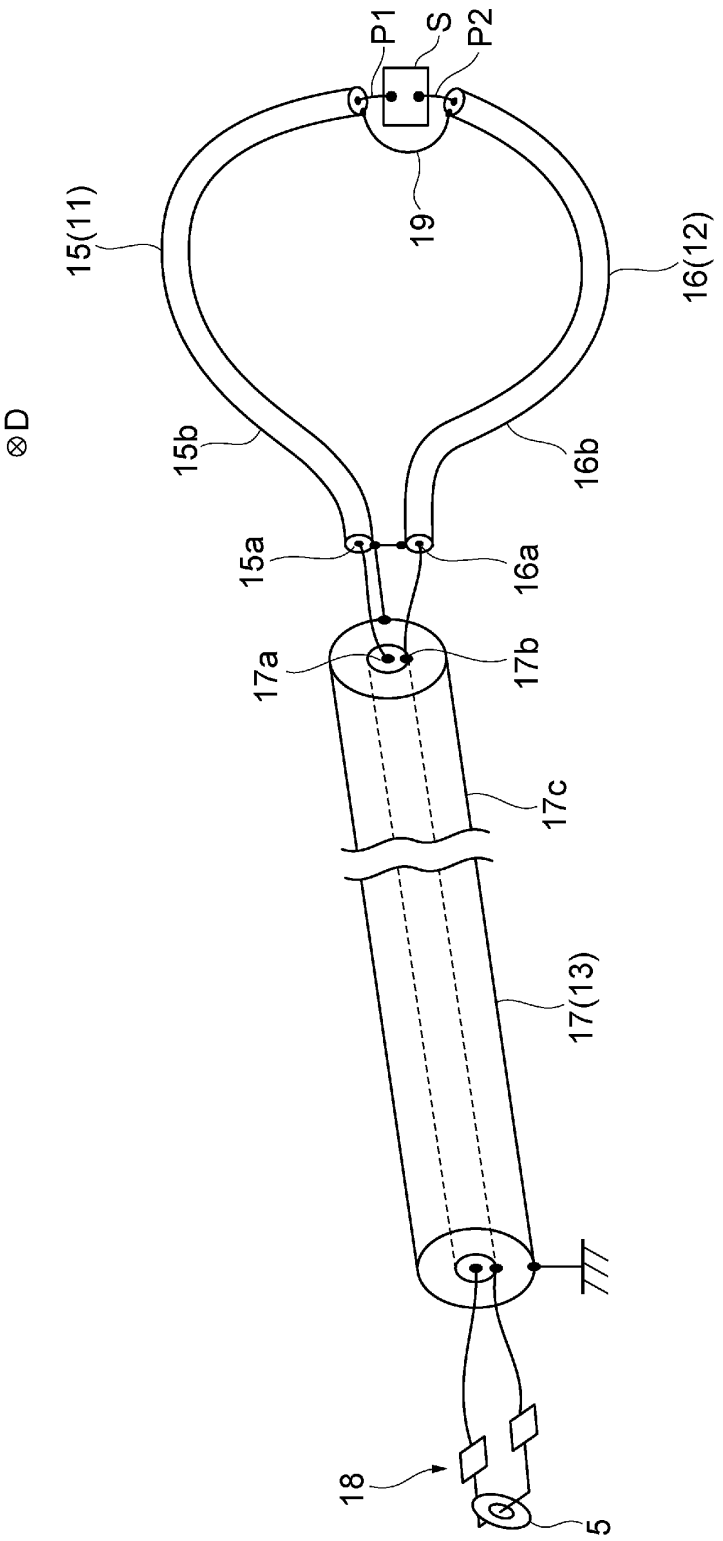
[図4]


15a
P1
S
P2
16a
LP1
19
15(11)
16(12)
LP2
A
15b
16b
20
20
⊗D
15a
16a
17a
17b
17c
17(13)
18
5

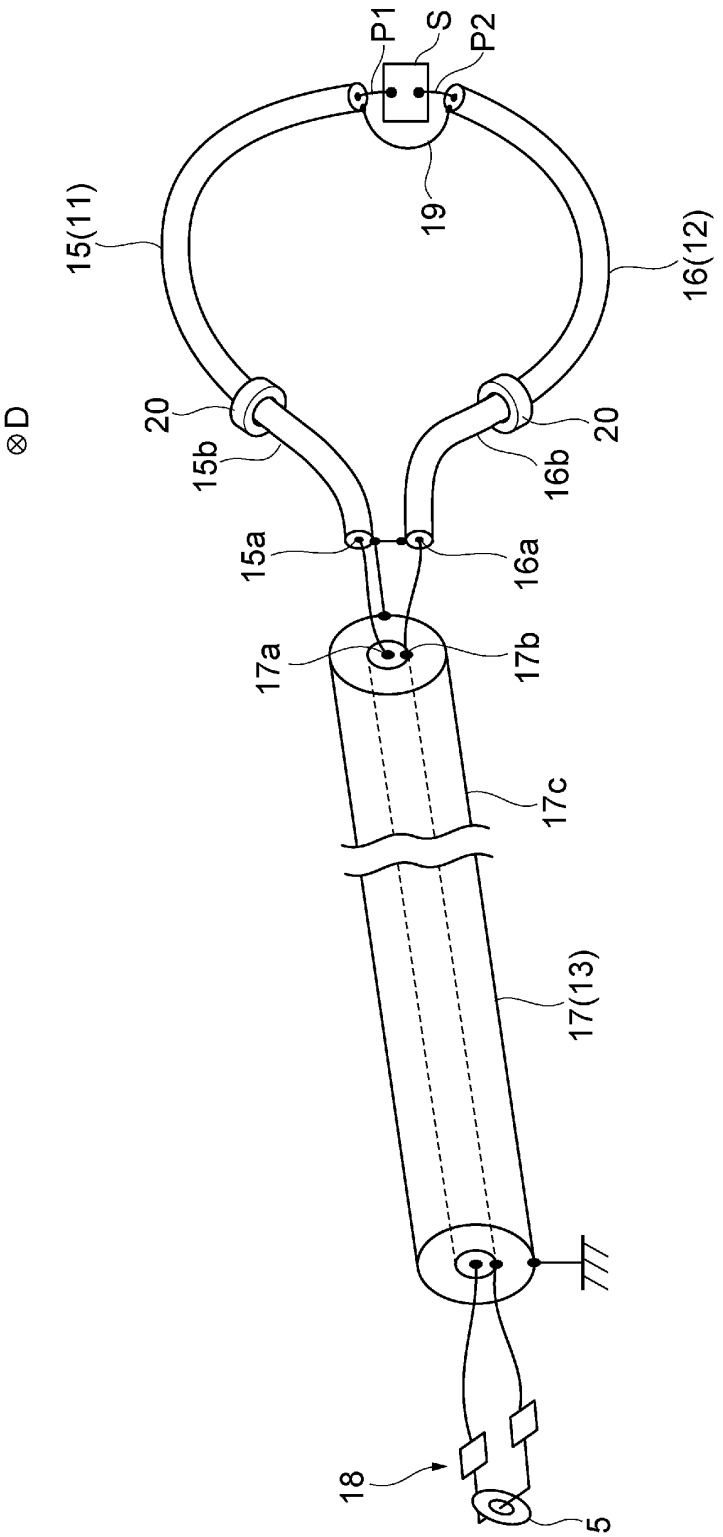
[図5]


⊗D
LP1
21(11)
21a
21b
P1
S
P2
22a
22b
22(12)
LP2
21a 21b
22a 22b
17a
17b
17c
17(13)
18
5

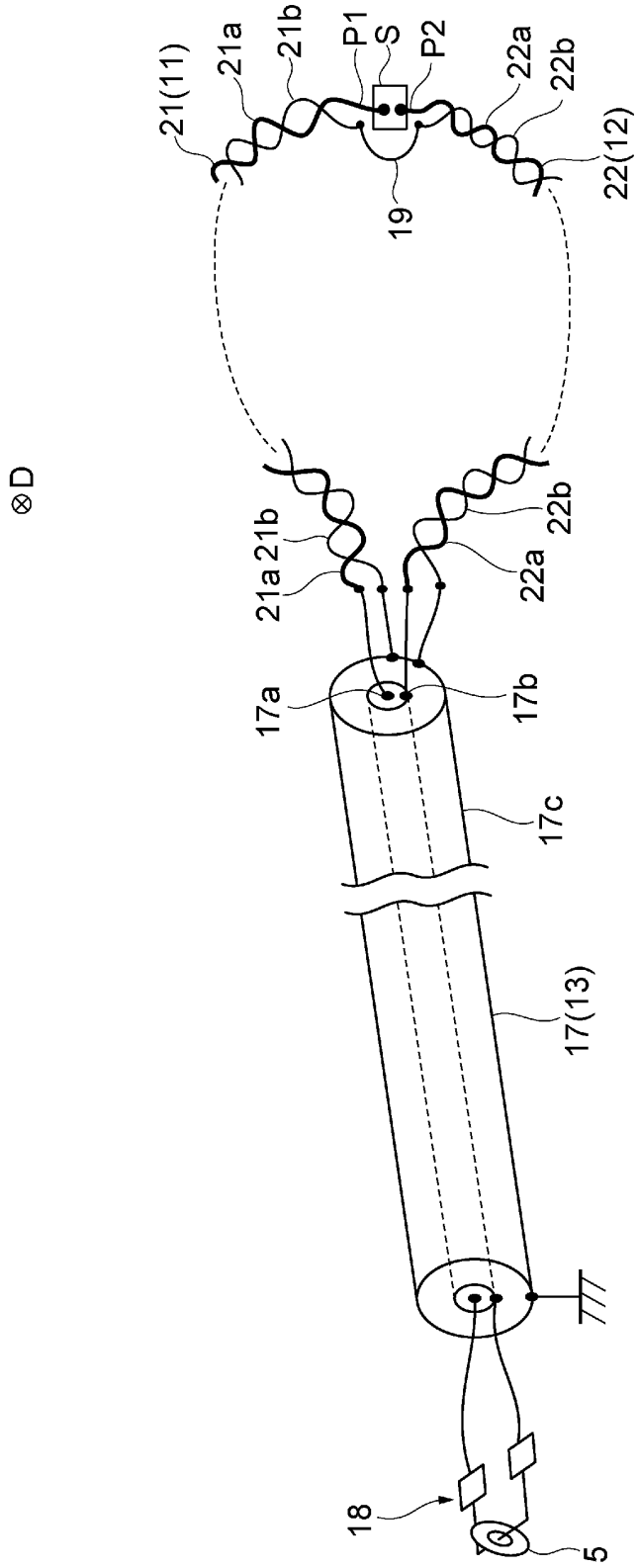
[図6]



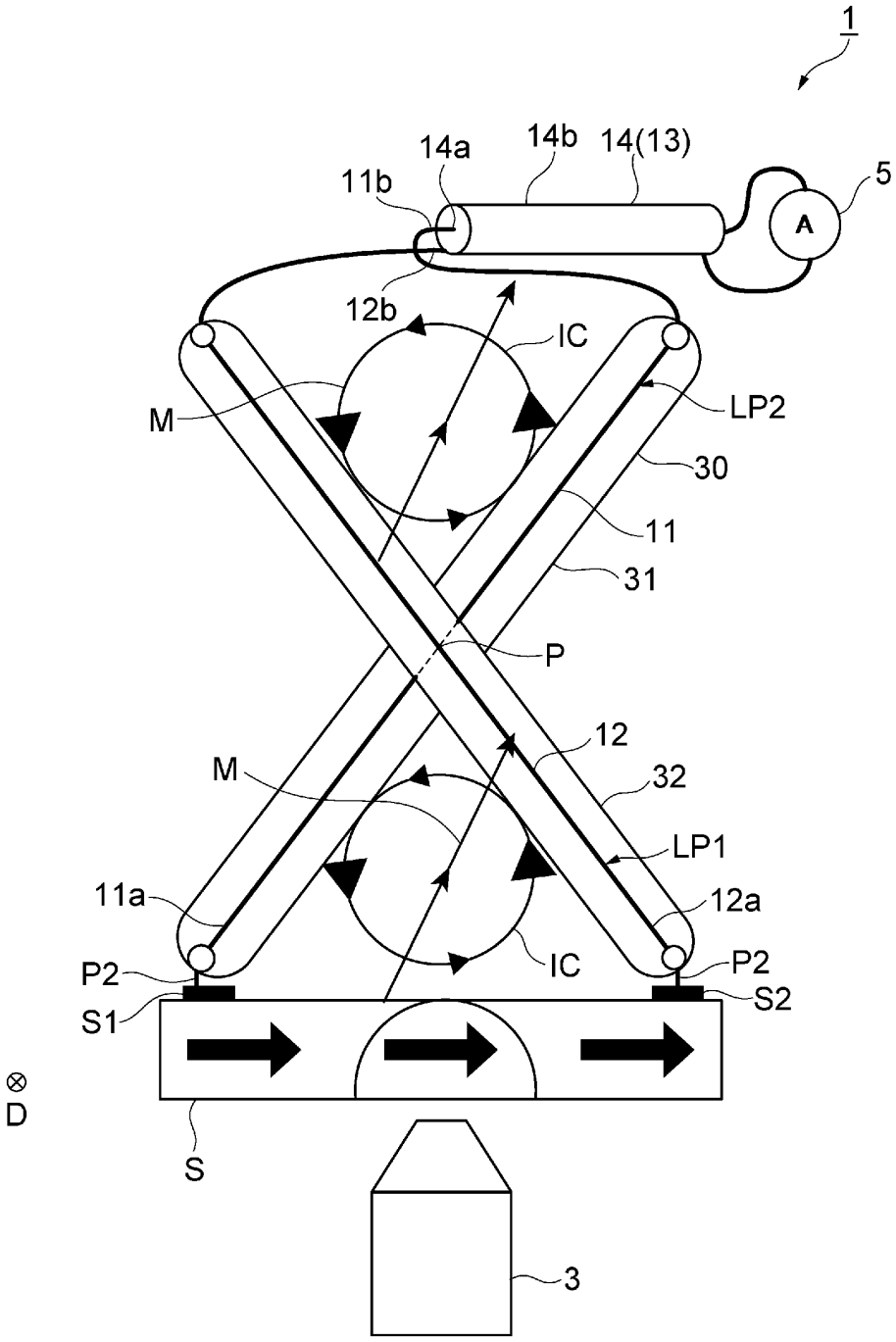
[図7]



[図8]


⊗D
21(11)
21a
21b
P1
S
P2
22a
22b
22(12)
19
21a
21b
22b
22a
17a
17b
17c
17(13)
18
5

[図9]


1
14a
14b
14(13)
11b
5
A
12b
IC
M
LP2
30
11
31
P
12
M
32
LP1
11a
12a
IC
P2
P2
S1
S2
⊗
D
S
3

[図10]

