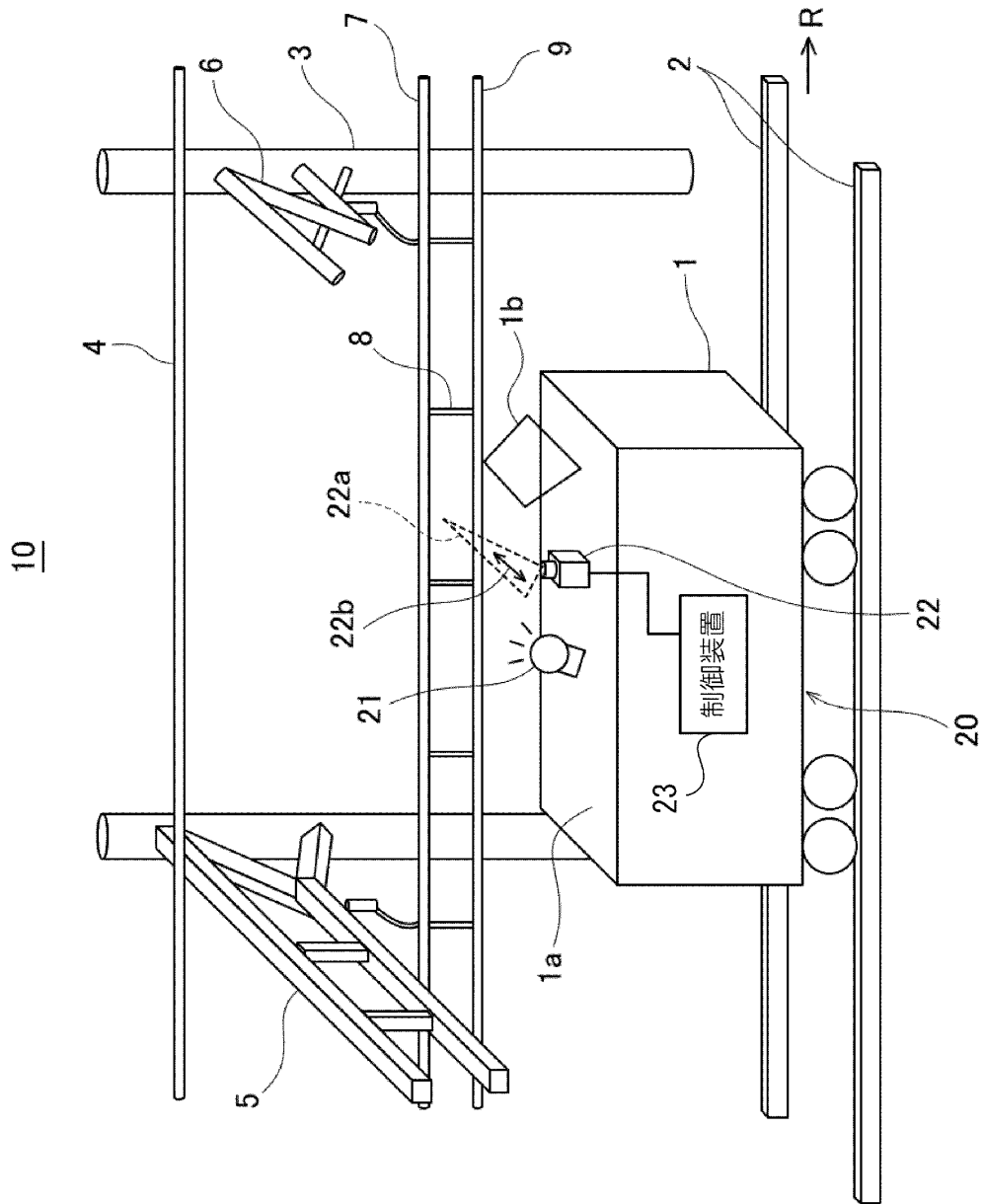


[図1]



[図2]

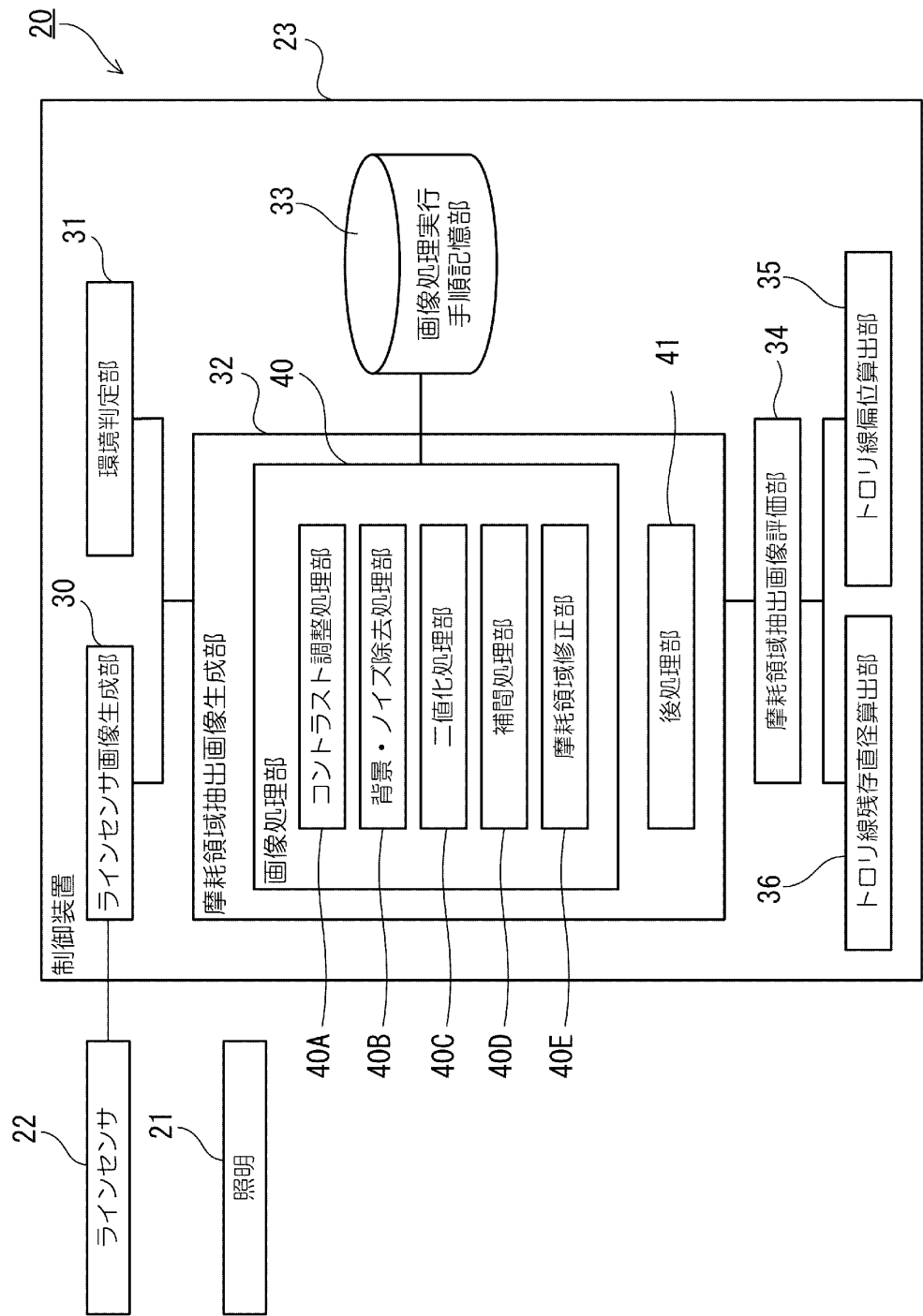
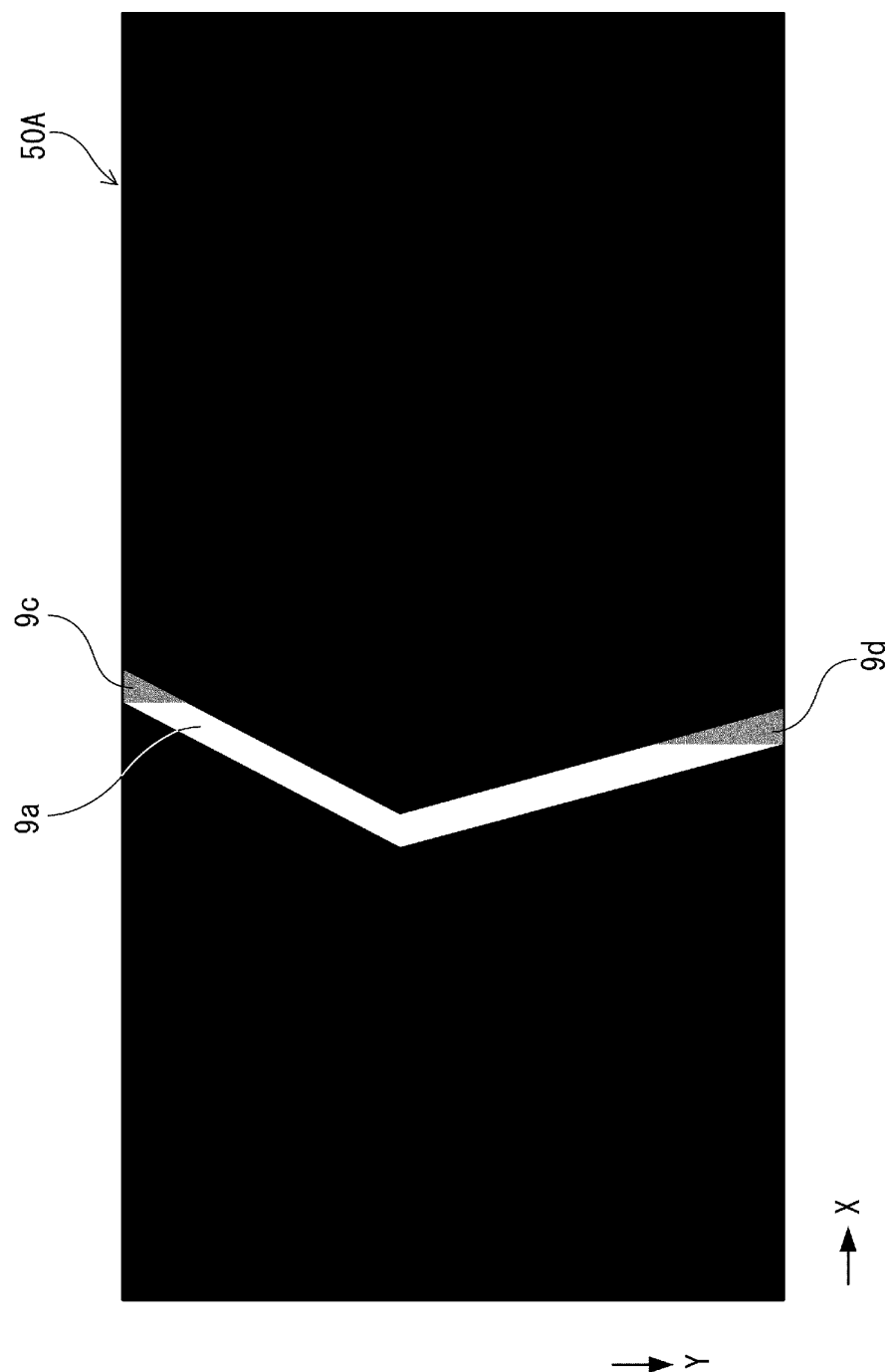
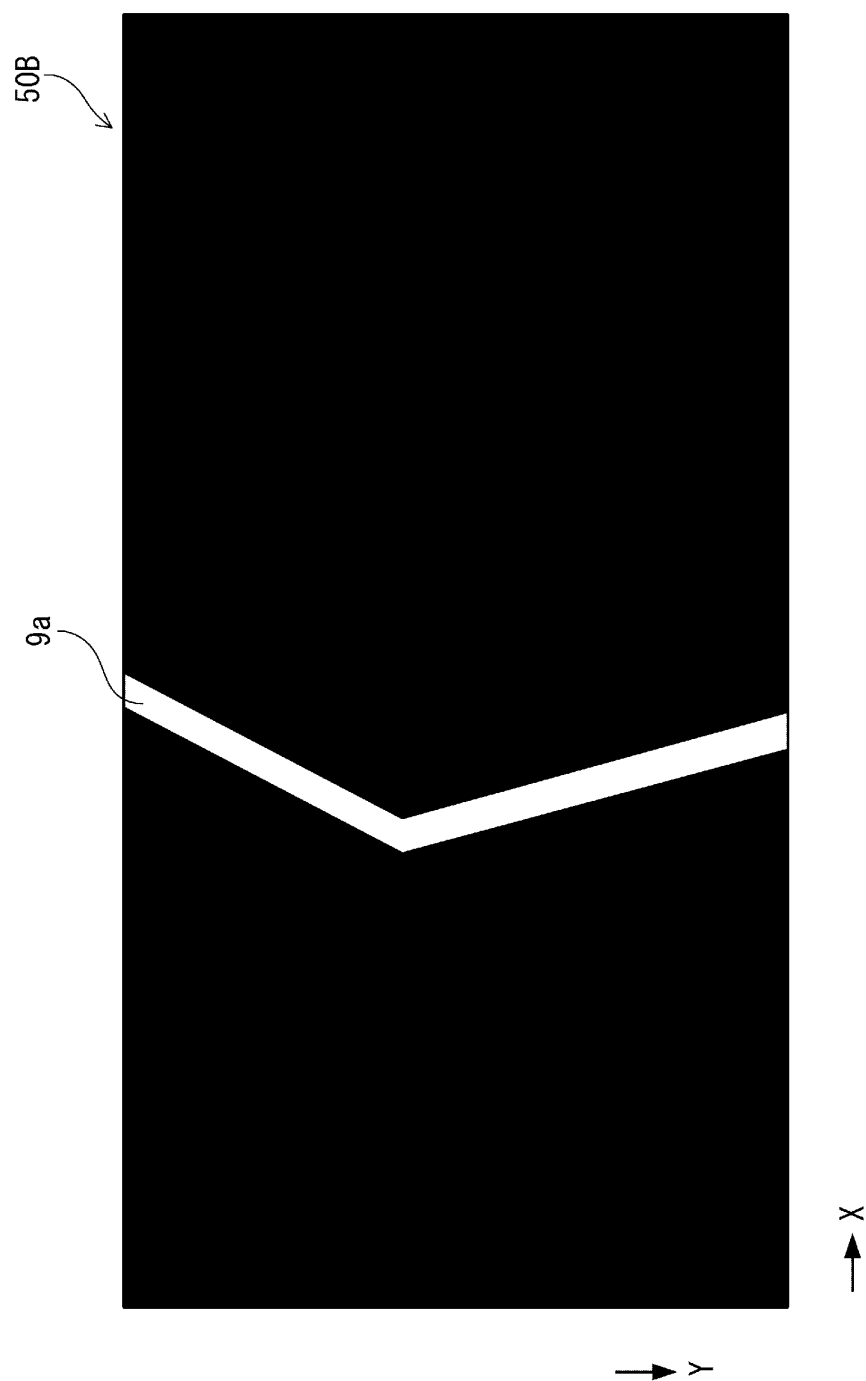


Figure 1 is a schematic diagram of a cross-section of a semiconductor device. The diagram shows a substrate 10 with a central region 4. A vertical line 5, 6 divides the device. A horizontal line 7 is located in the central region 4. A diagonal line 9 is shown, with segments 9a, 9b, and 9c. A shaded region 9d is located in the upper right corner. A coordinate system with X and Y axes is shown in the bottom right corner.

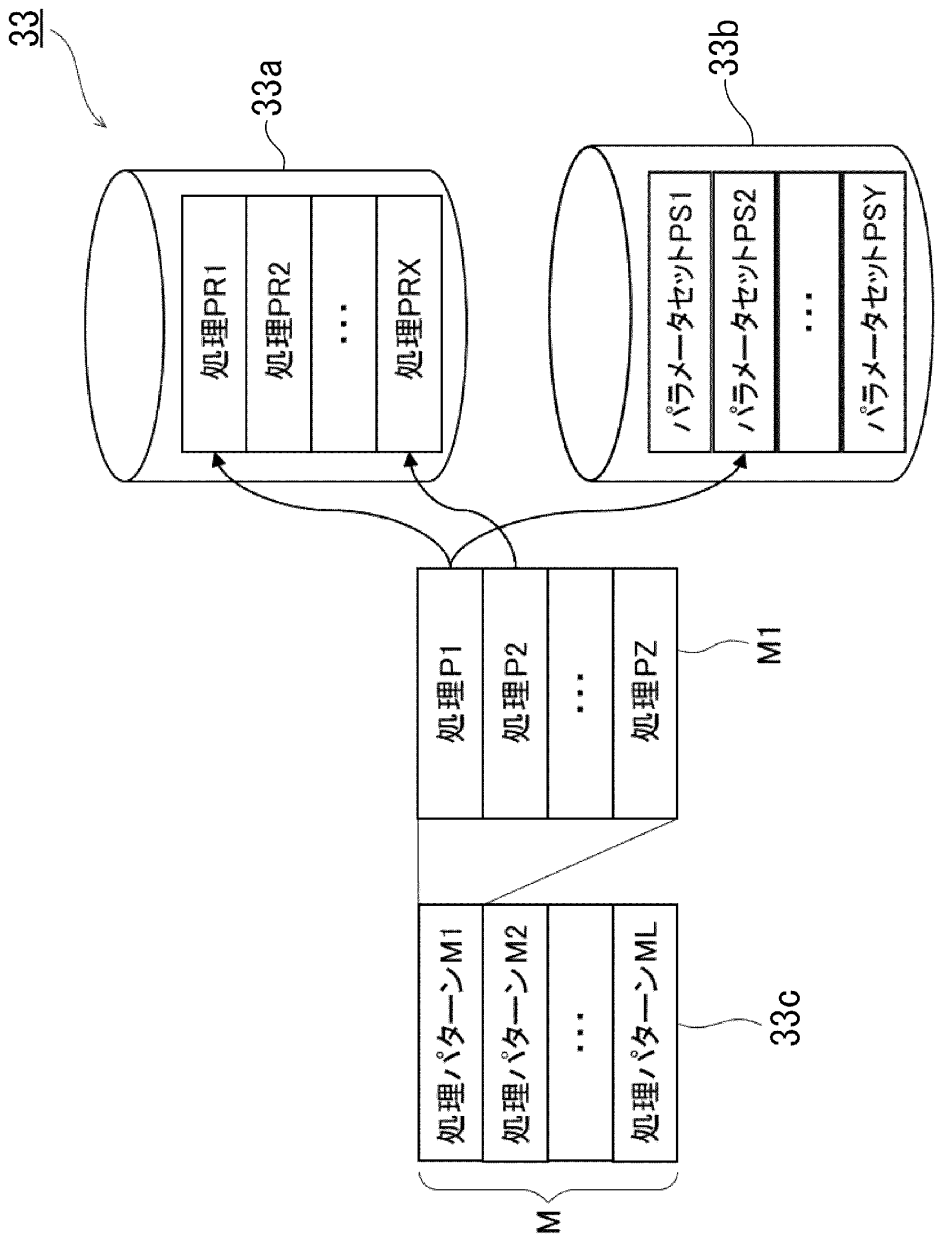
[図4]



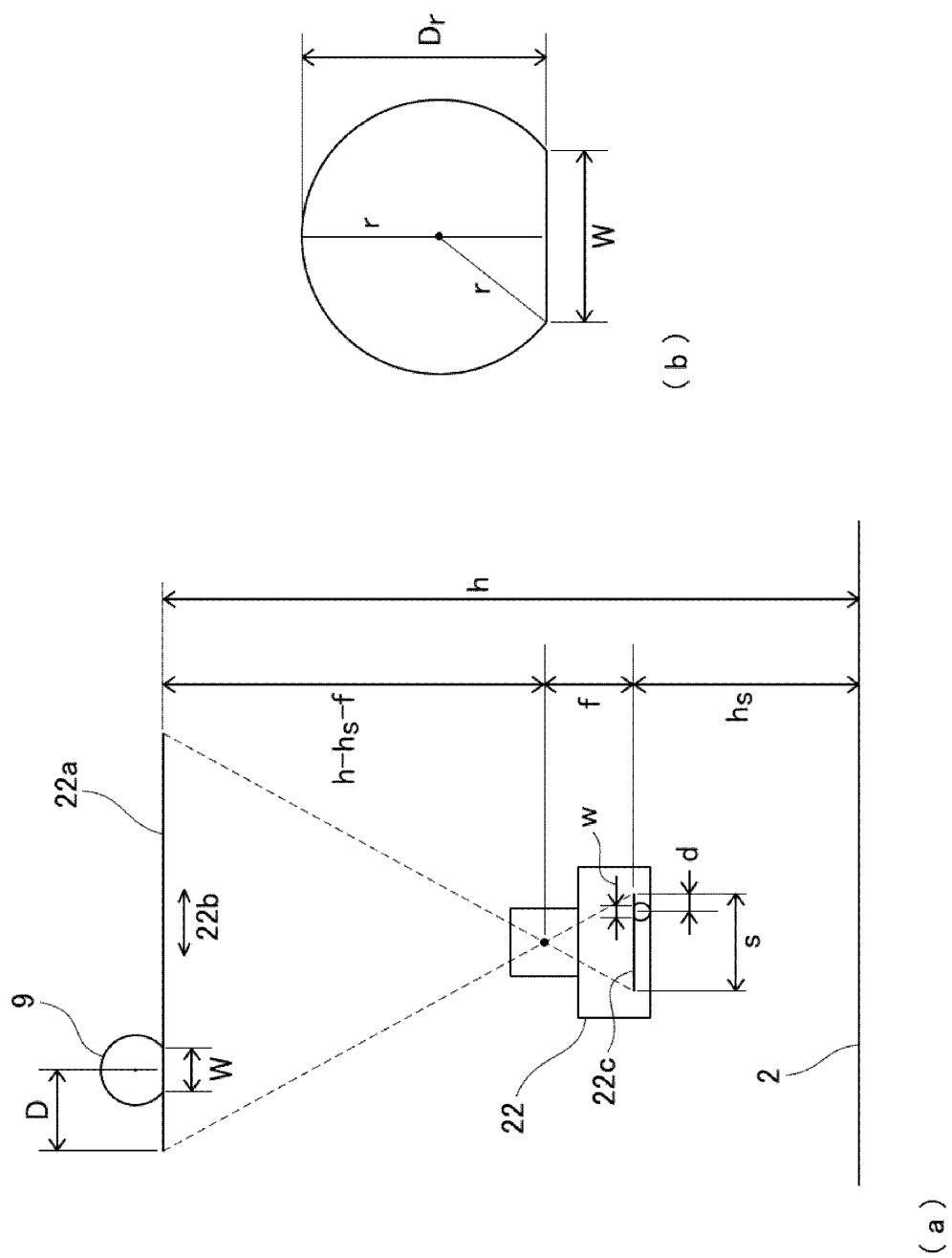
[図5]



[図6]



[図7]



[図8]

