

The plan view of the semiconductor device 10 shows a rectangular layout divided into four main regions: POS A, POS B, POS C, and POS D. Within these regions, there are several functional blocks and patterns. In the POS A region, there are blocks labeled TP1, EX1, and PUA. In the POS B region, there are blocks labeled TP2, EX2, and PUB. In the POS C region, there are blocks labeled TP3, EX2, and PUC. In the POS D region, there are blocks labeled TP4, EX2, and PUD. A central region contains blocks labeled WS, CL1, CL2, CL3, and Dry. A cross-sectional view 11 is shown on the right side, and a detailed view 13 is shown at the bottom right, focusing on the region around the WS, CL1, CL2, CL3, and Dry blocks. Various other labels like 20a, 20b, 21a, 21b, 21c, 21d, 22, 23, 24a, 24b, 30a, 311a, 312a, 313a, 314a, 32a, 33a, 39a, 111, 112, 113, and 15 are used to identify specific components and features.

[illegible]

Fig.3

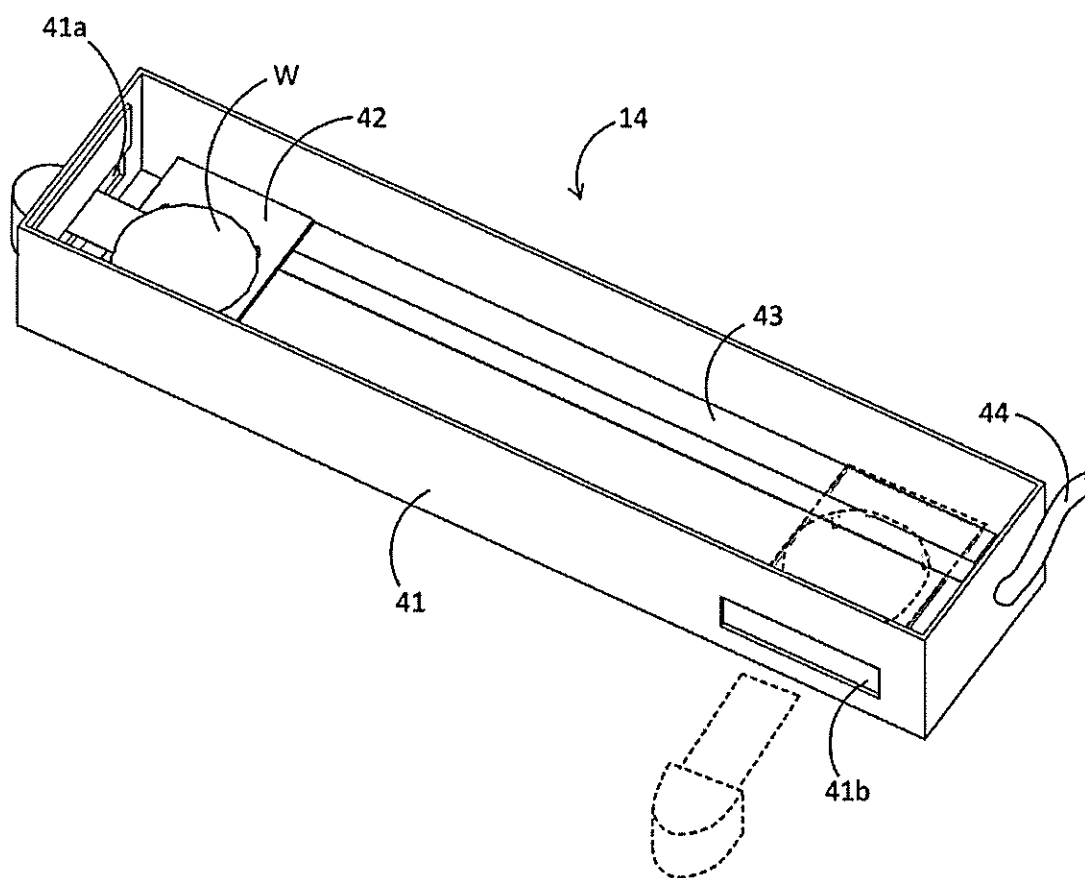


Fig.4

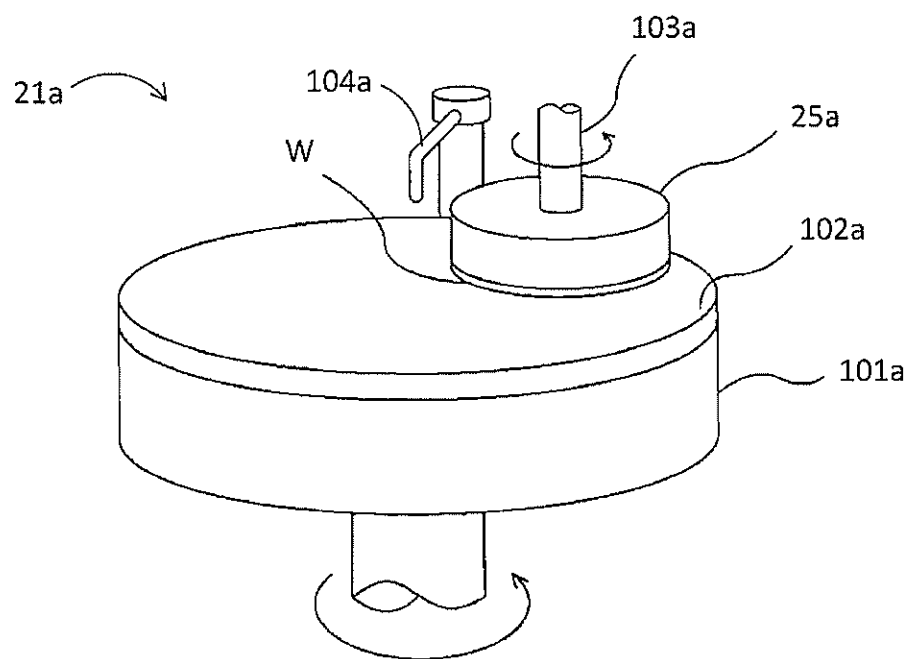


Fig.5

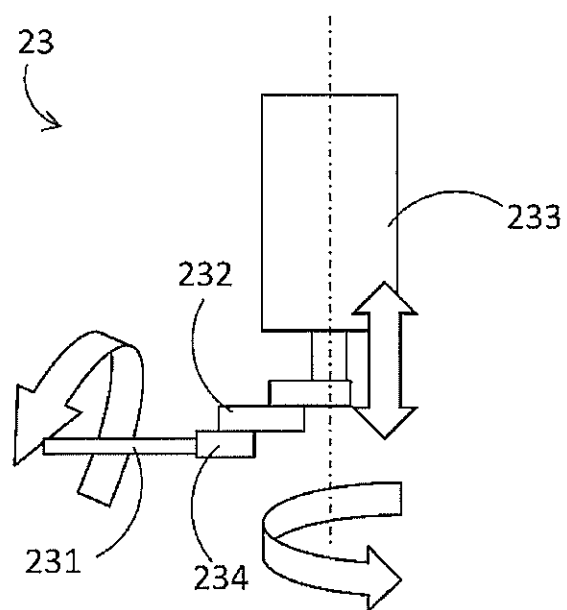


Fig.6

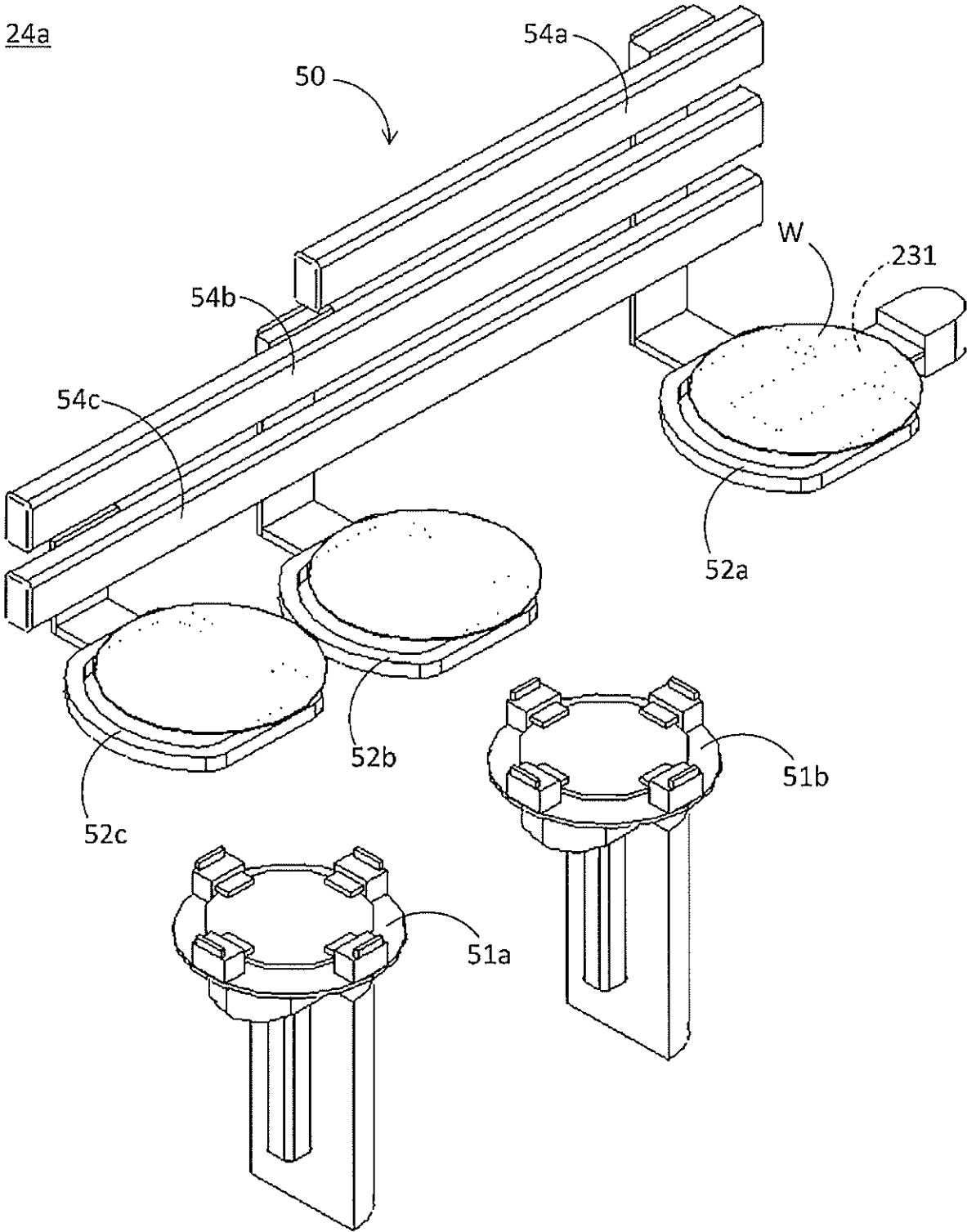


Fig.7

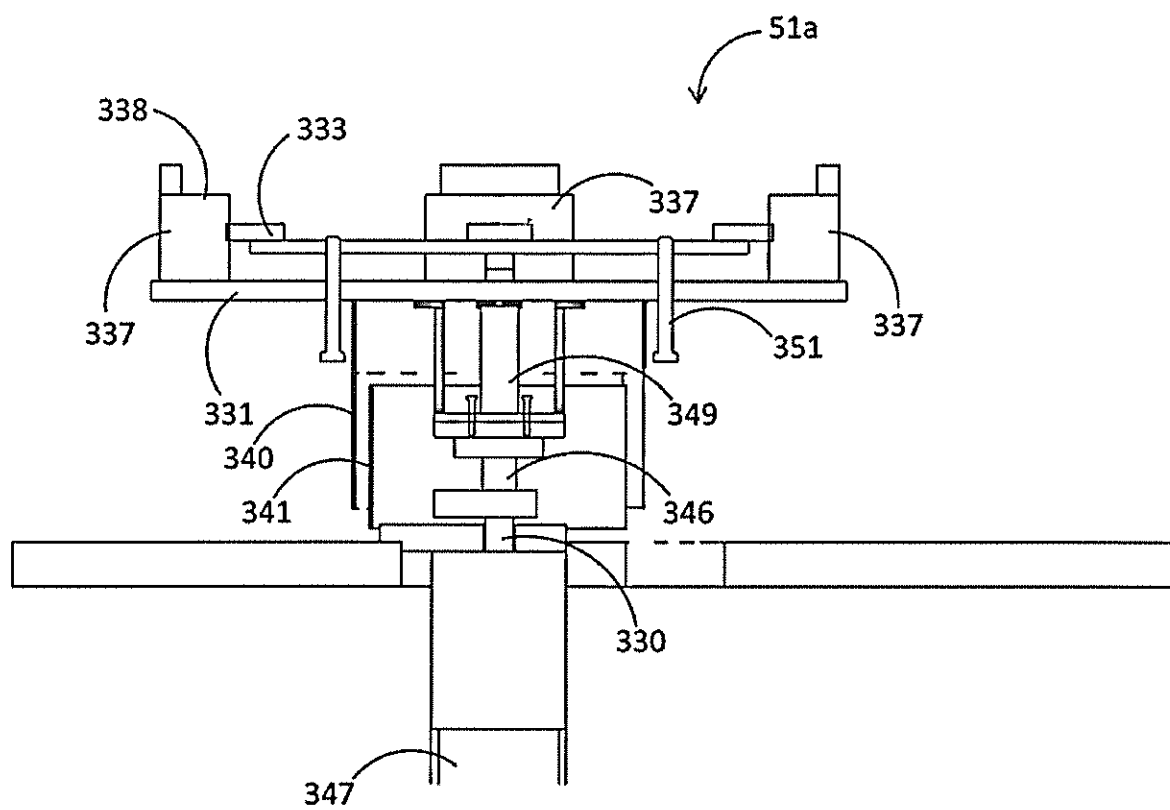


Fig.8

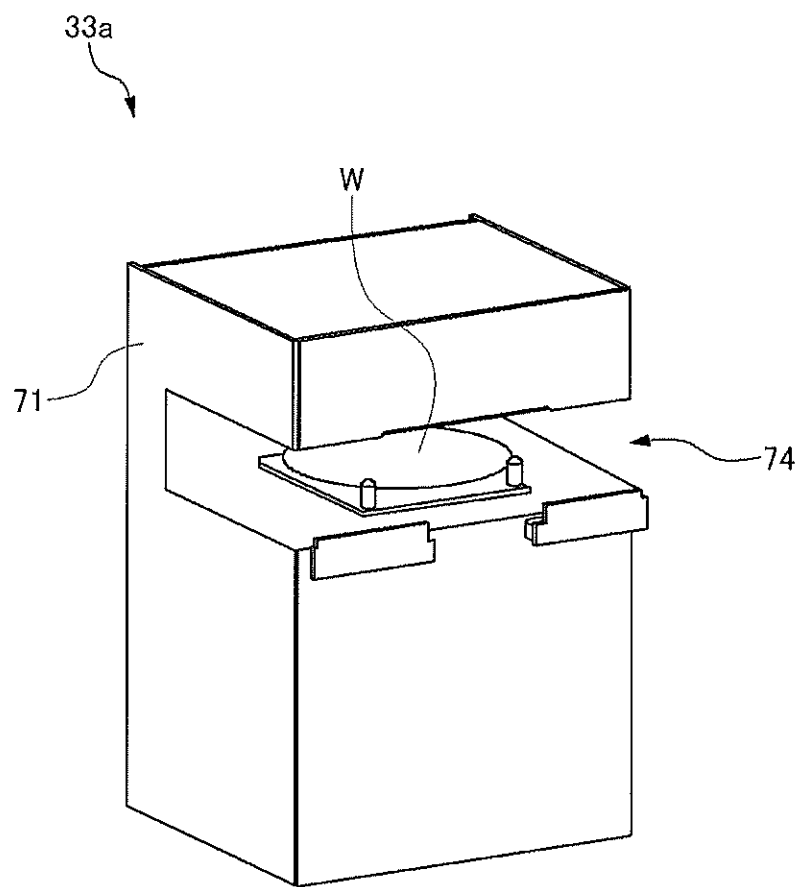


Fig.9

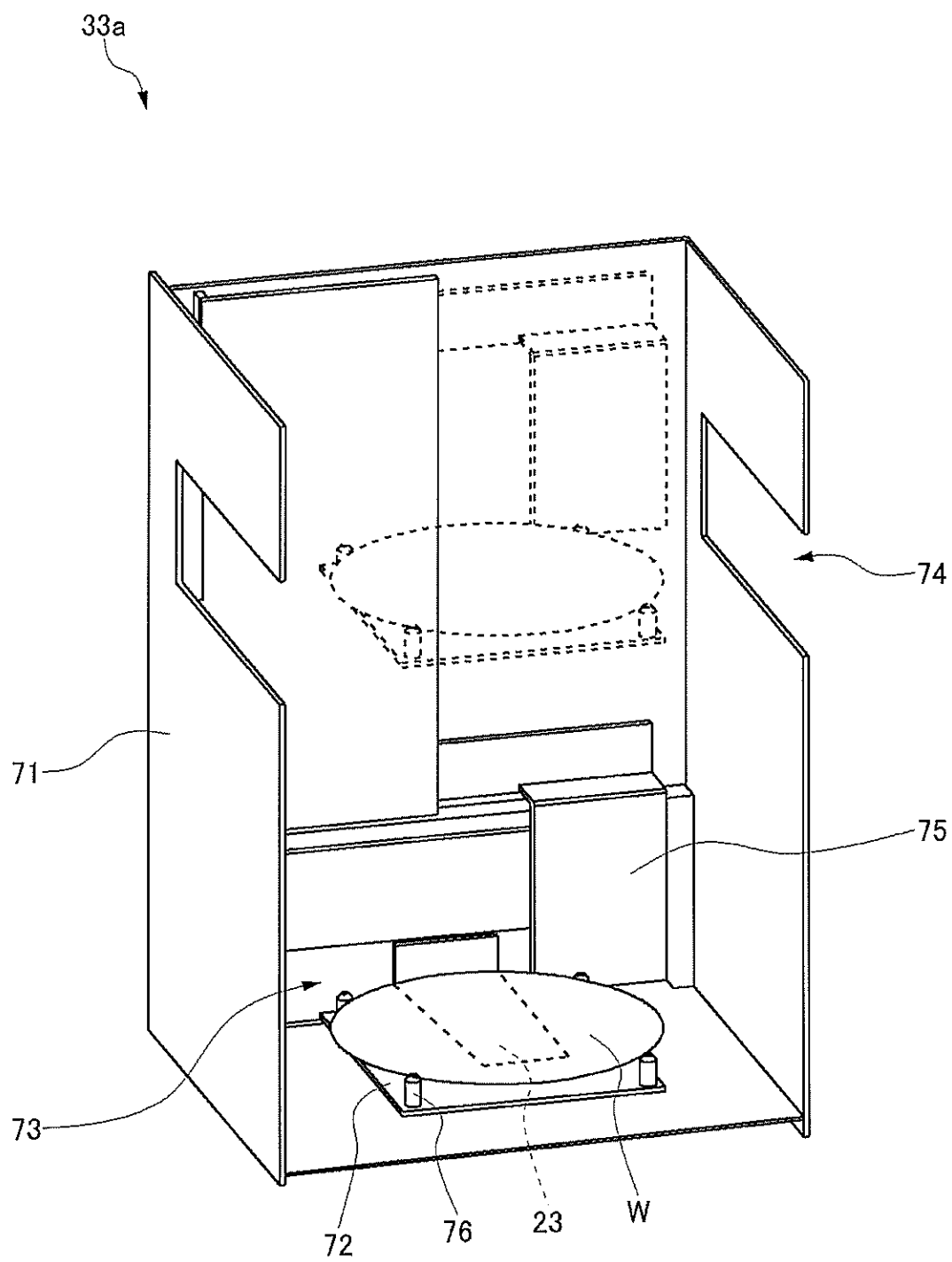


Fig.10

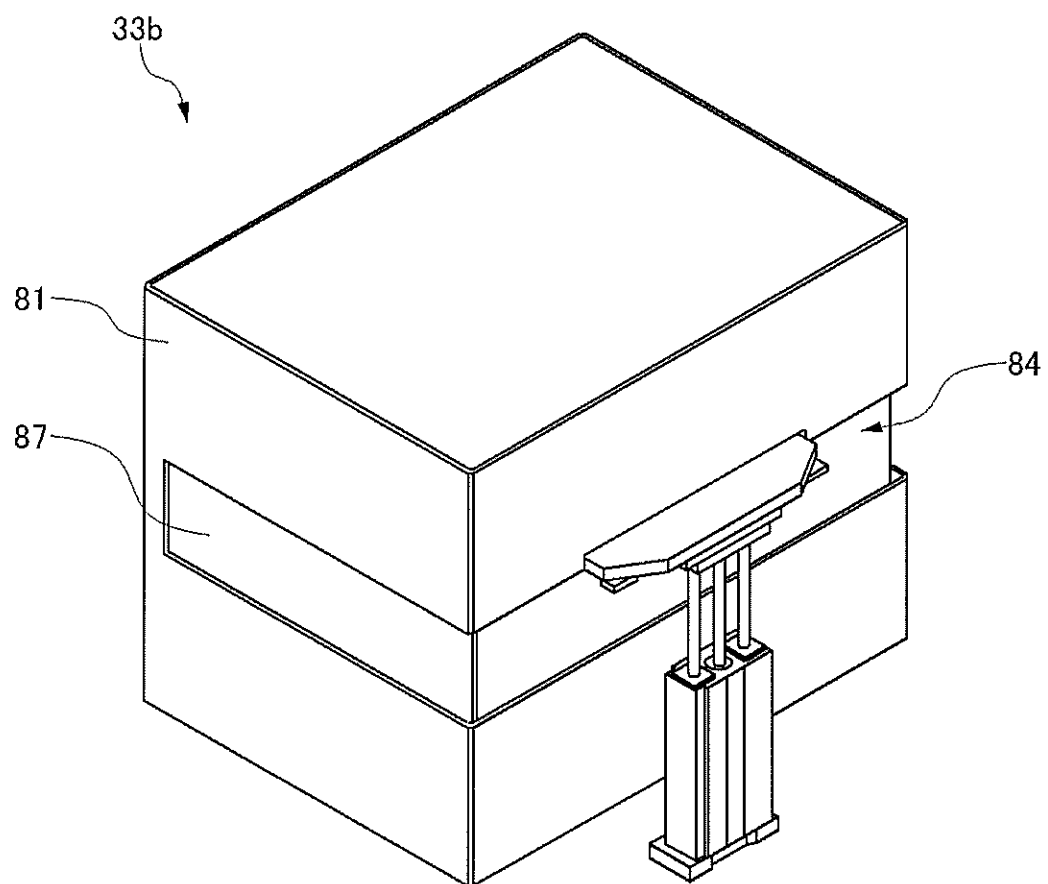


Fig.11

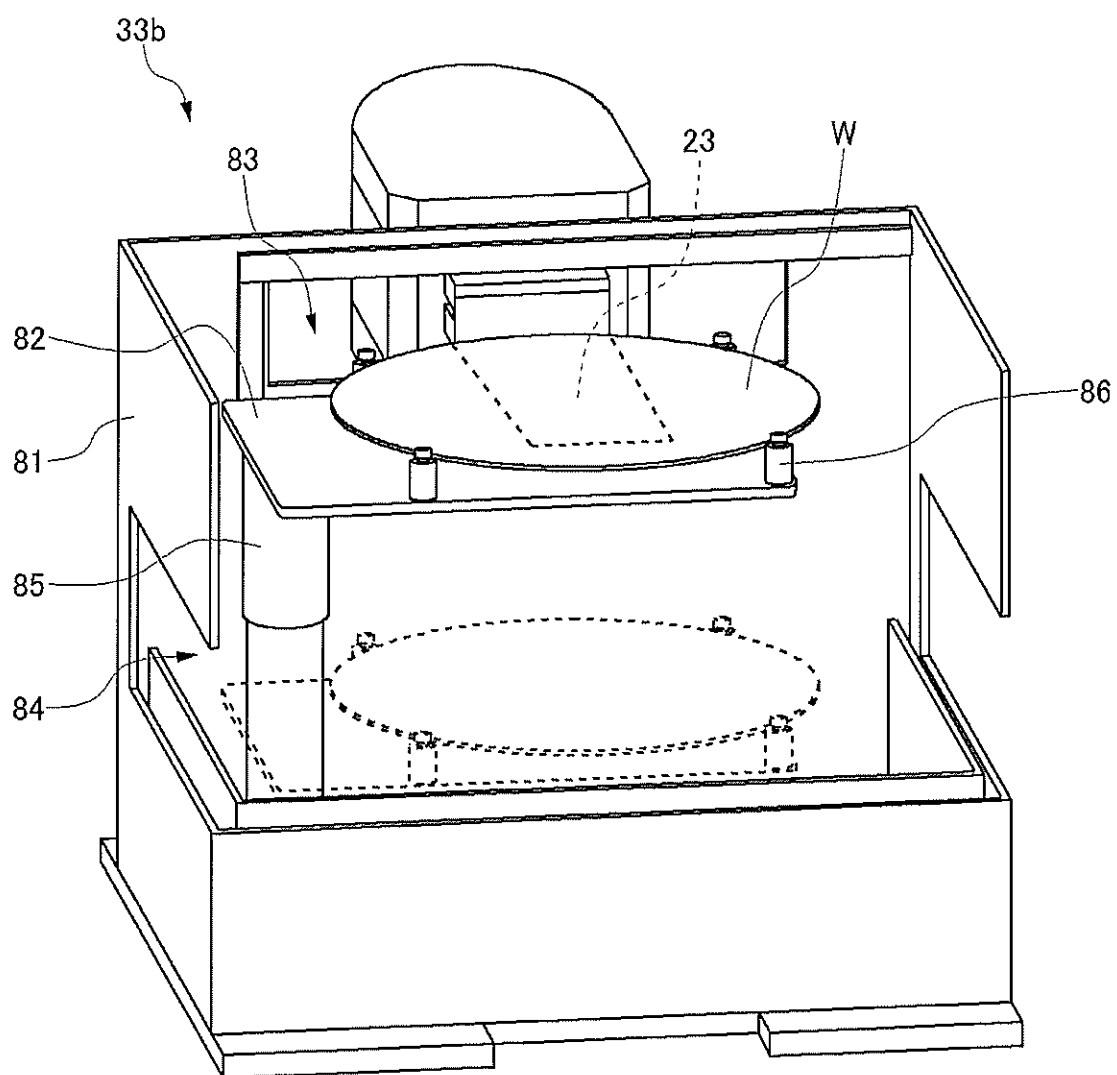


Fig.12

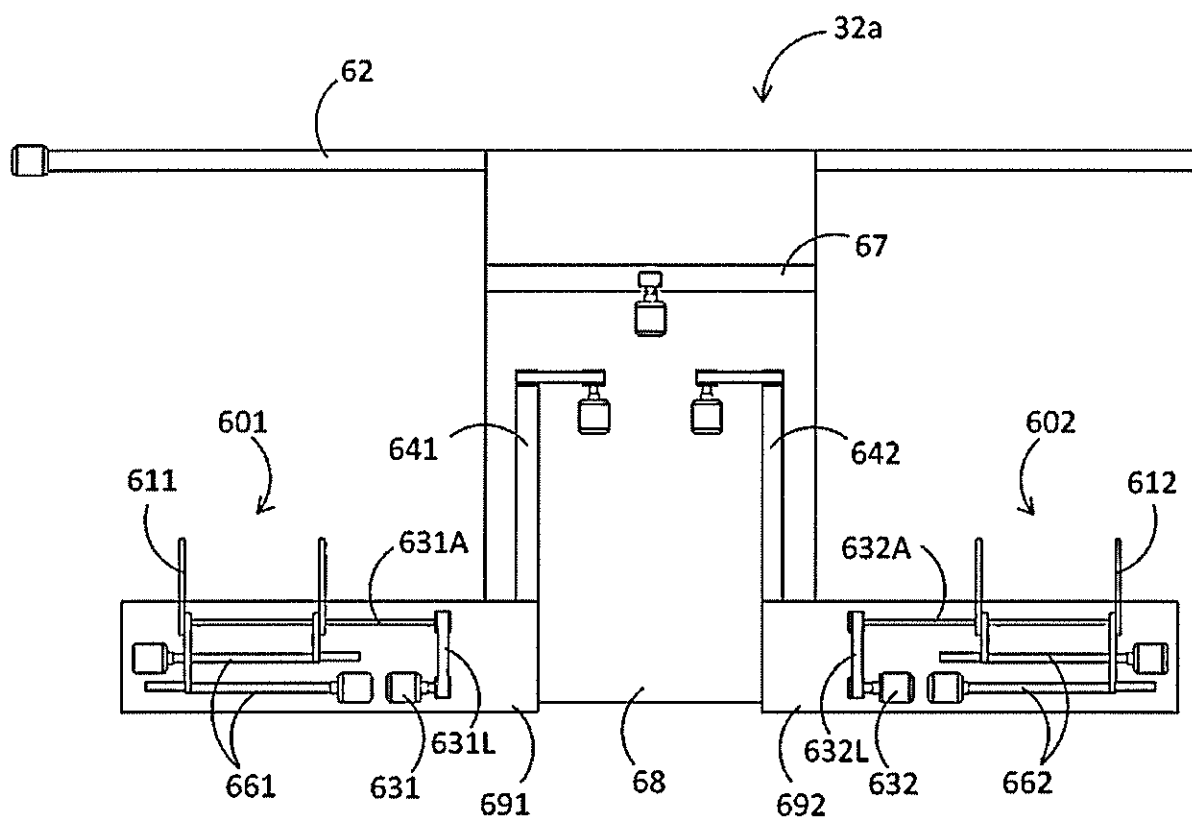


Fig.13A

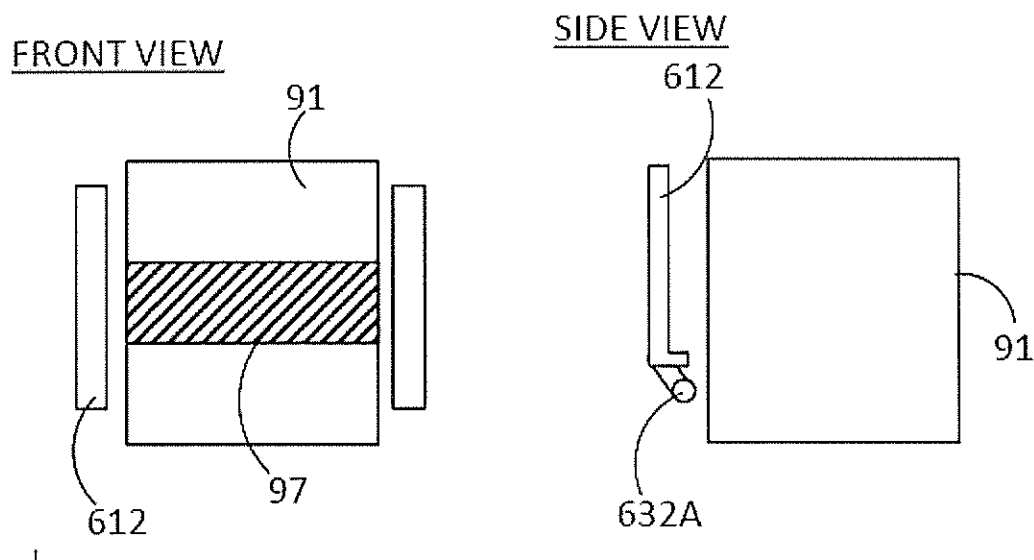
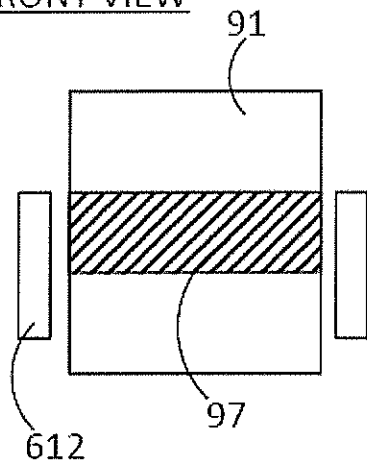


Fig.13B

FRONT VIEW



SIDE VIEW

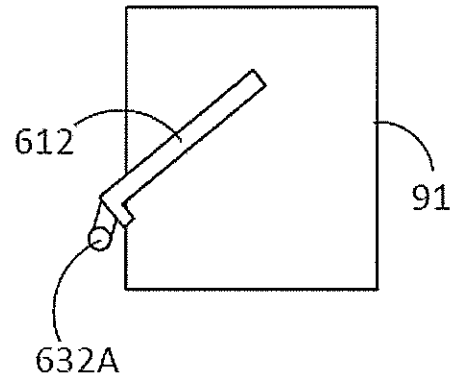
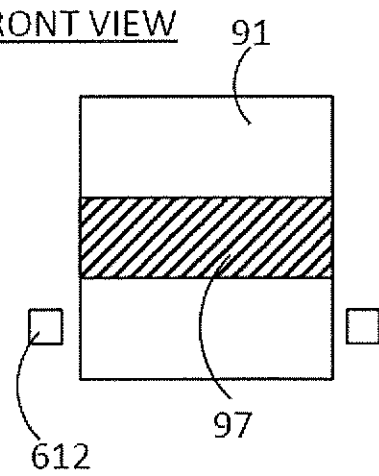


Fig.13C

FRONT VIEW



SIDE VIEW

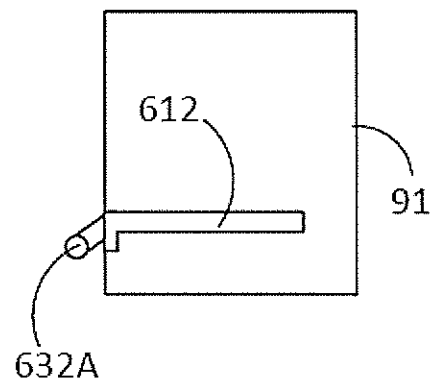
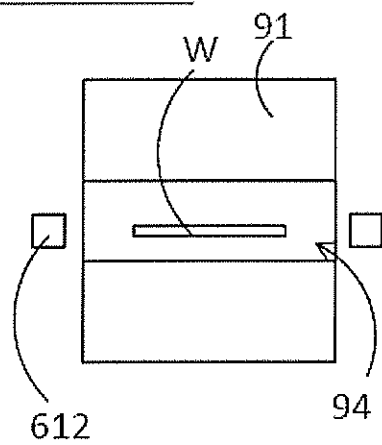


Fig.13D

FRONT VIEW



SIDE VIEW

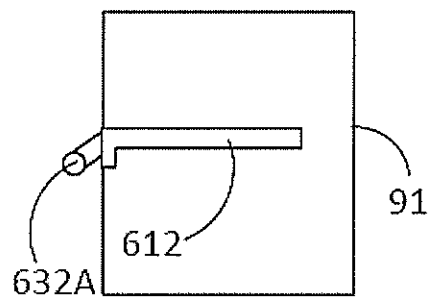
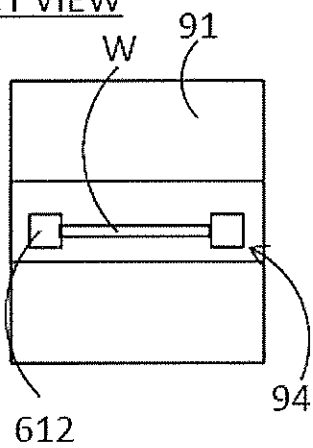


Fig.13E

FRONT VIEW



SIDE VIEW

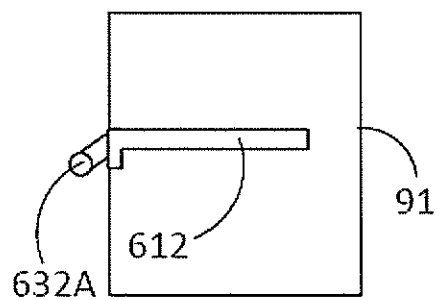


Fig.14

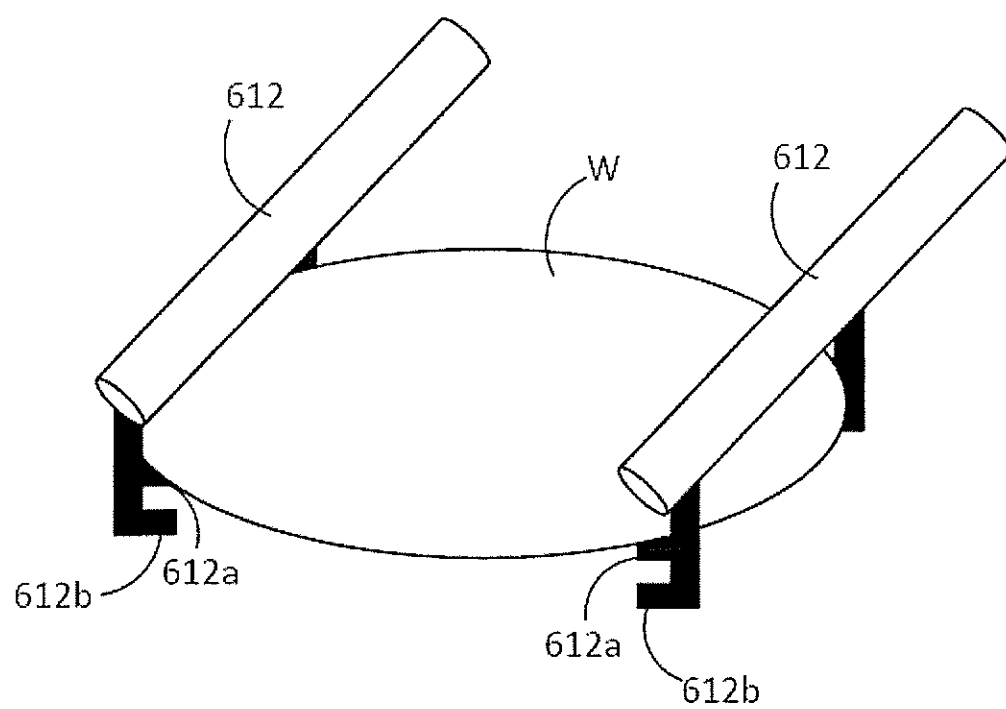


Fig.15

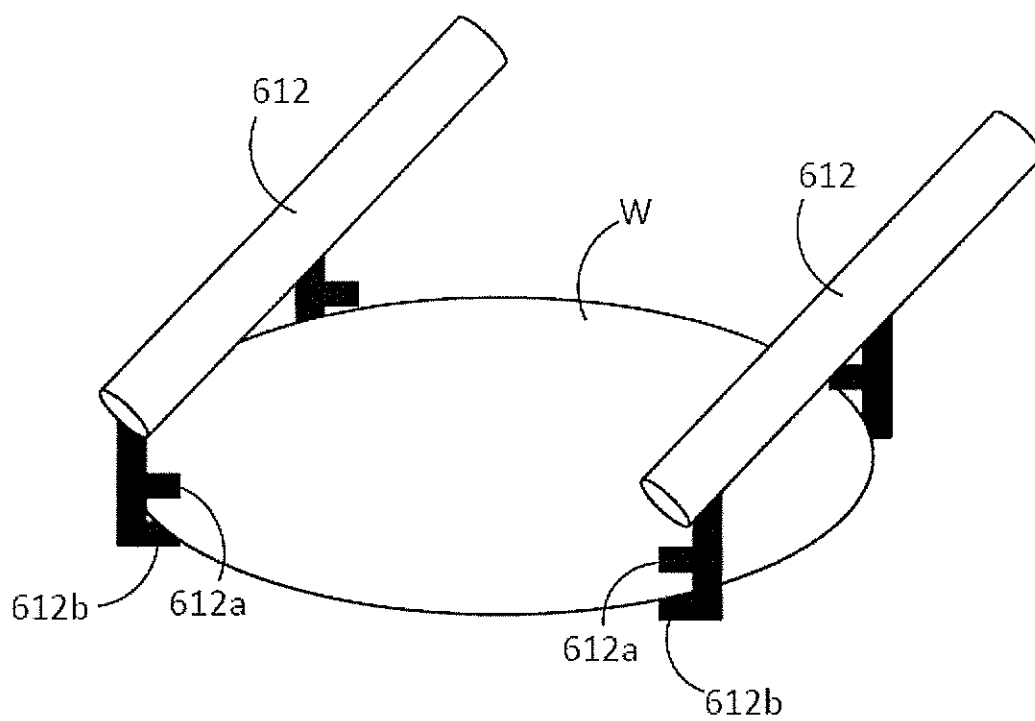


Fig.16A

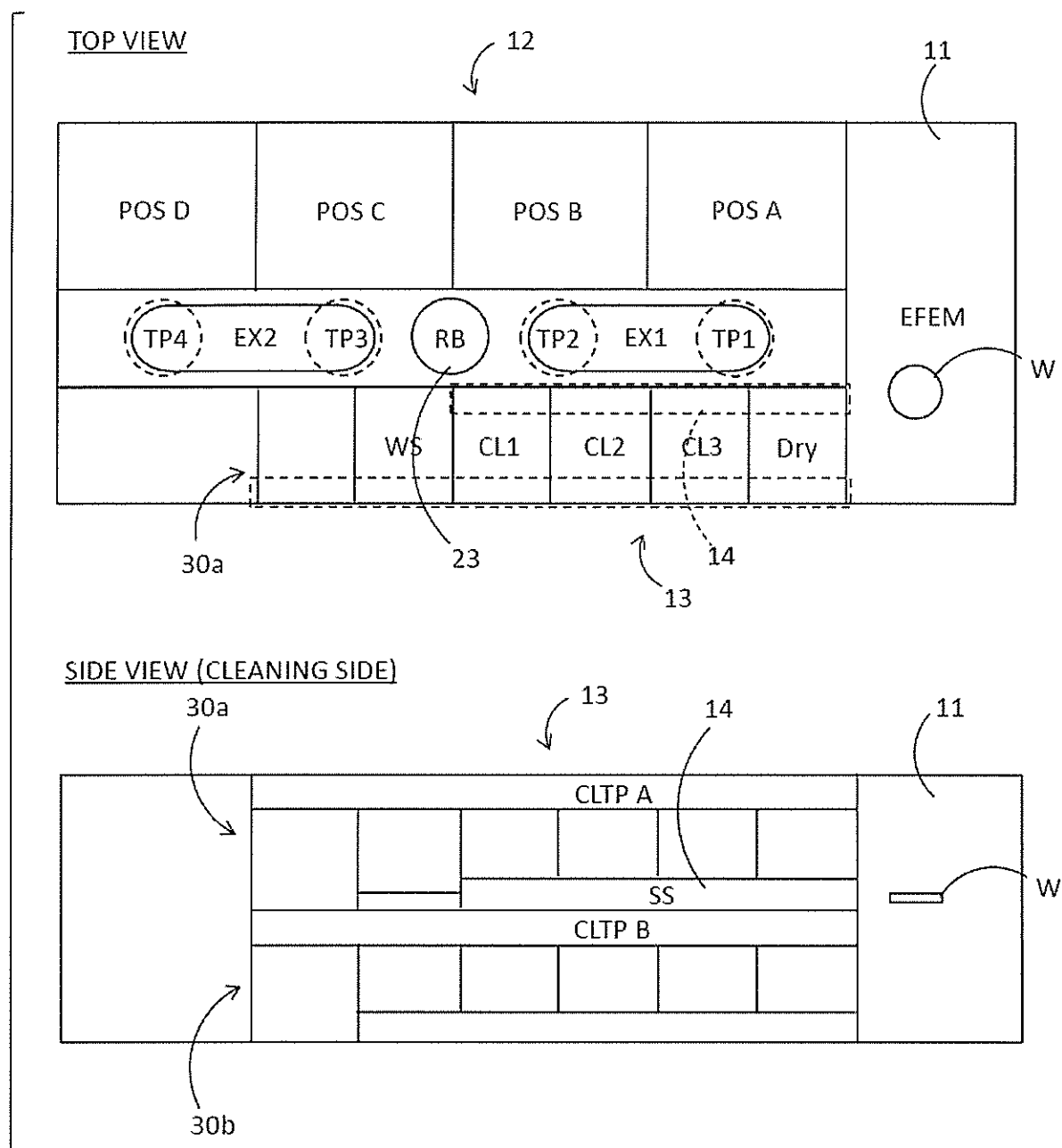


Fig.16B

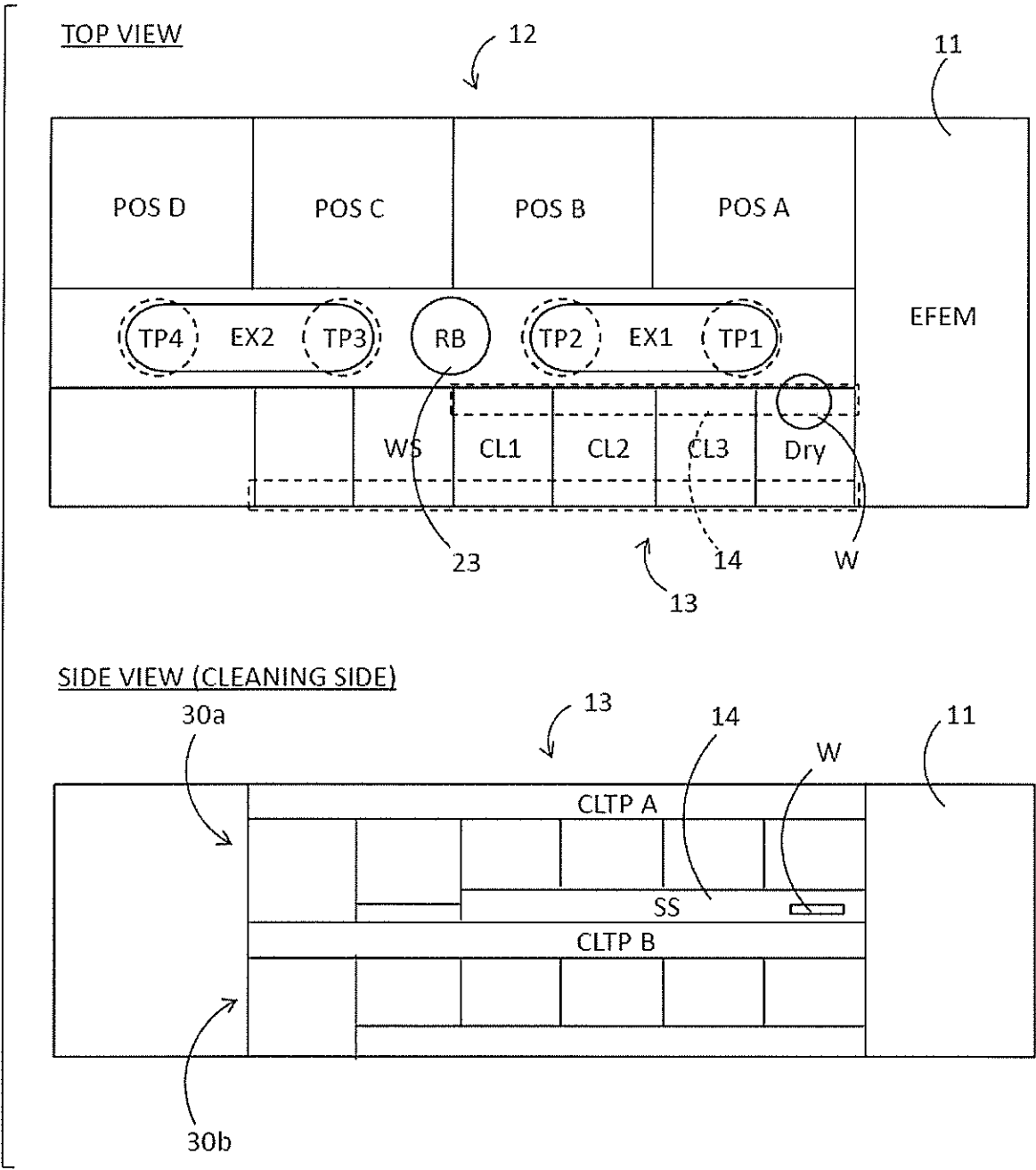


Fig.16C

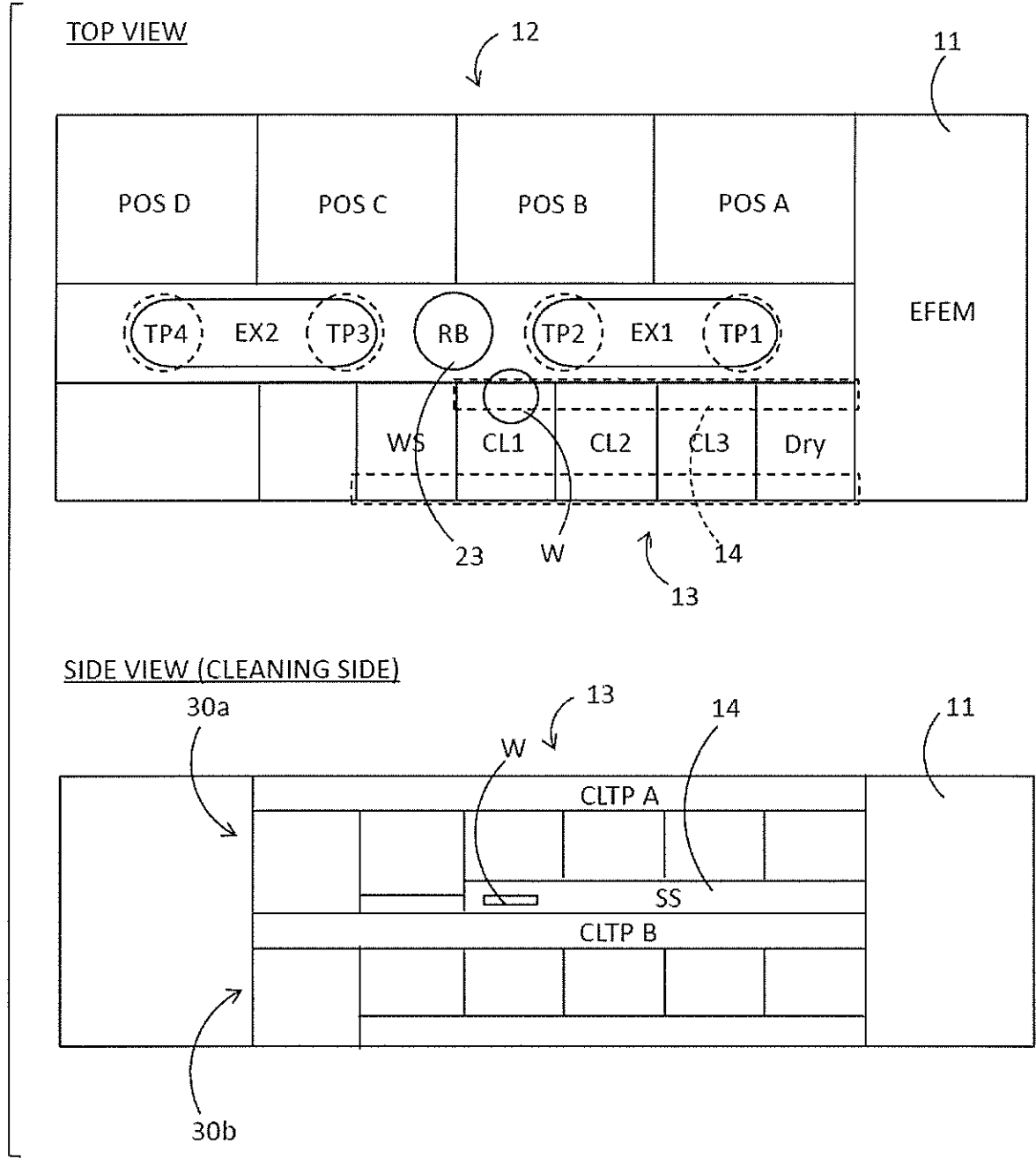


Fig.17A

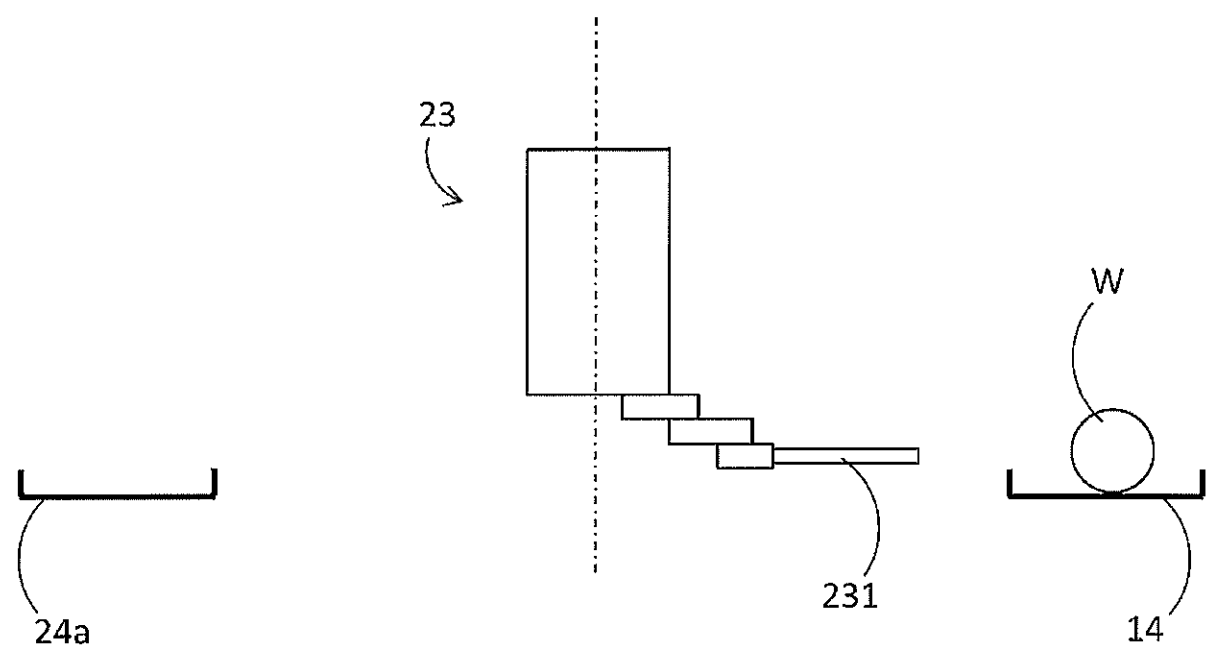


Fig.17B

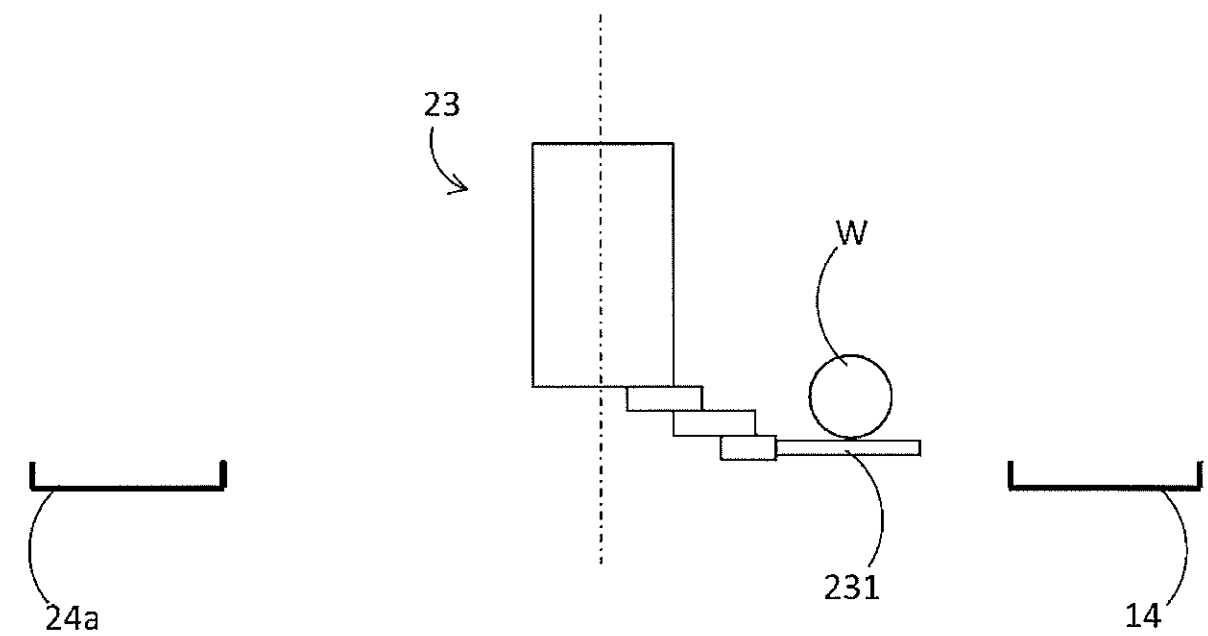


Fig.17C

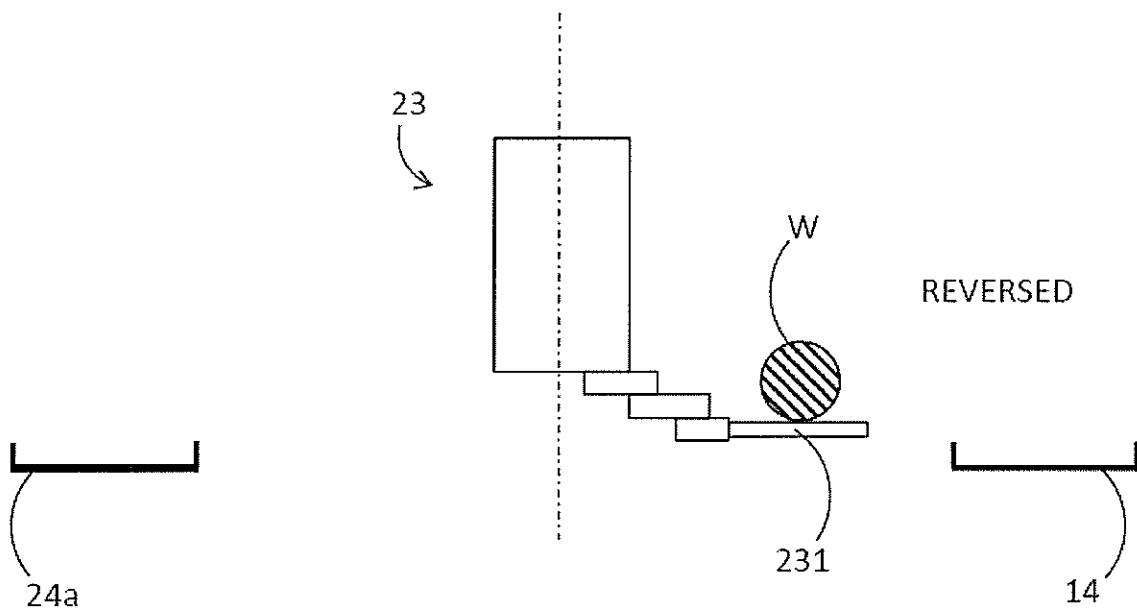


Fig.17D

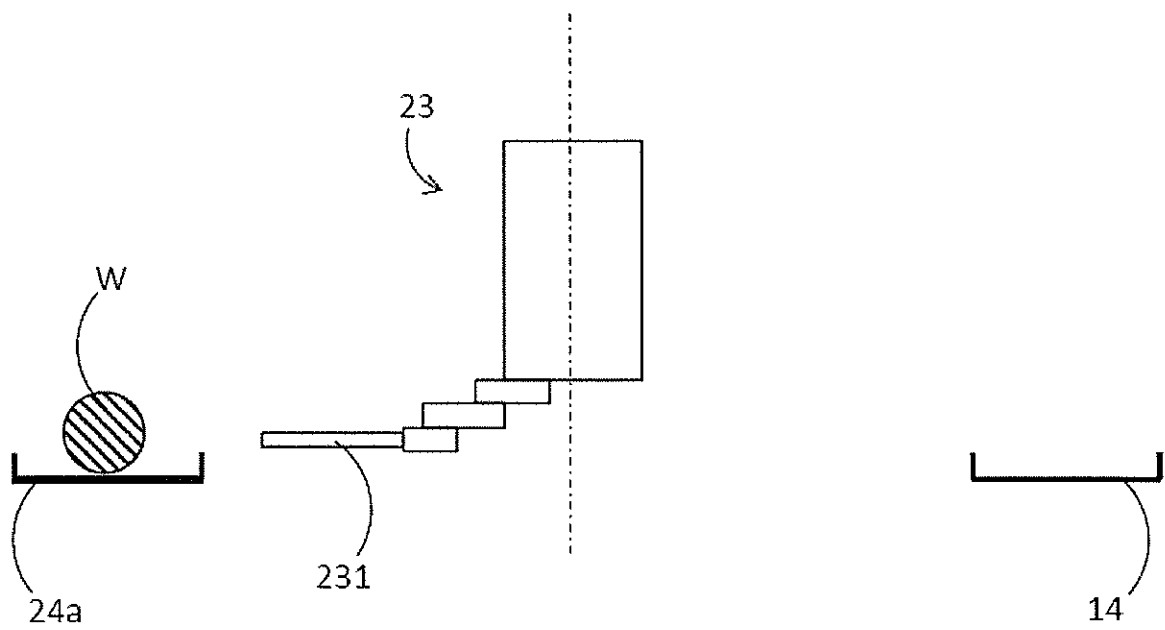


Fig.18A

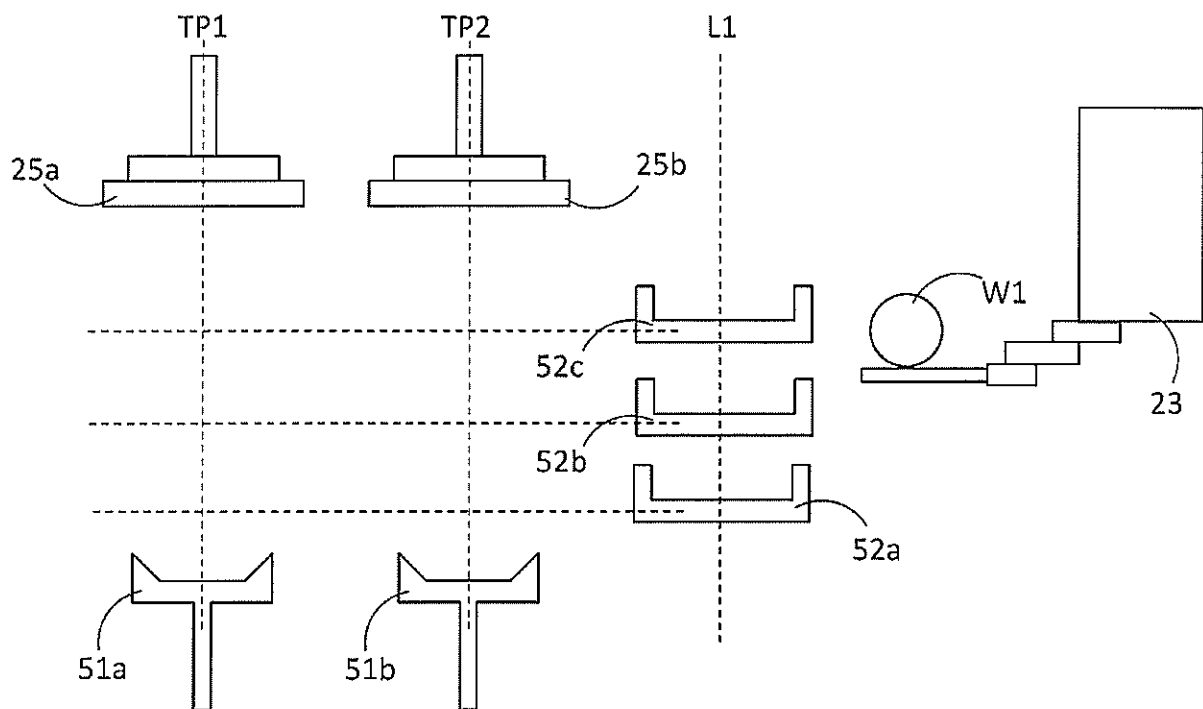


Fig.18B

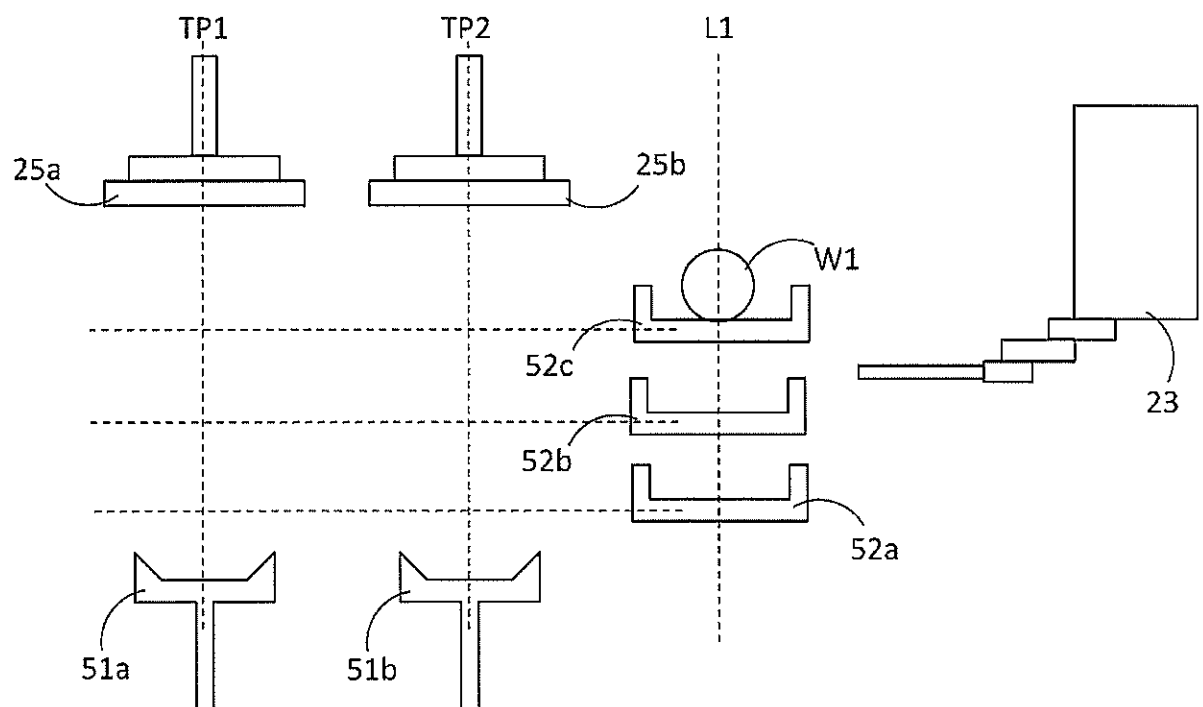


Fig.18C

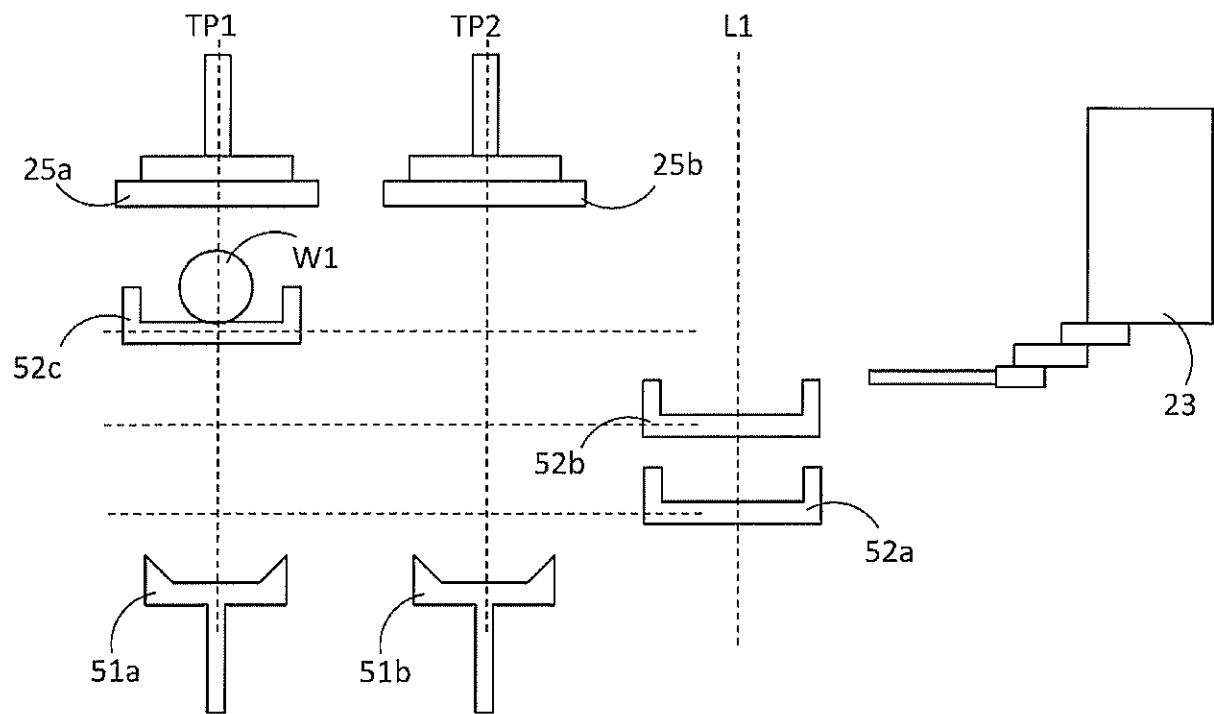


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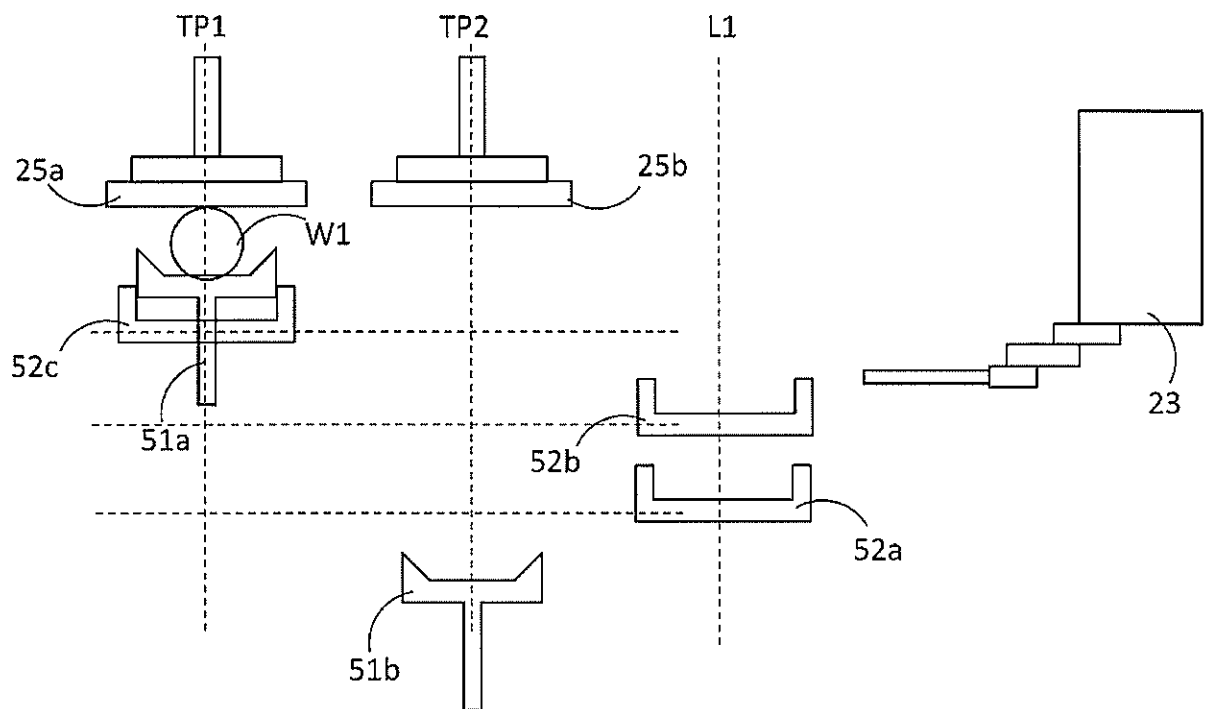


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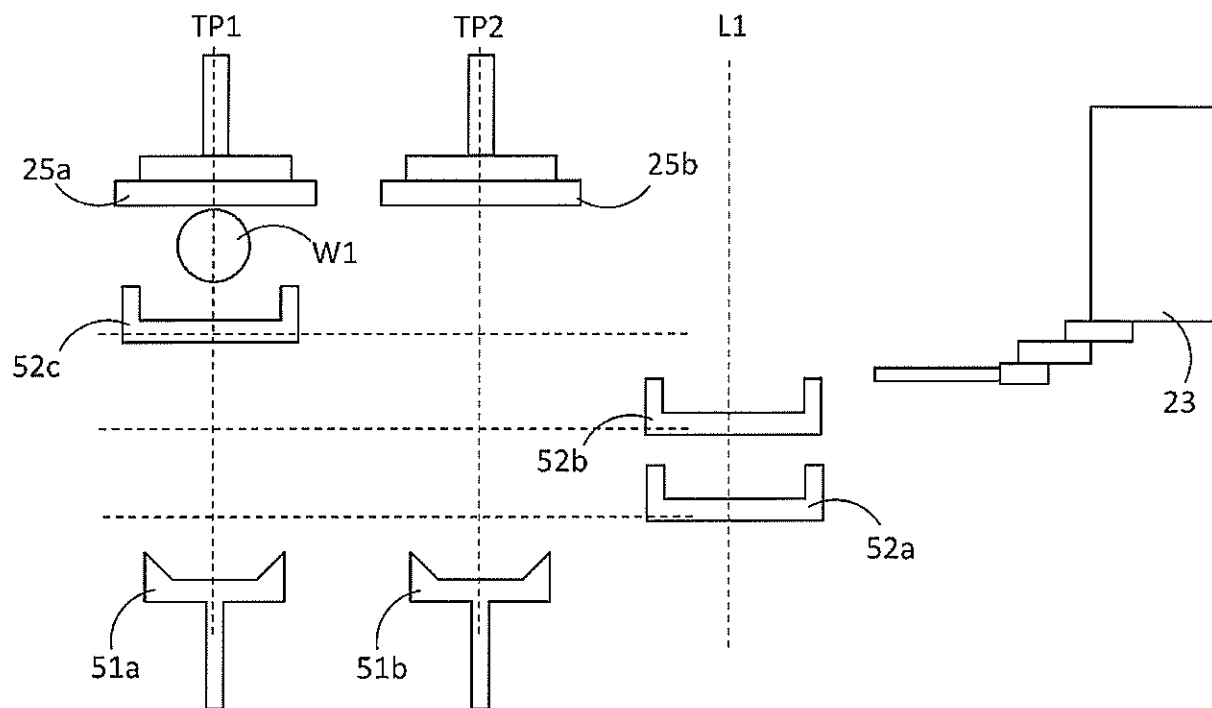


Fig.18F

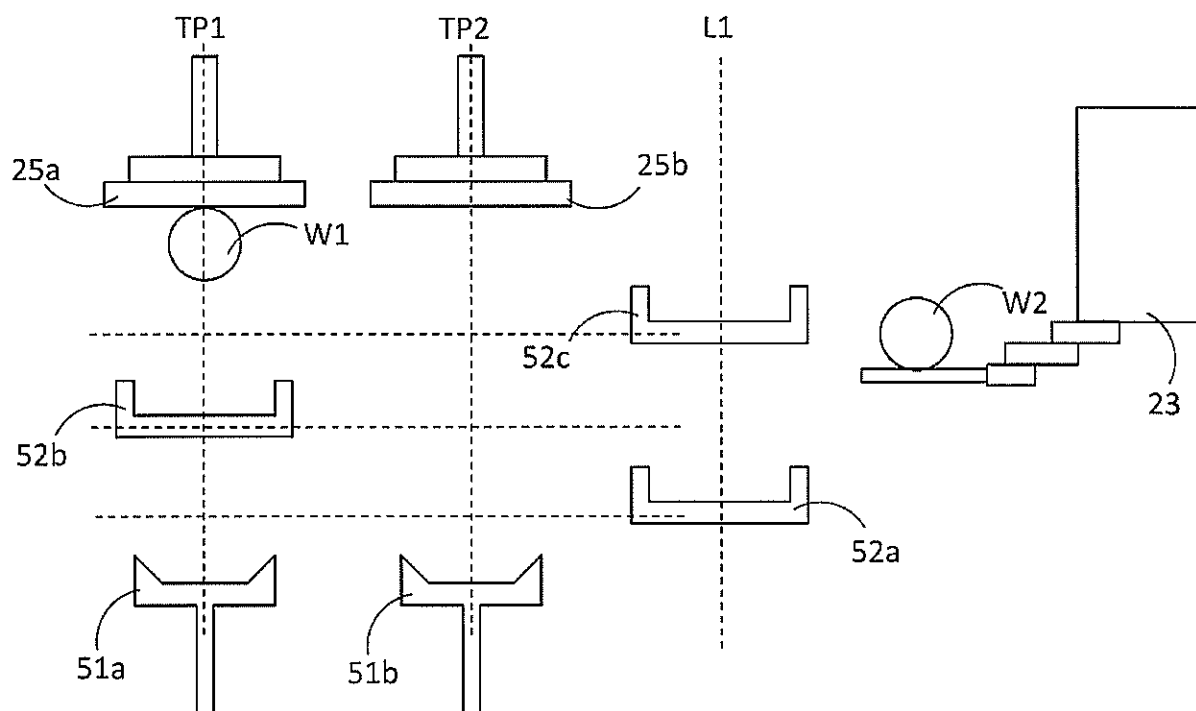


Fig.18G

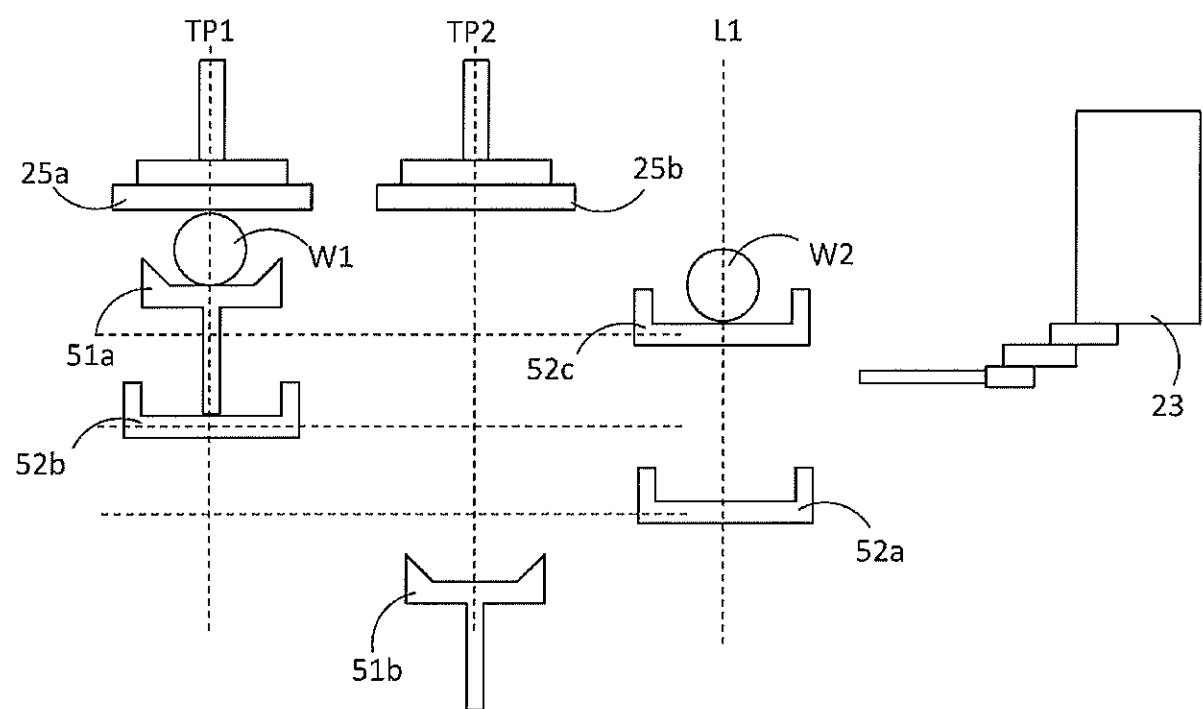


Fig.18H

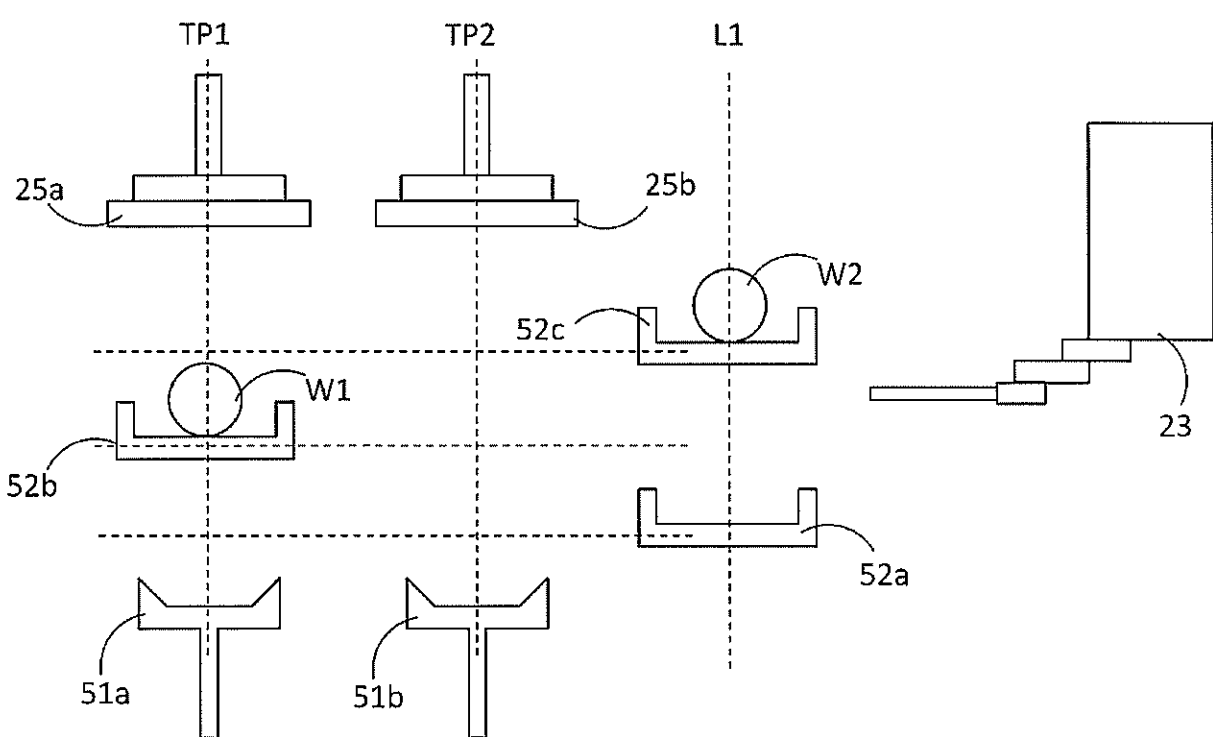


Fig.18I

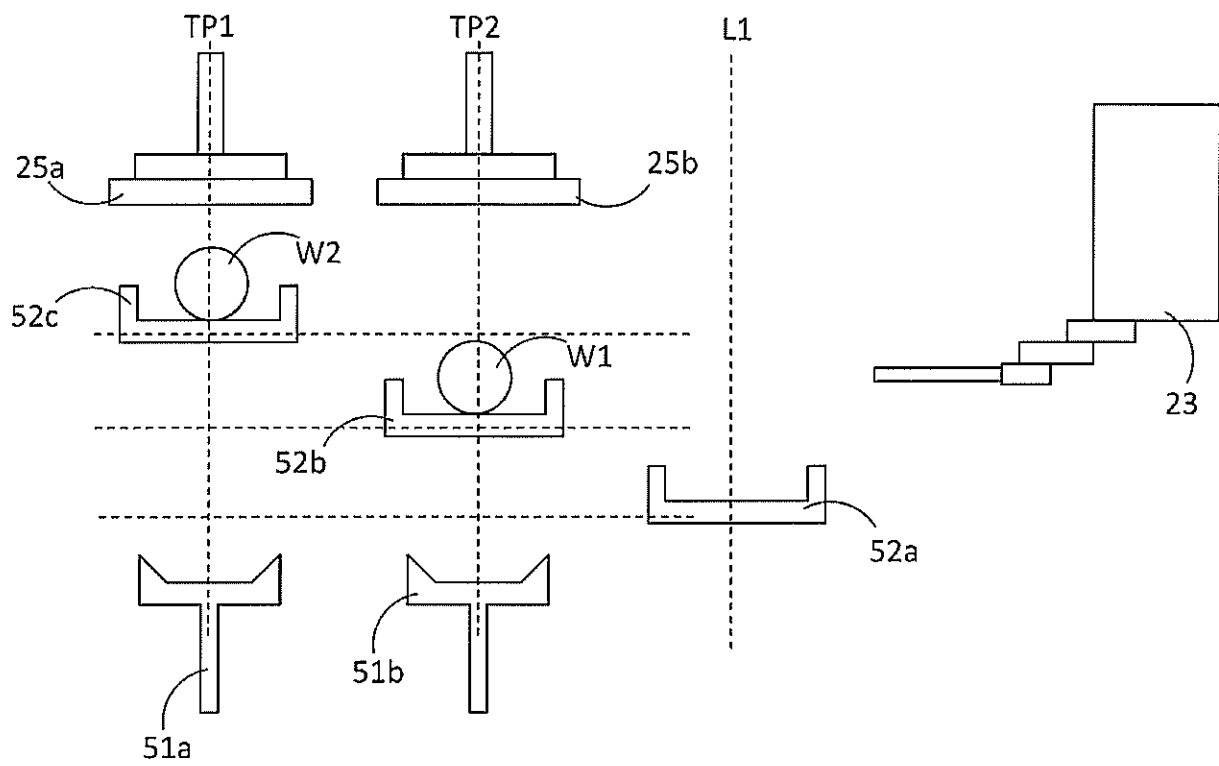


Fig.18J

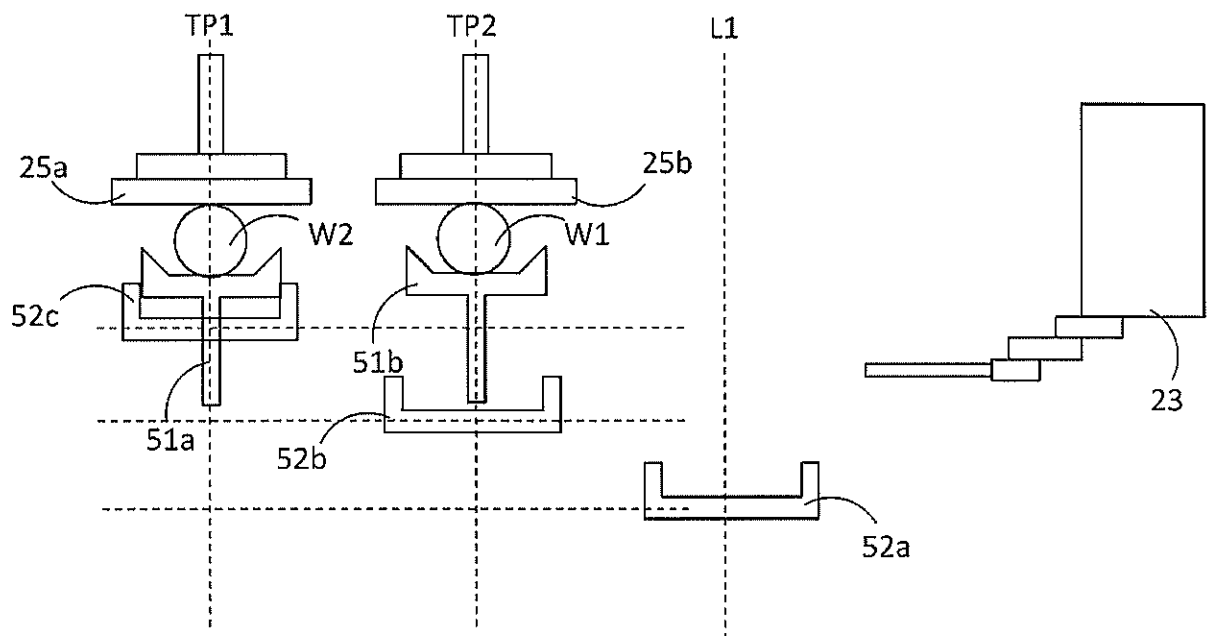


Fig.18K

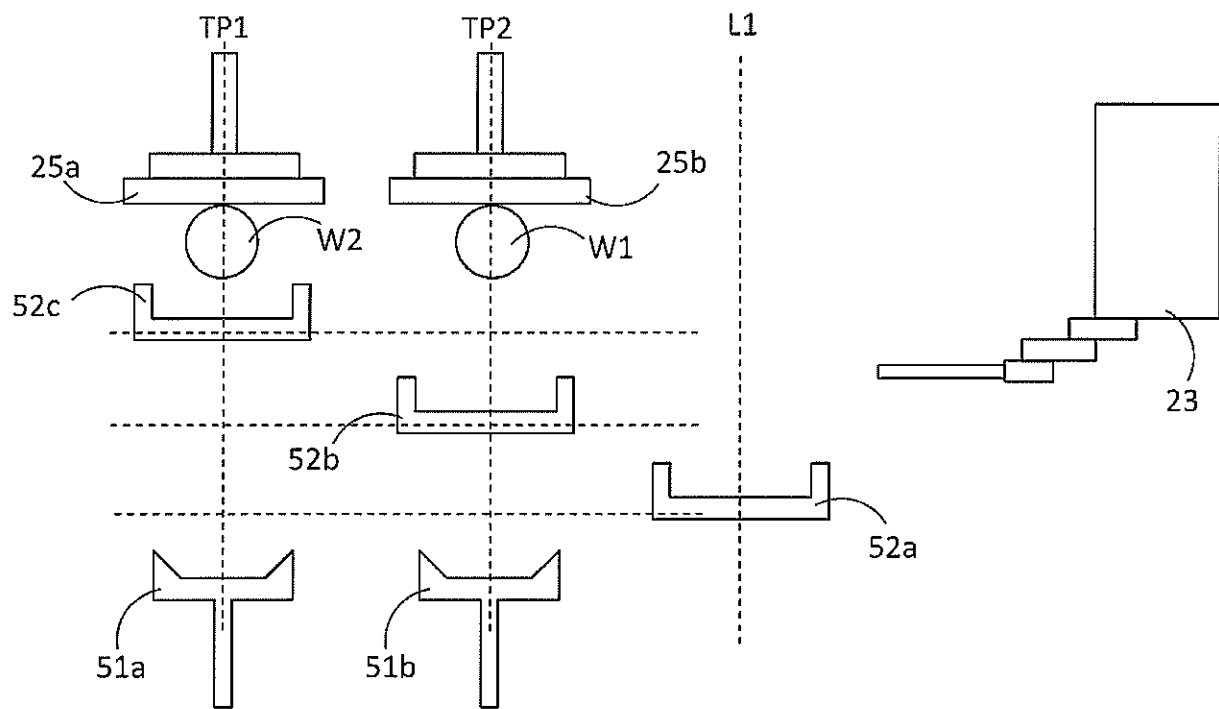


Fig.18L

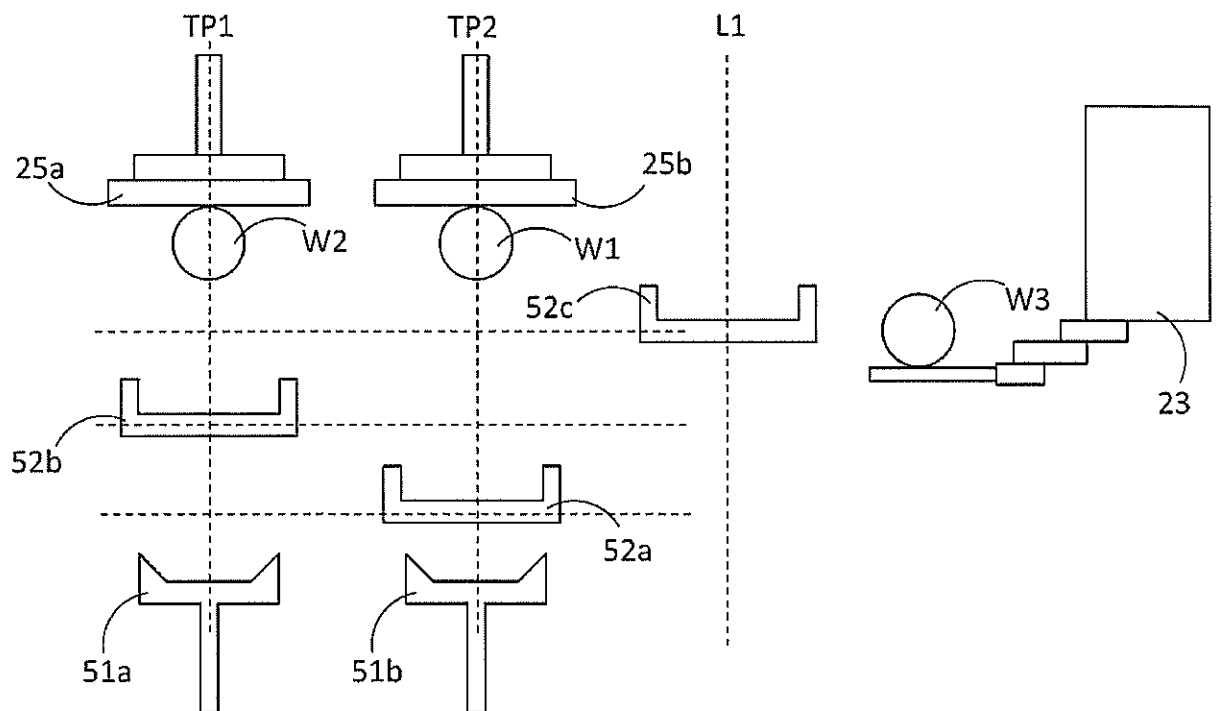


Fig.18M

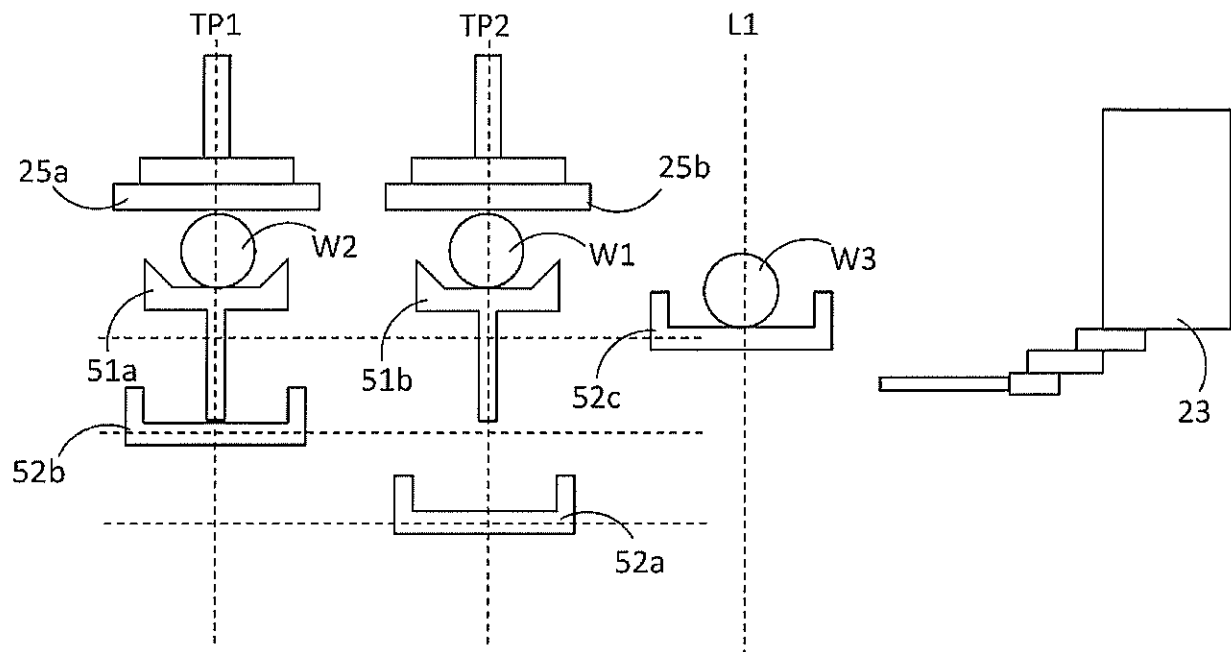


Fig.18N

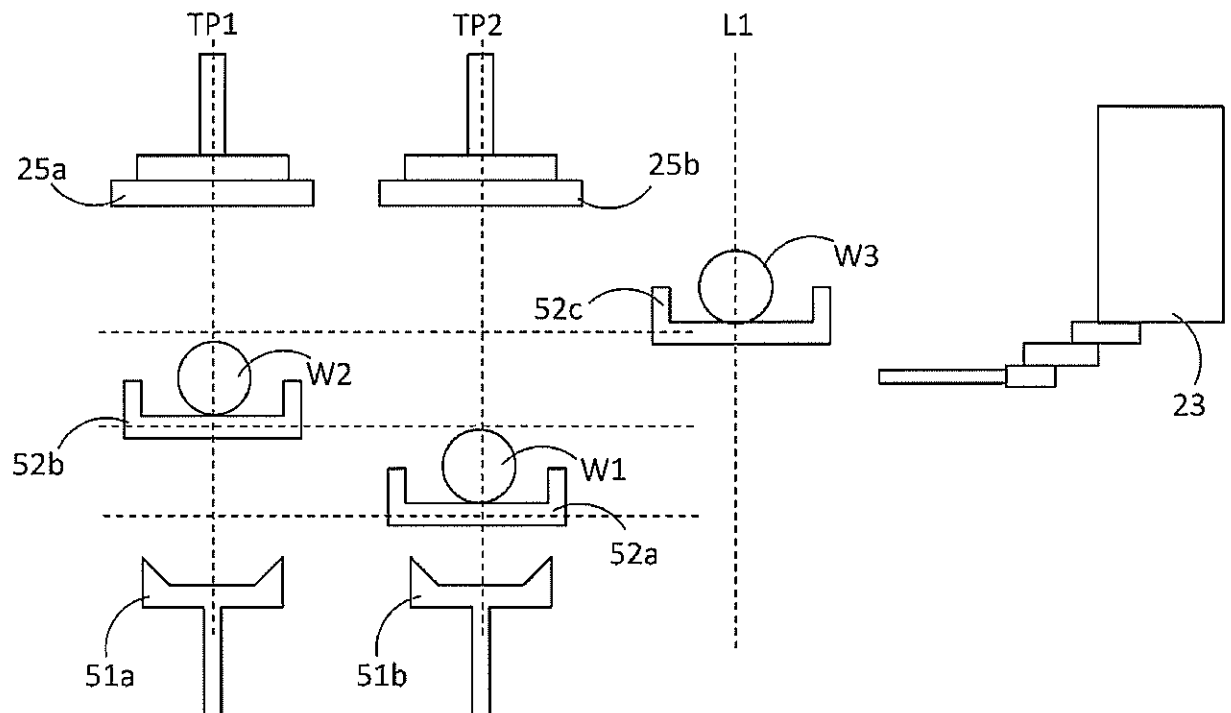


Fig.18O

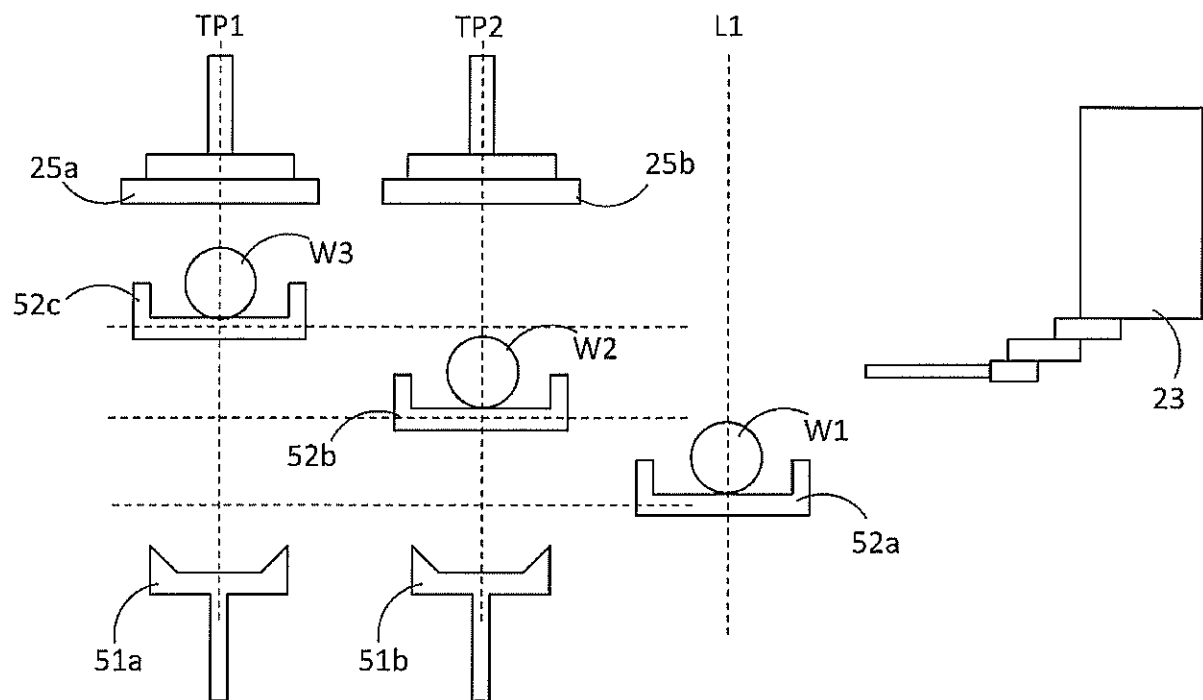


Fig.19A

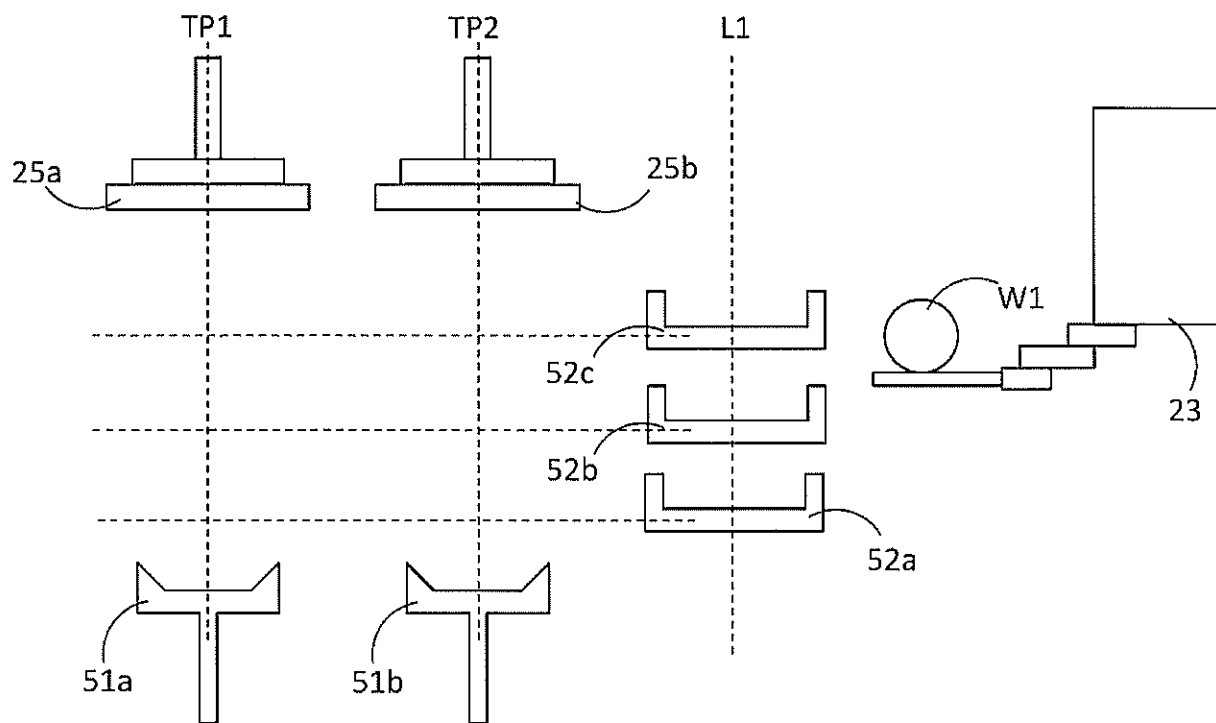


Fig.19B

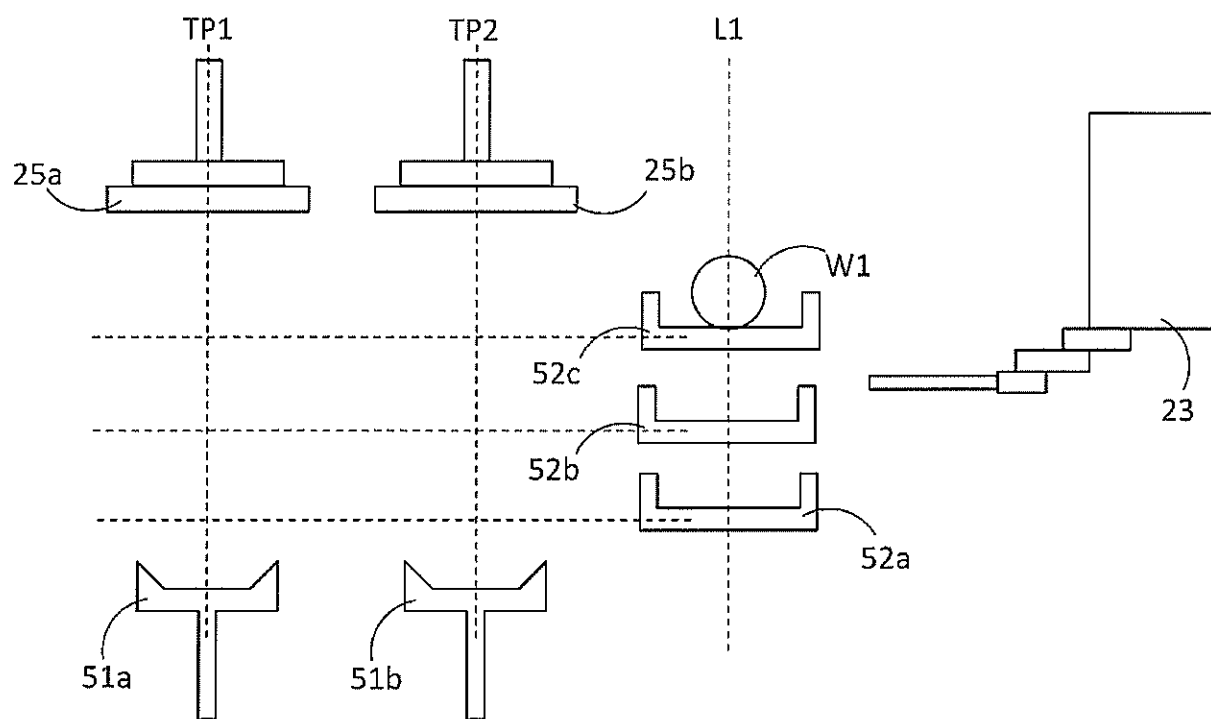


Fig.19C

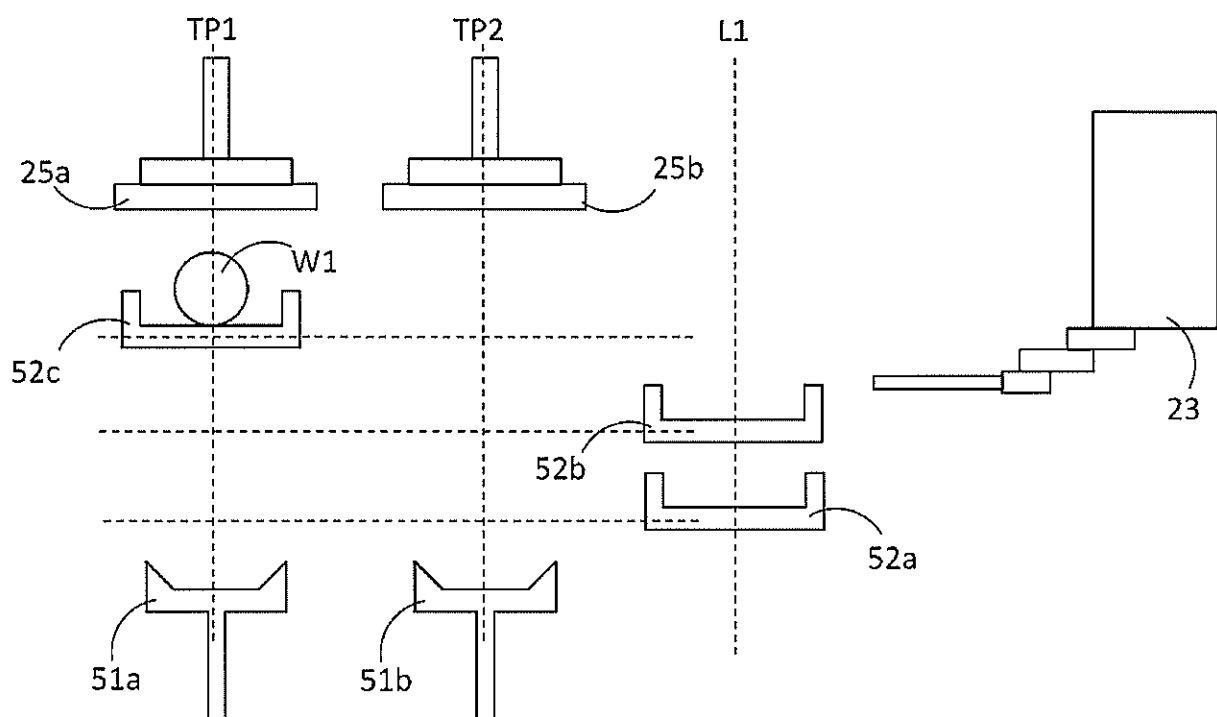


Fig.19D

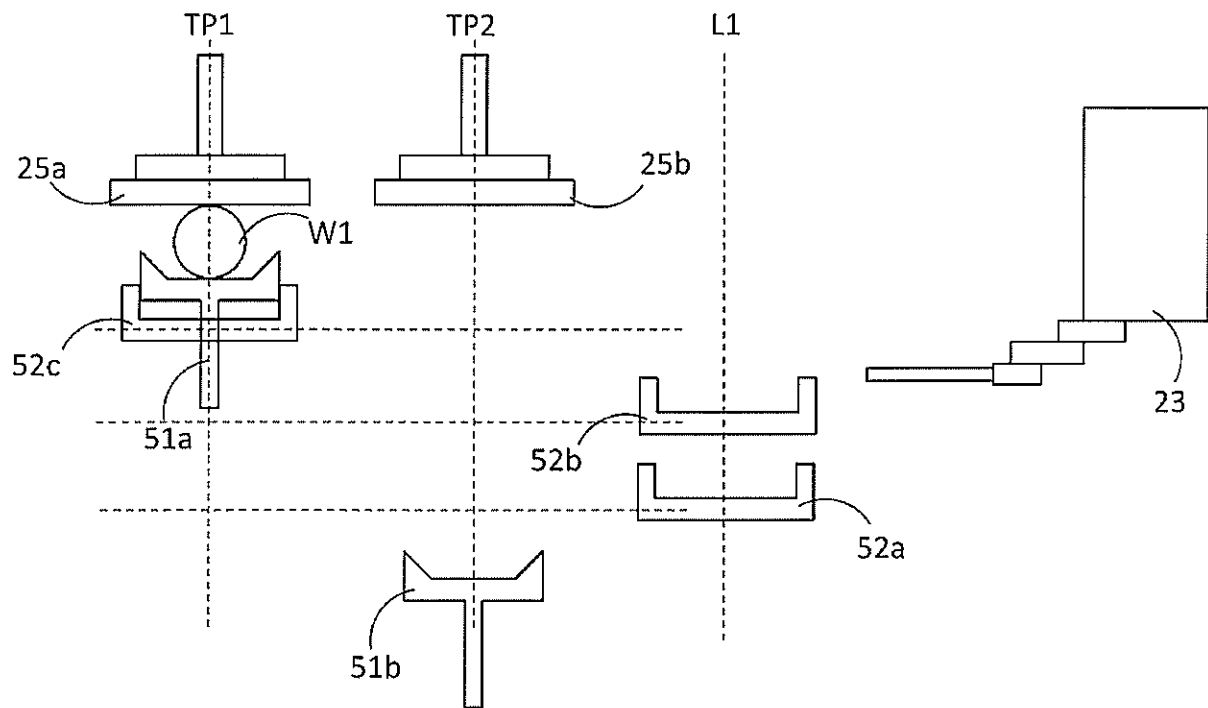


Fig.19E

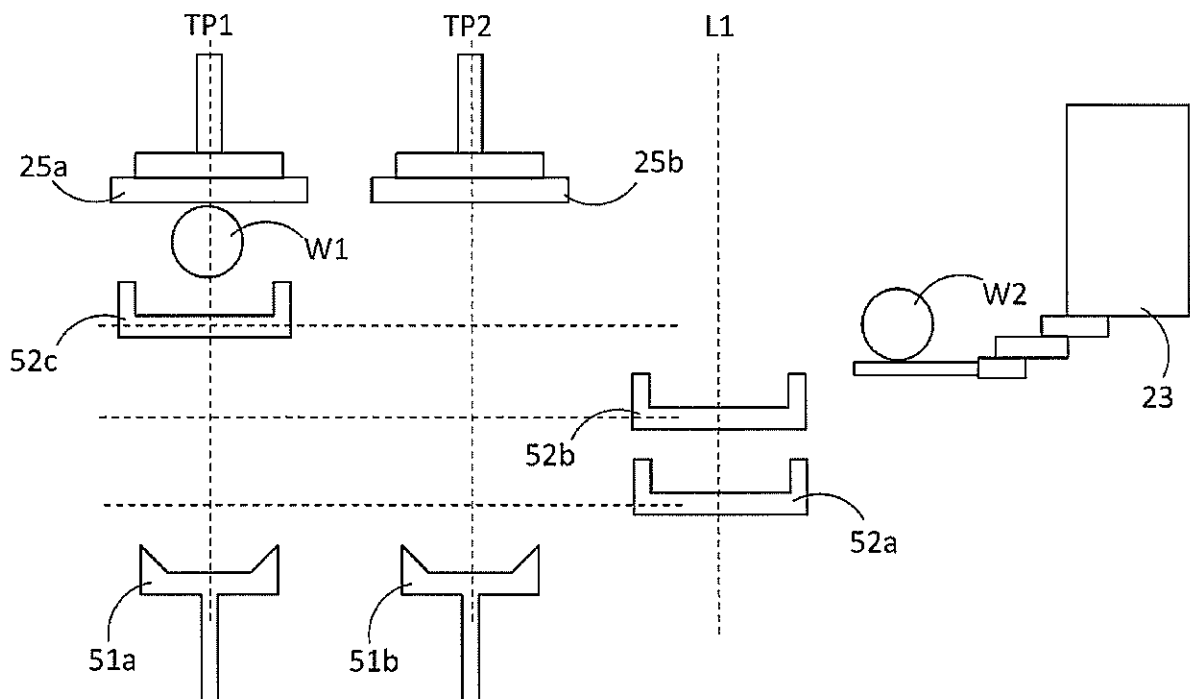


Fig.19F

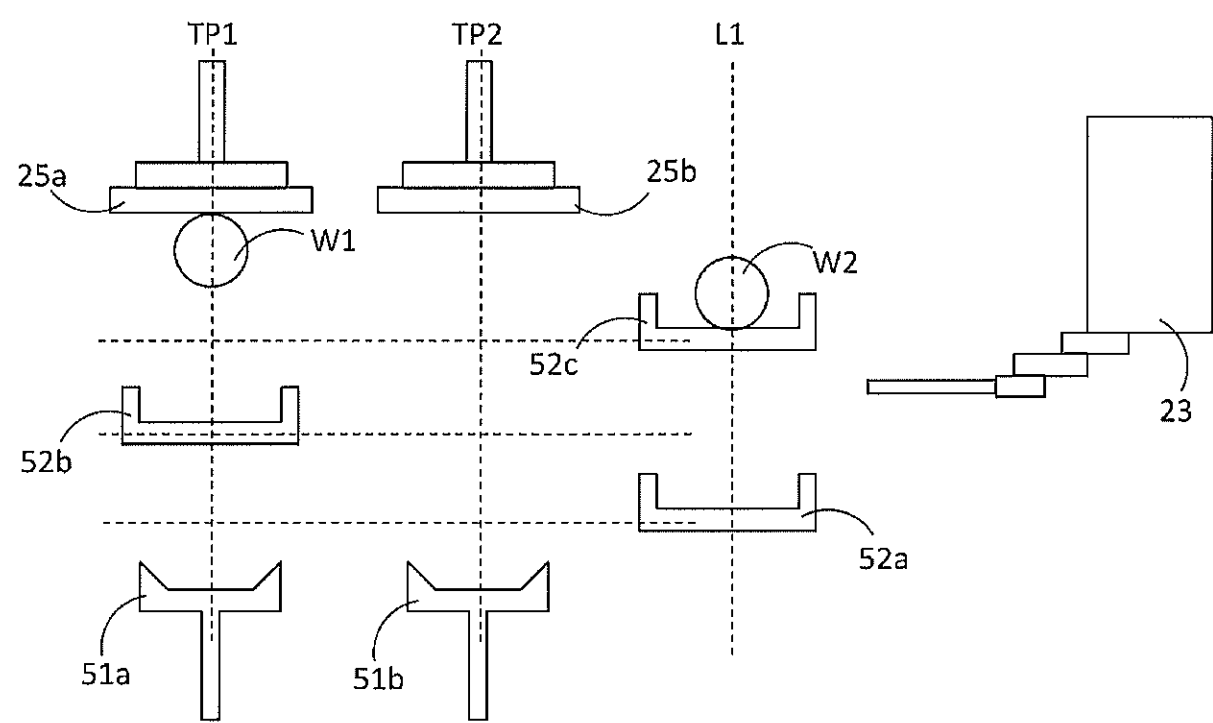


Fig.19G

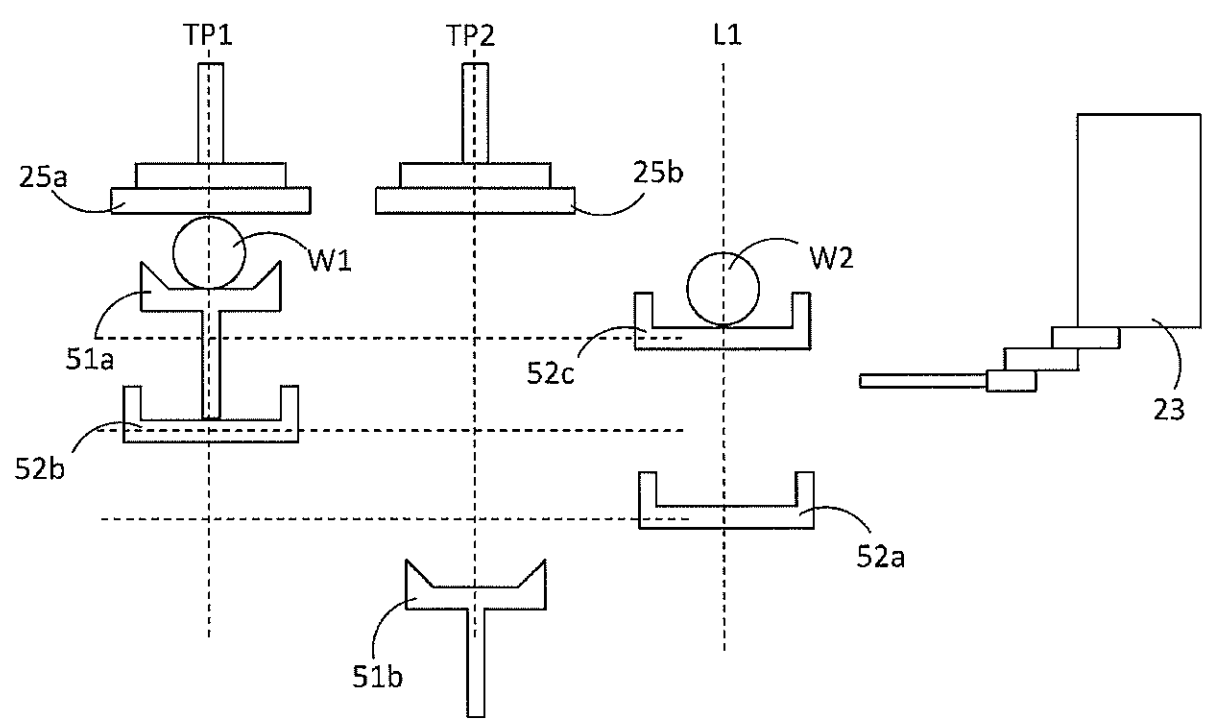


Fig.19H

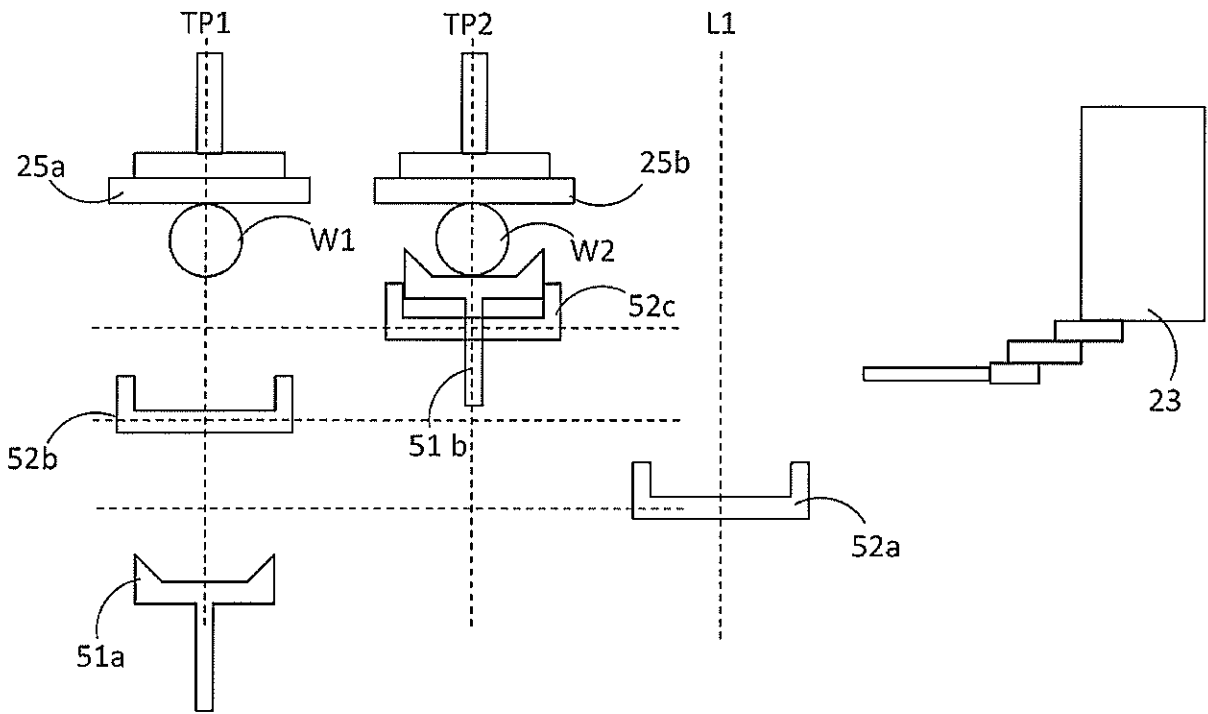


Fig.19I

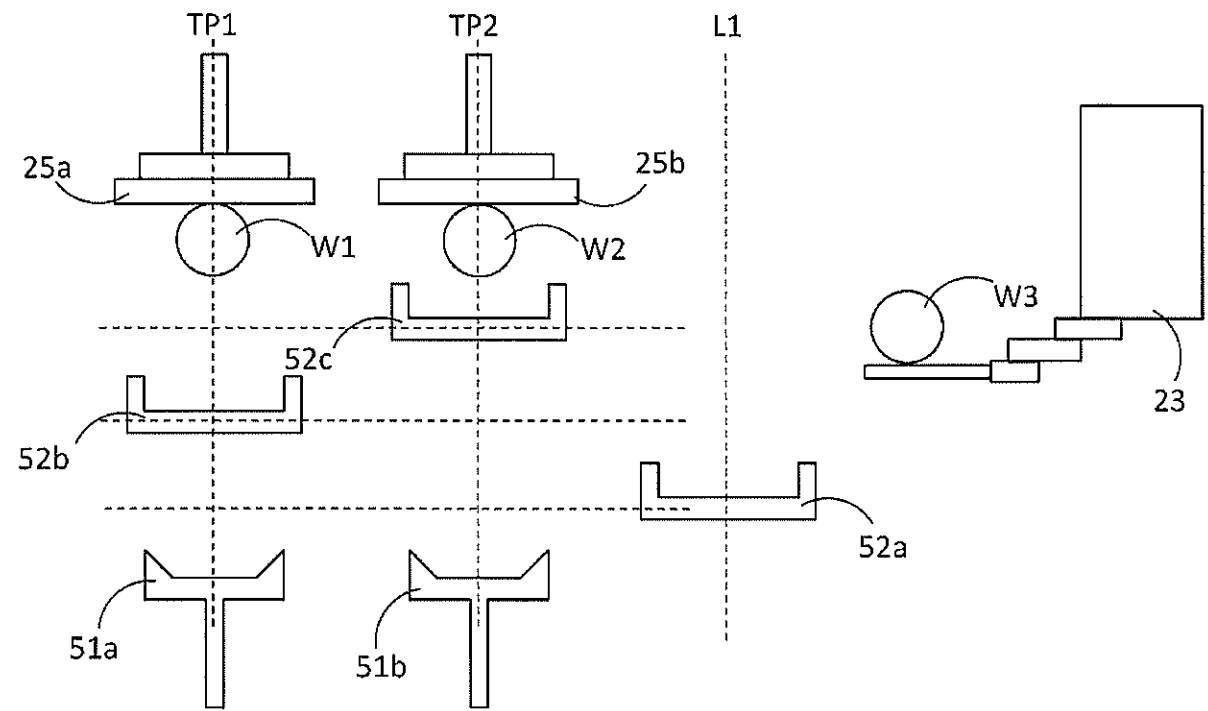


Fig.19J

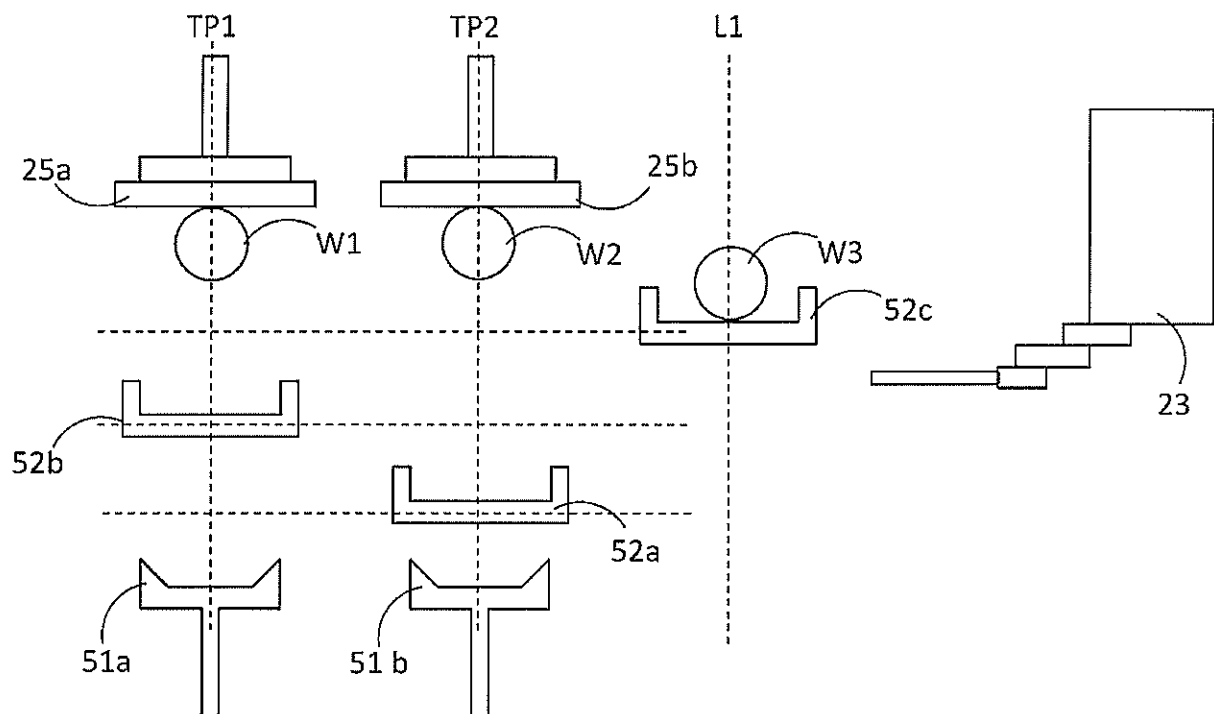


Fig.19K

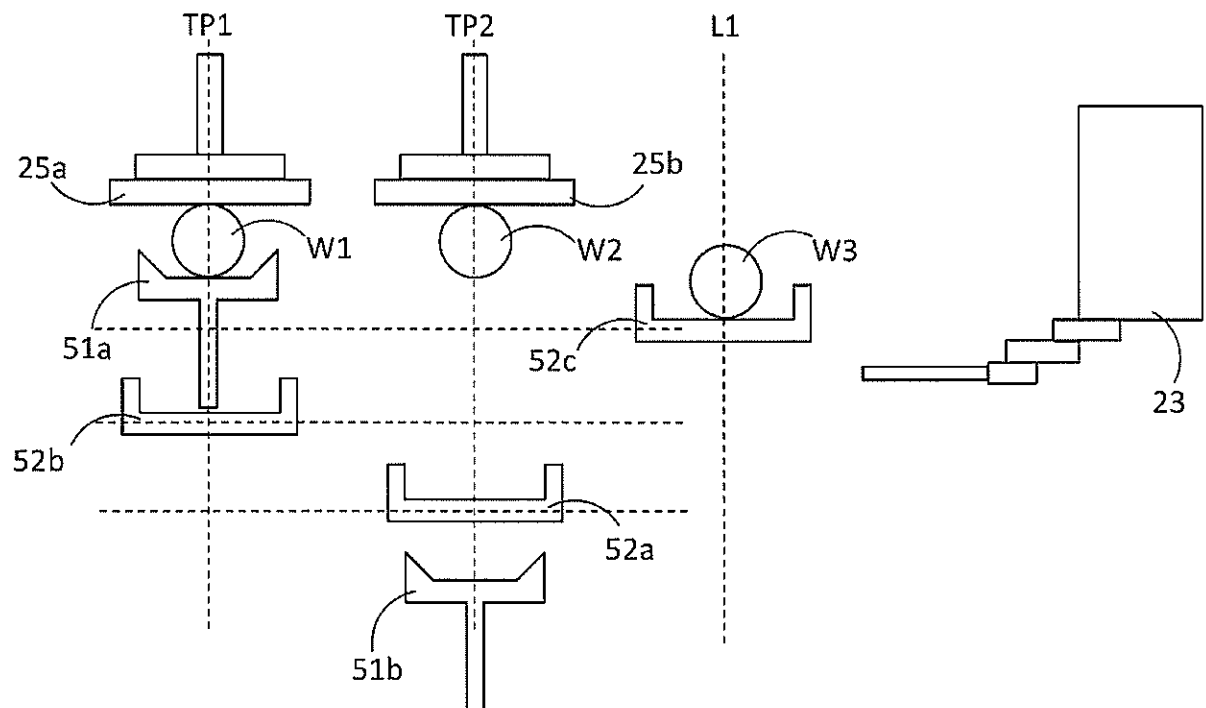


Fig.19L

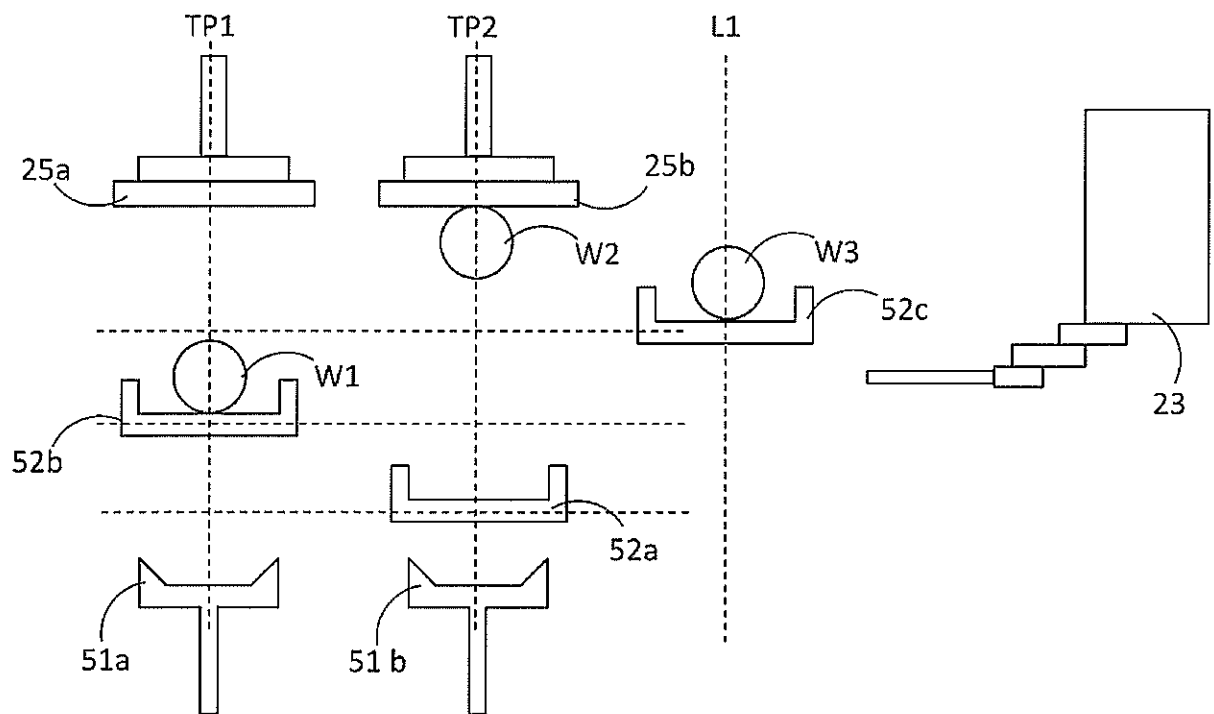


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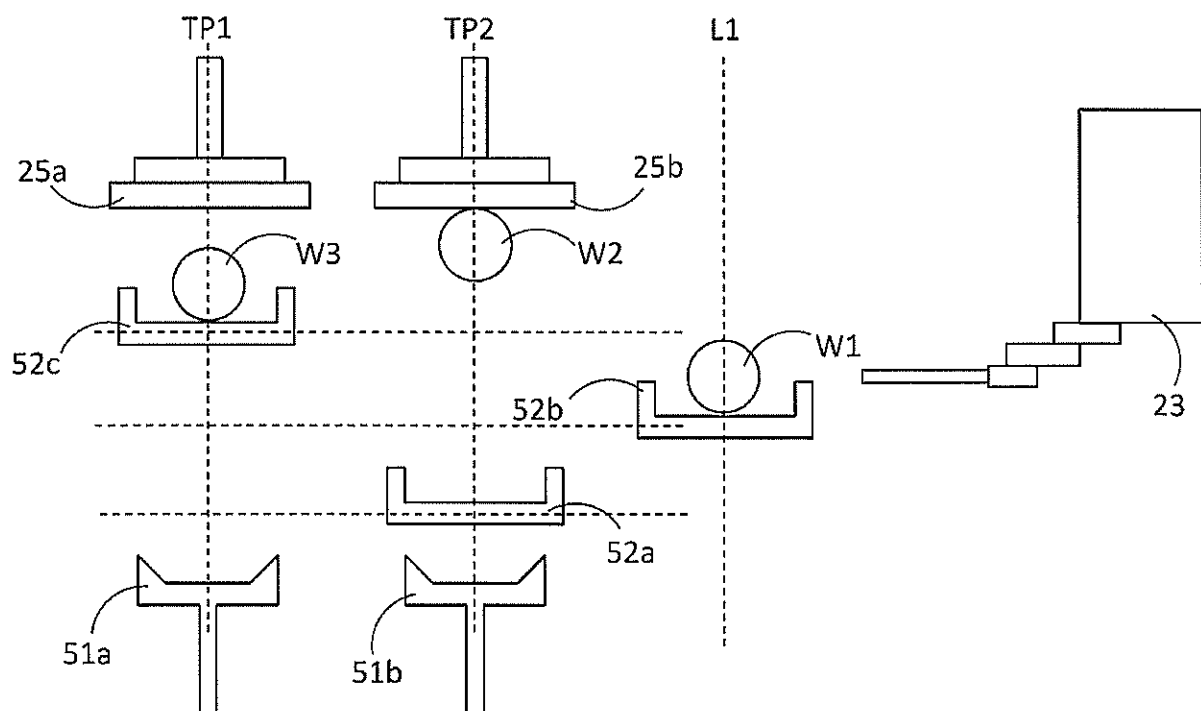


Fig.19N

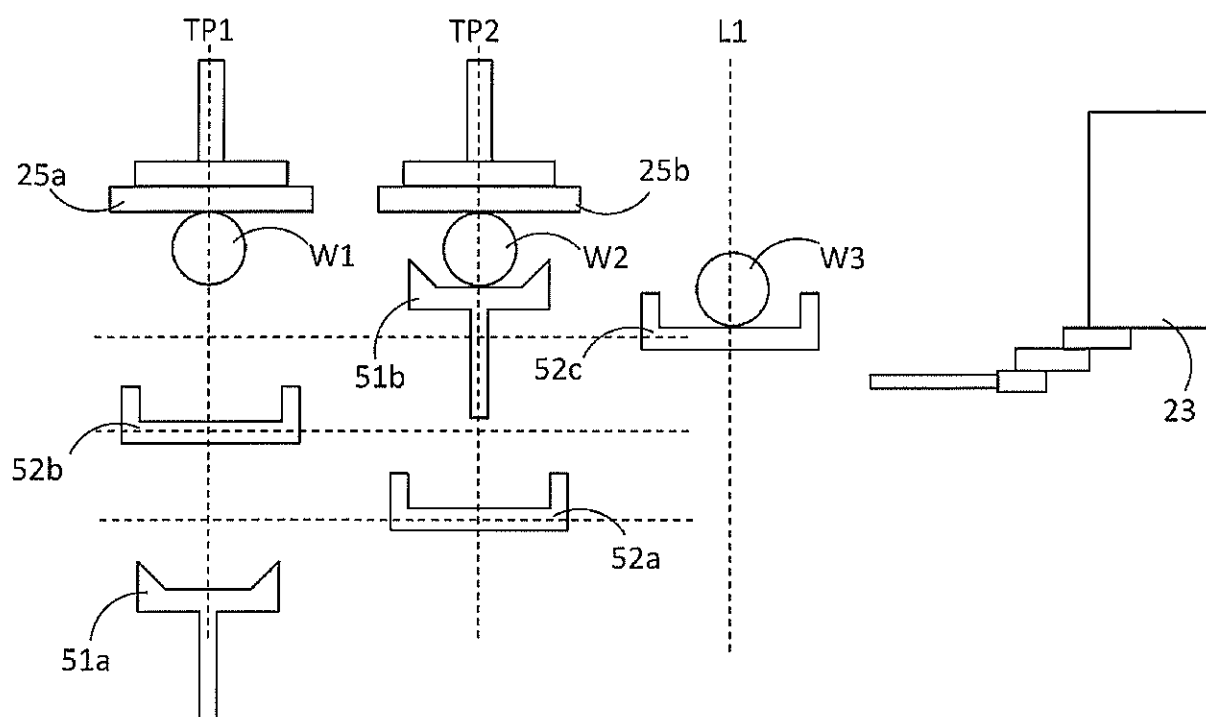


Fig.19O

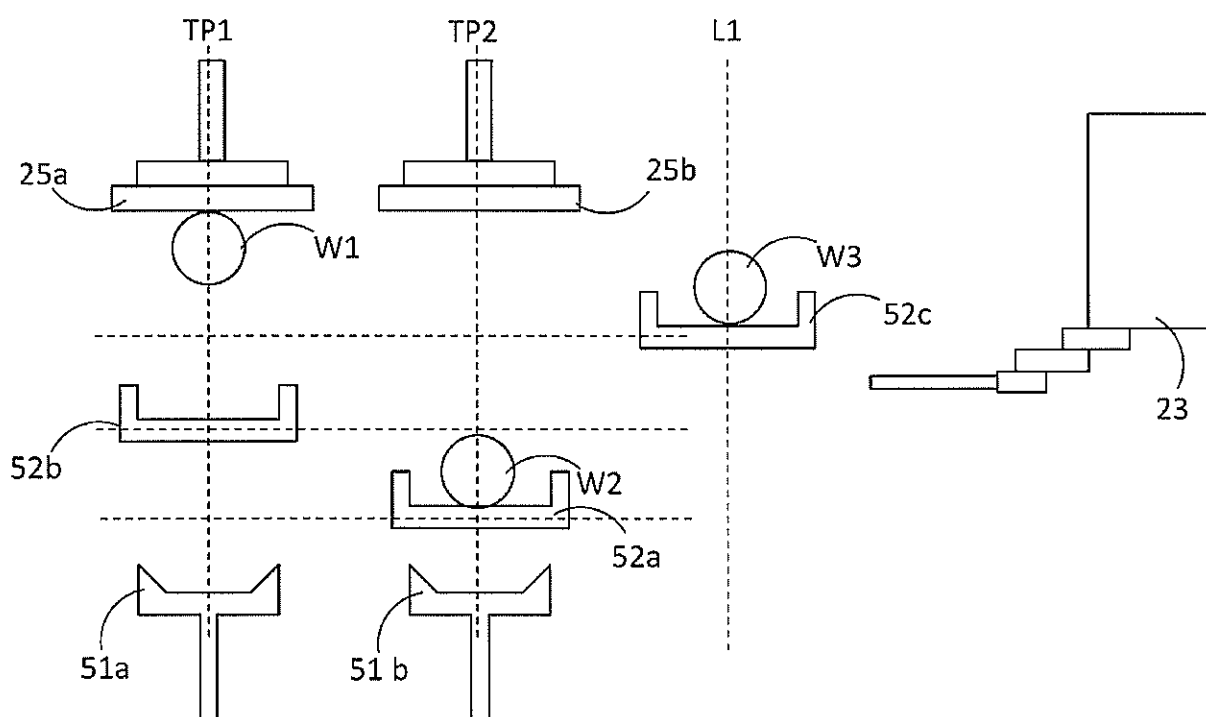


Fig.19P

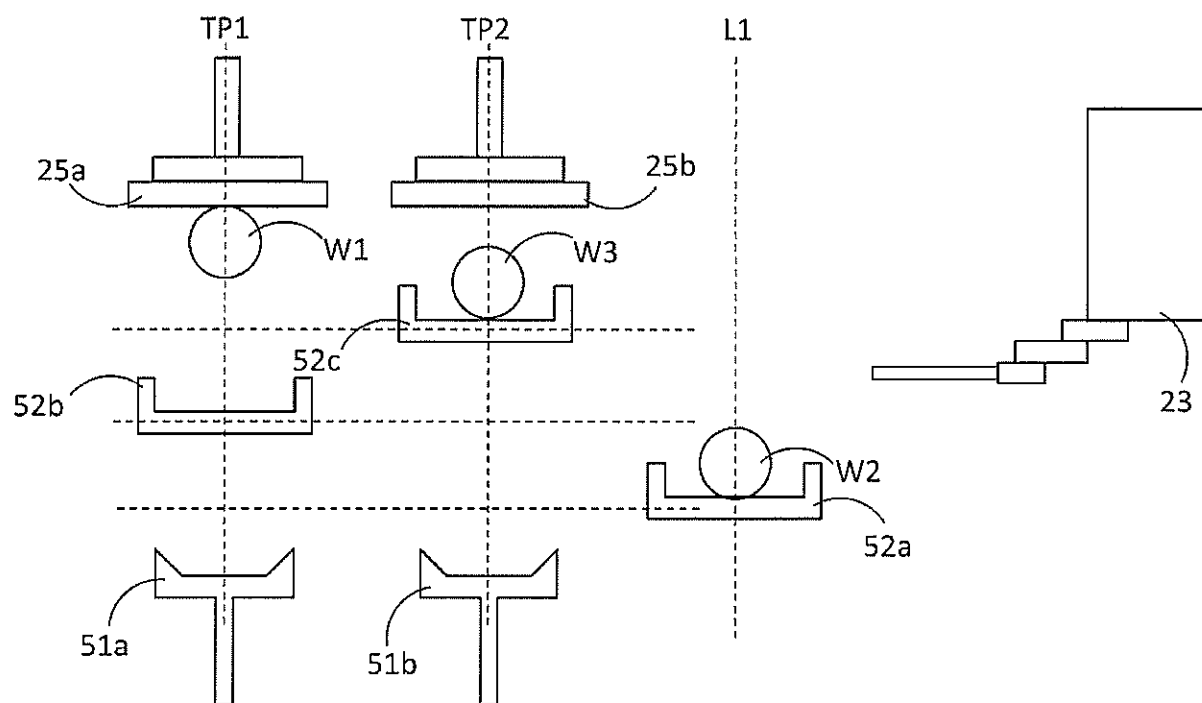


Fig.20A

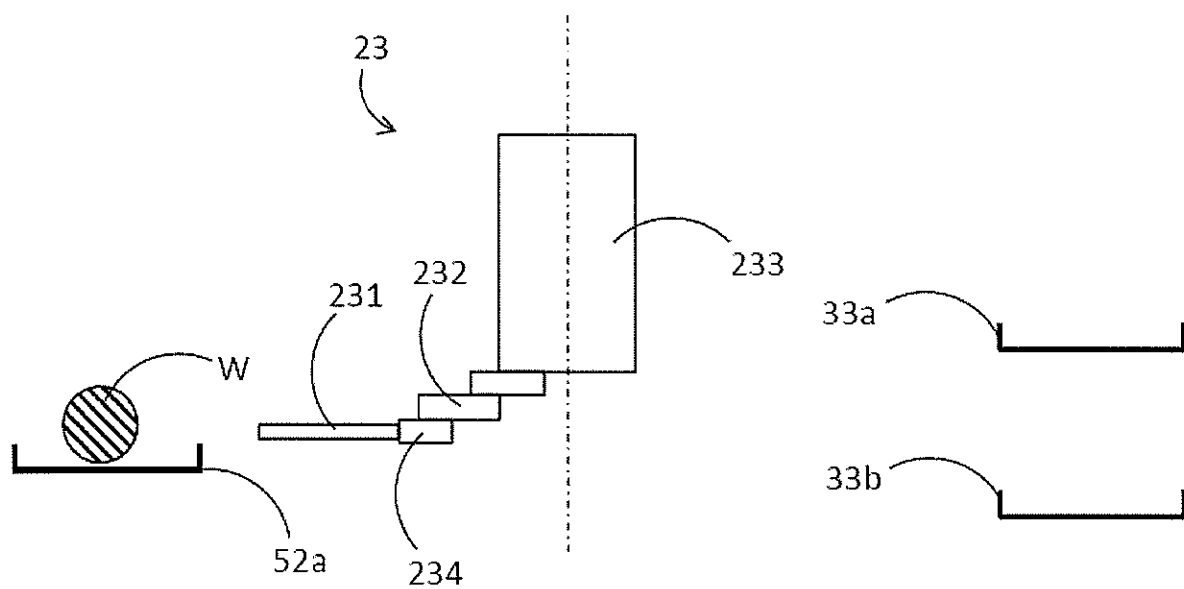


Fig.20B

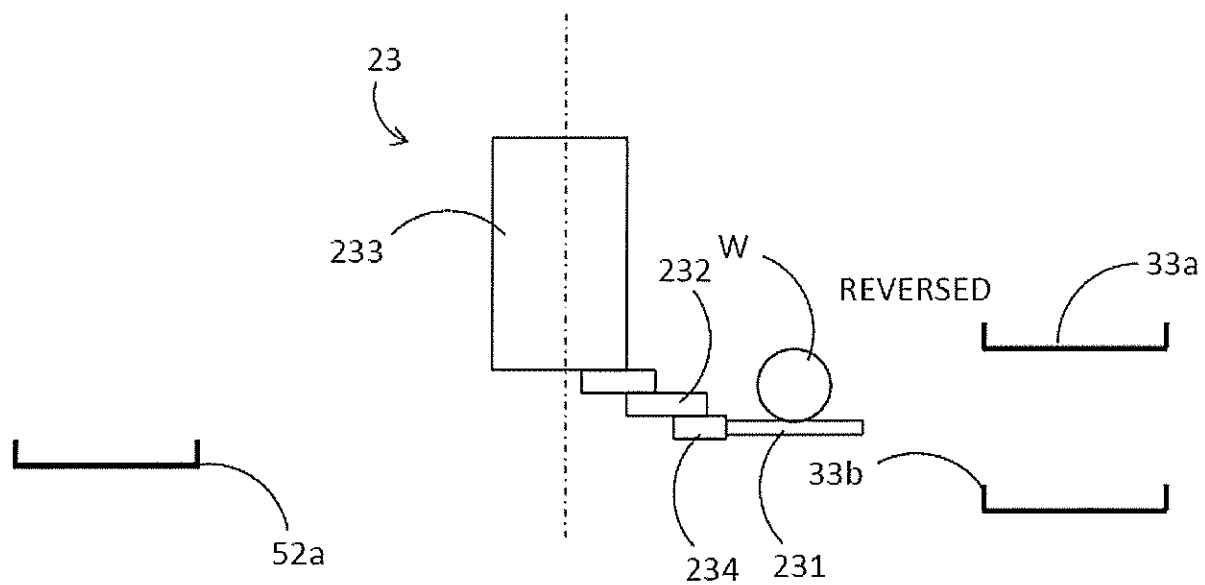


Fig.20C

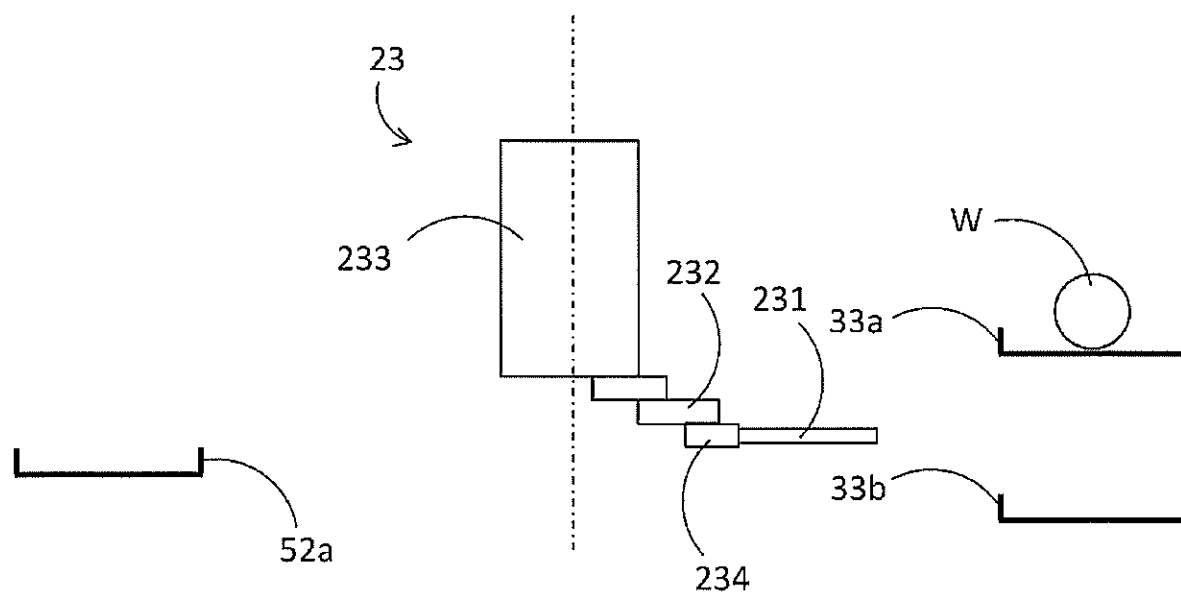


Fig.21A

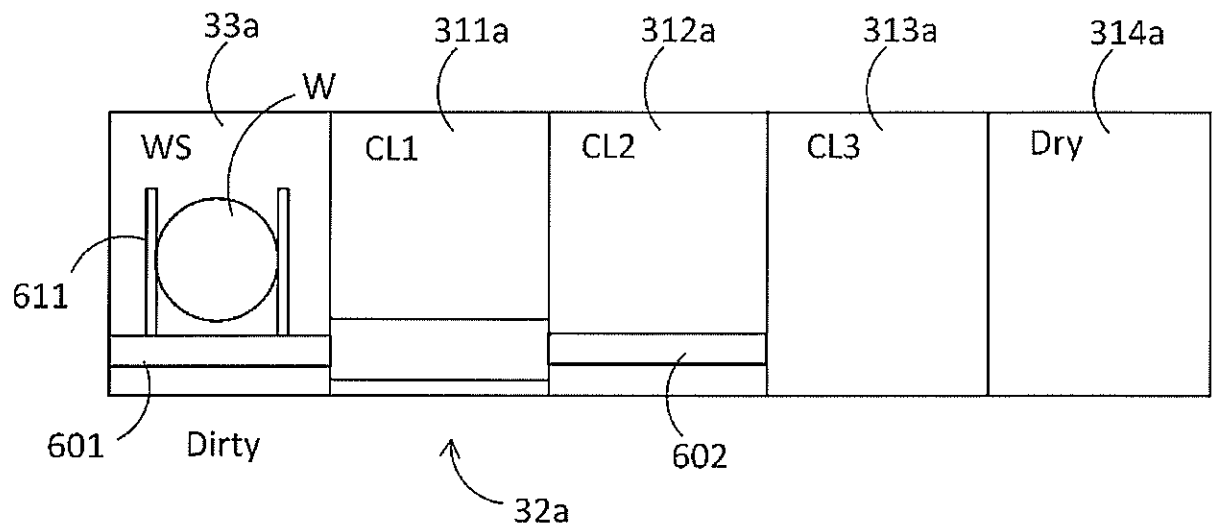


Fig.21B

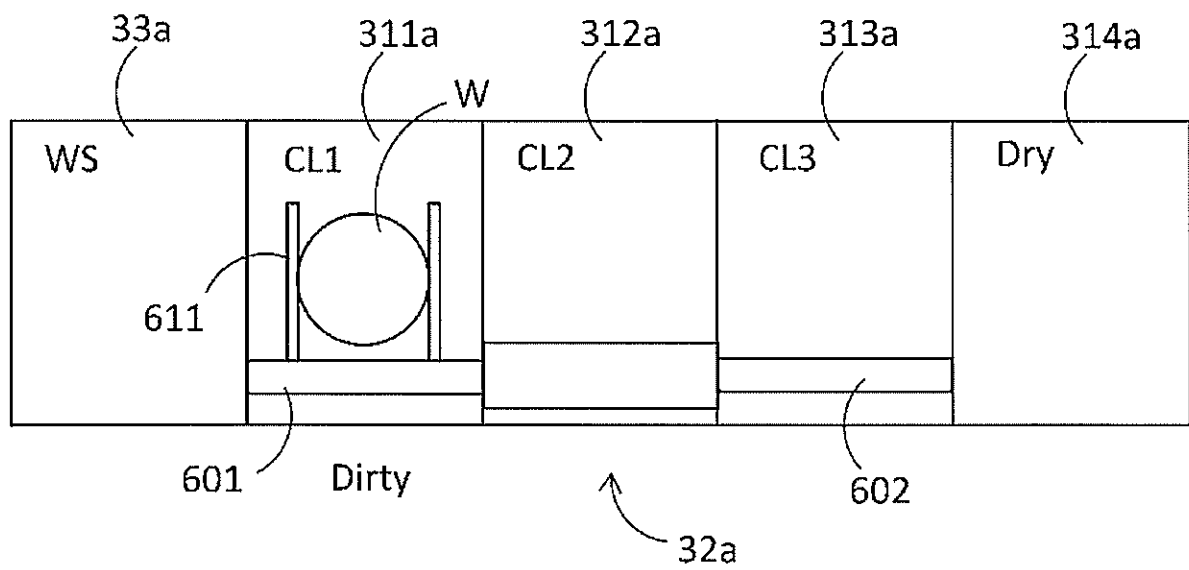


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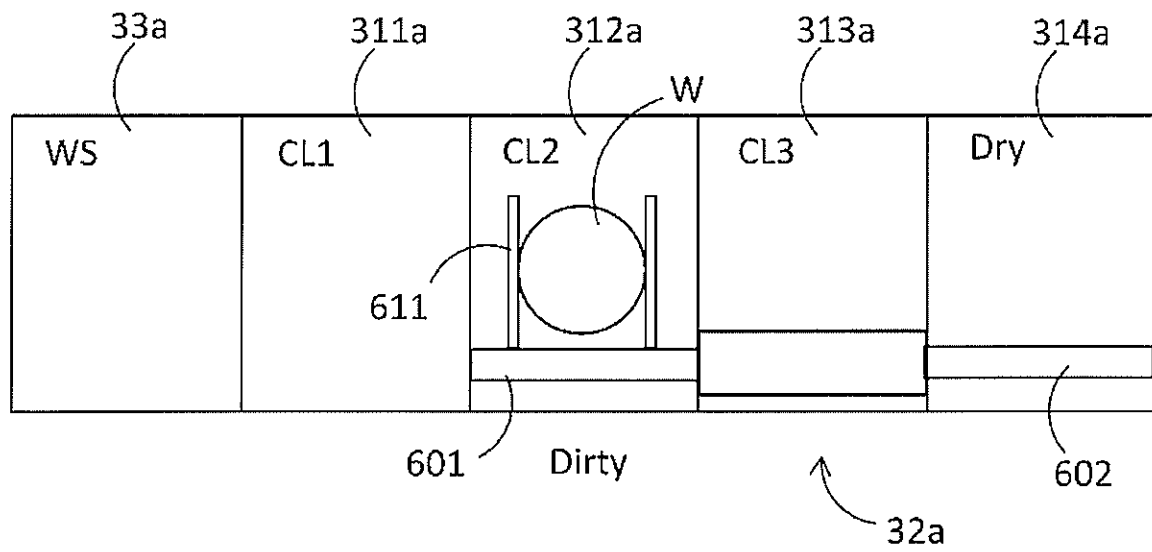


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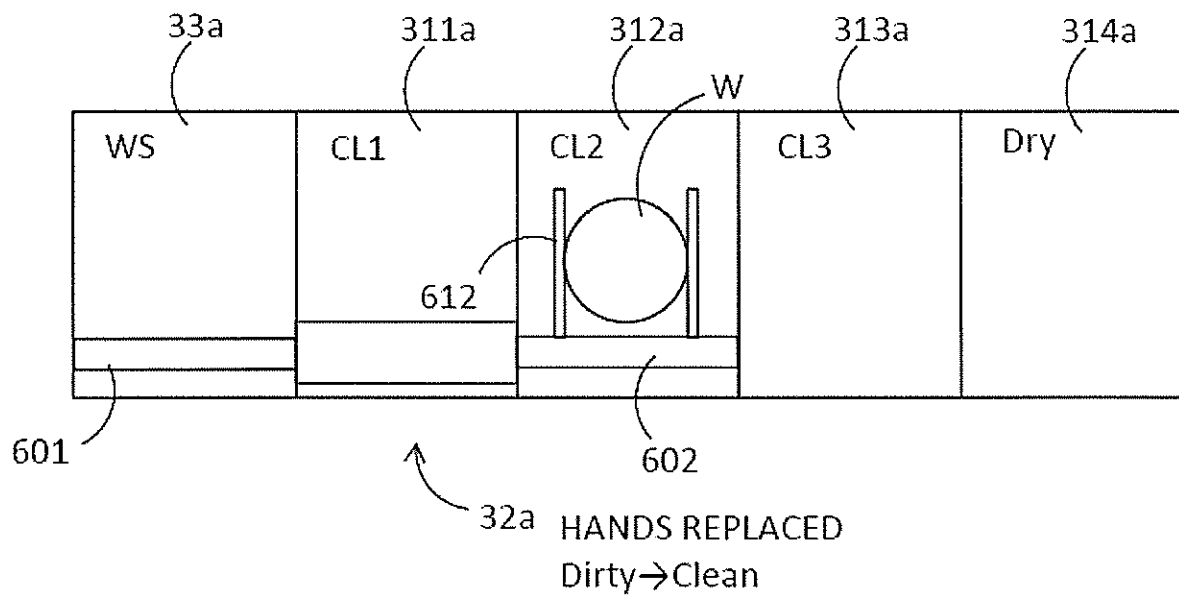


Fig.21E

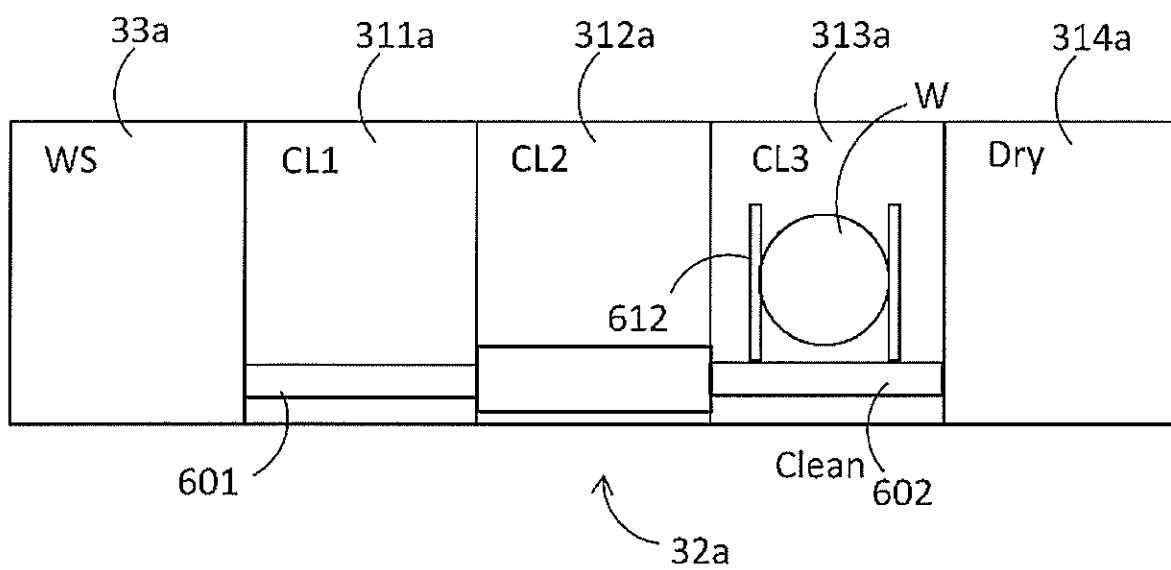


Fig.21F

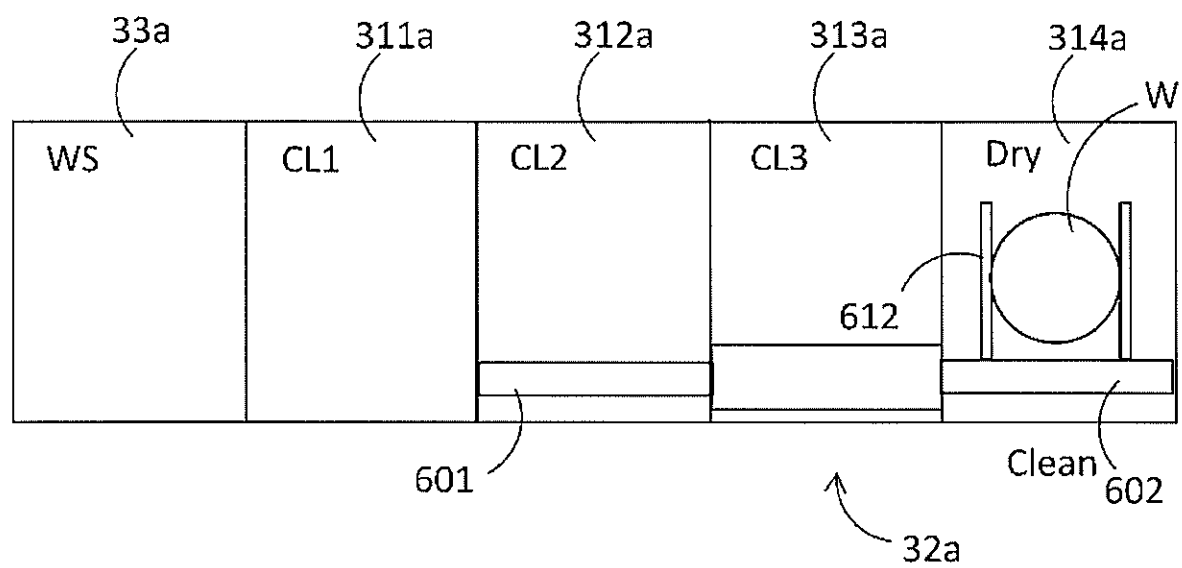


Fig.22A

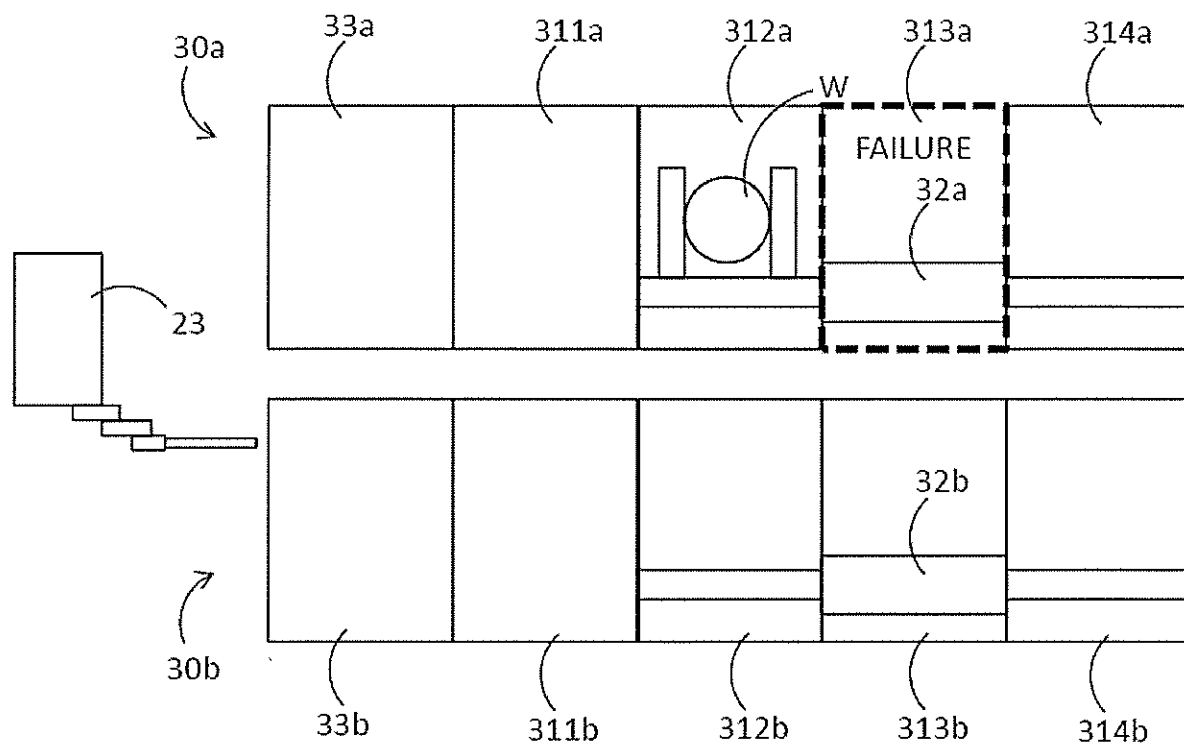


Fig.22B

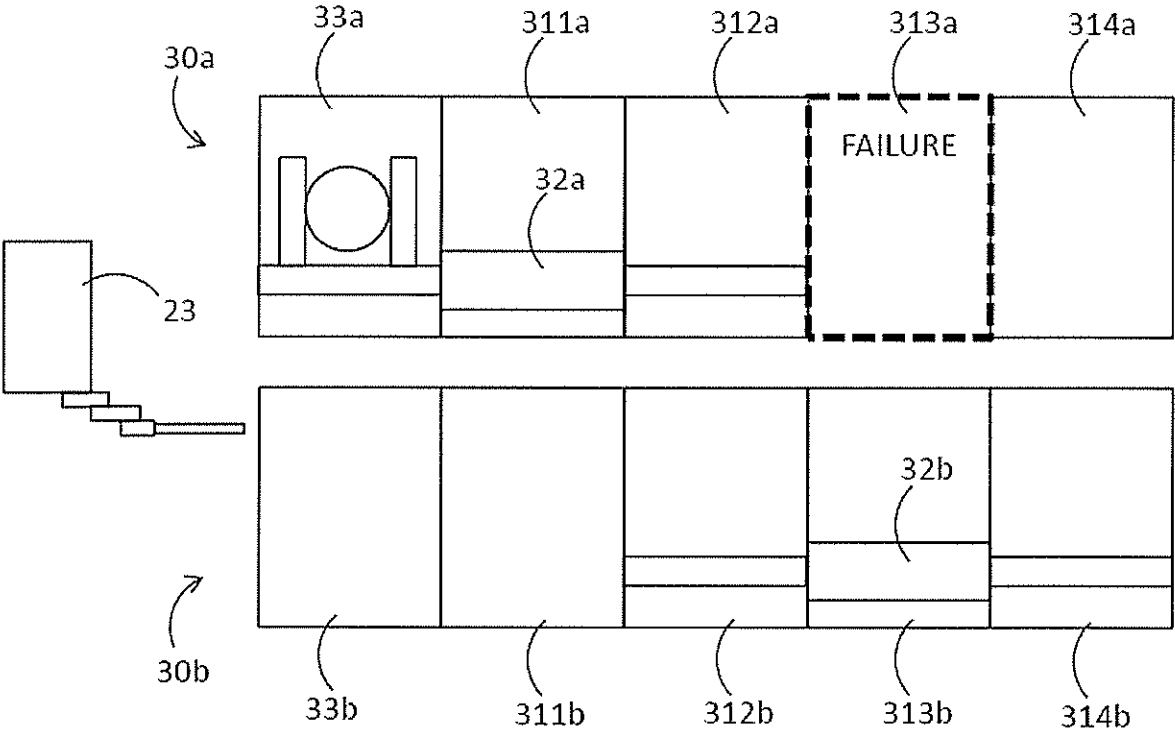


Fig.22C

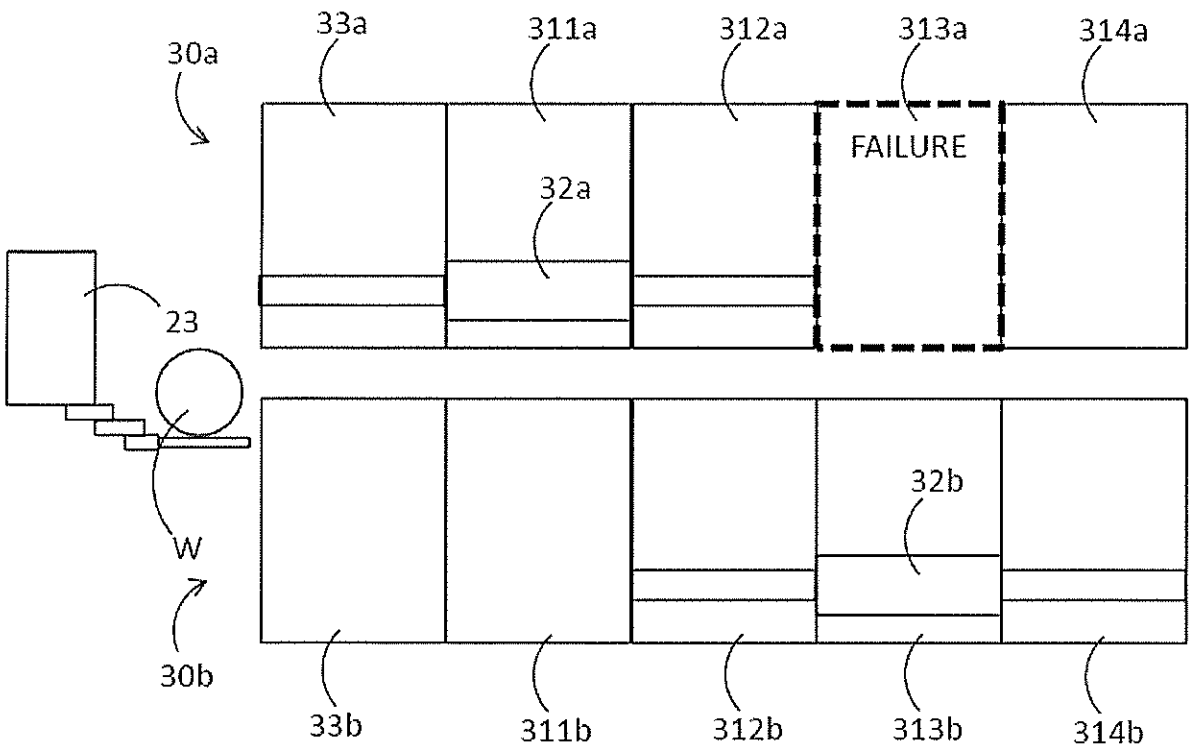


Fig.22D

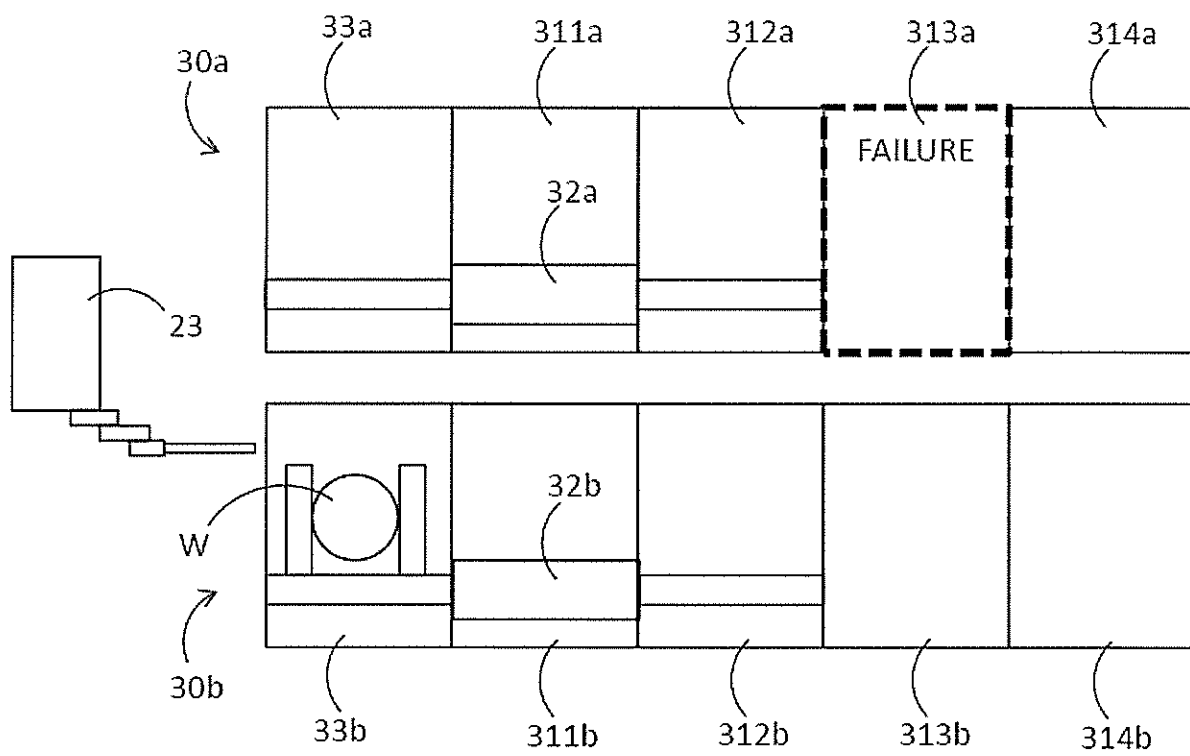


Fig.22E

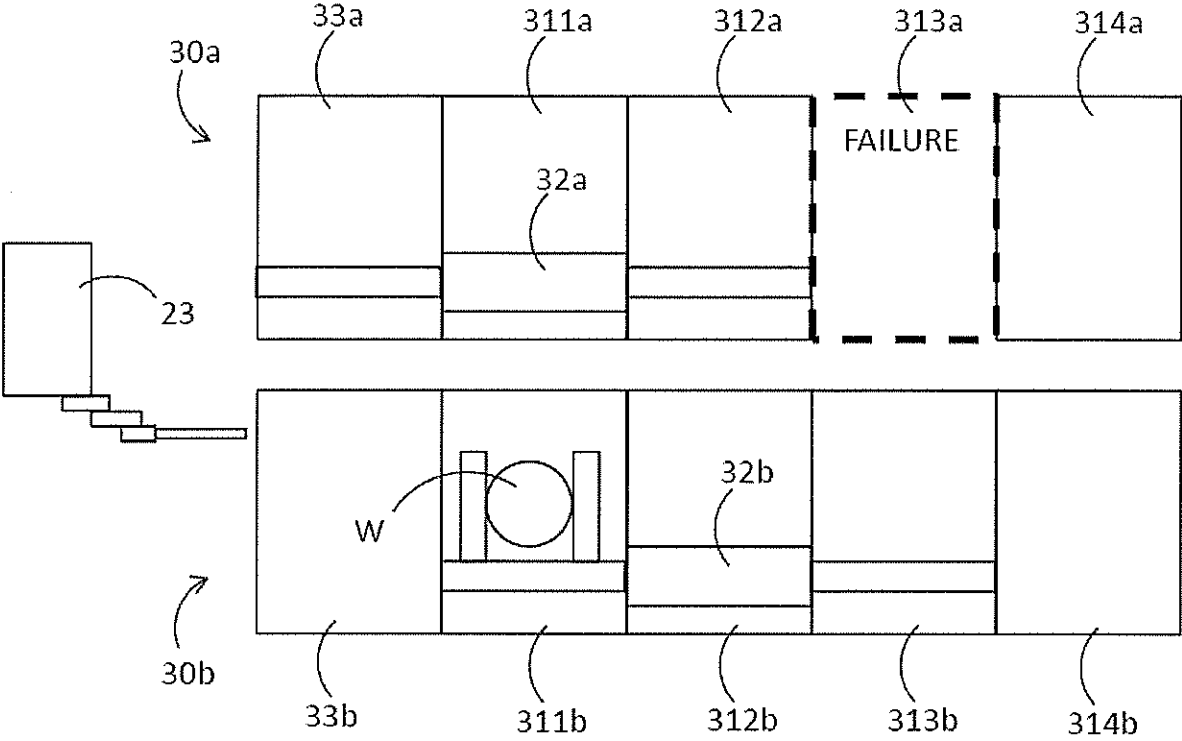


Fig.23

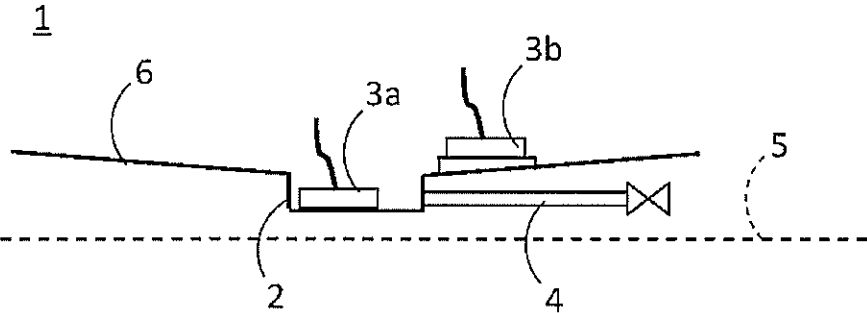


Fig.24

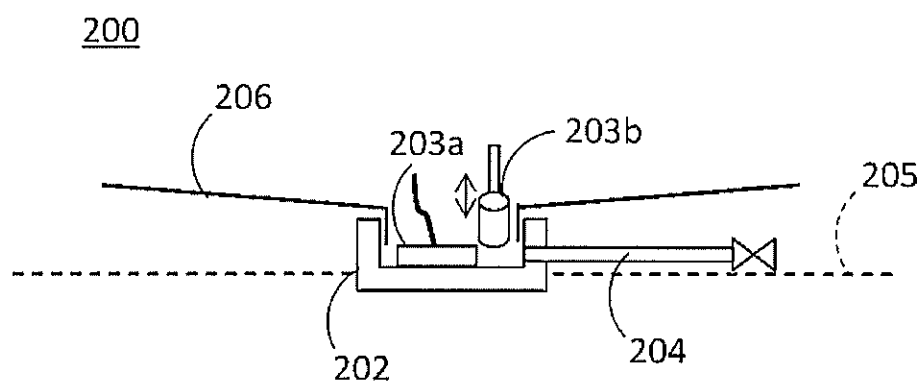


Fig.25

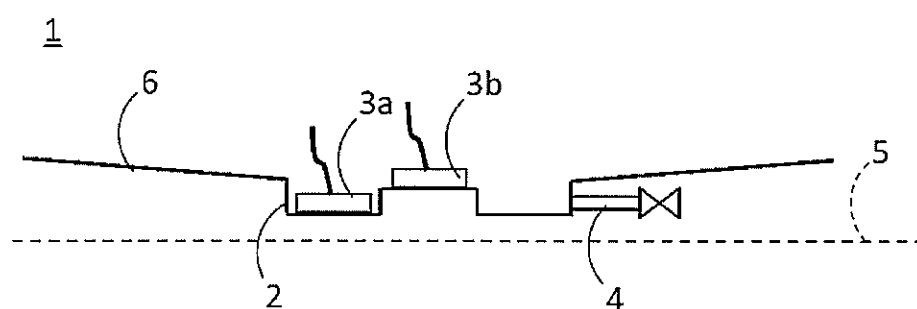


Fig.26

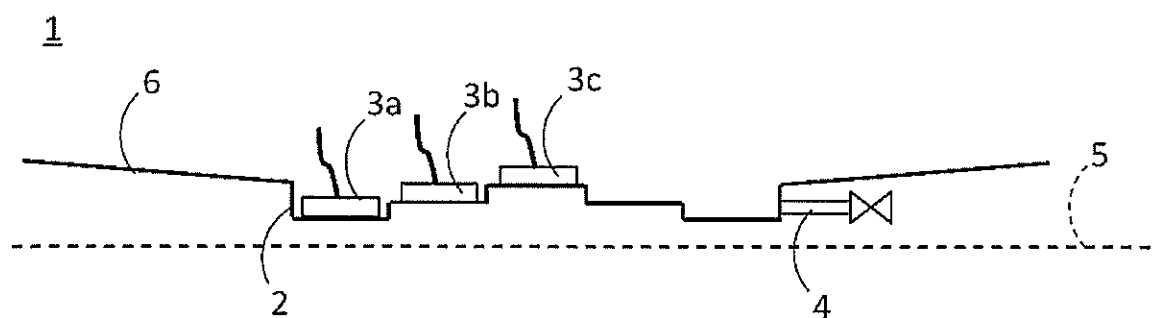


Fig.27

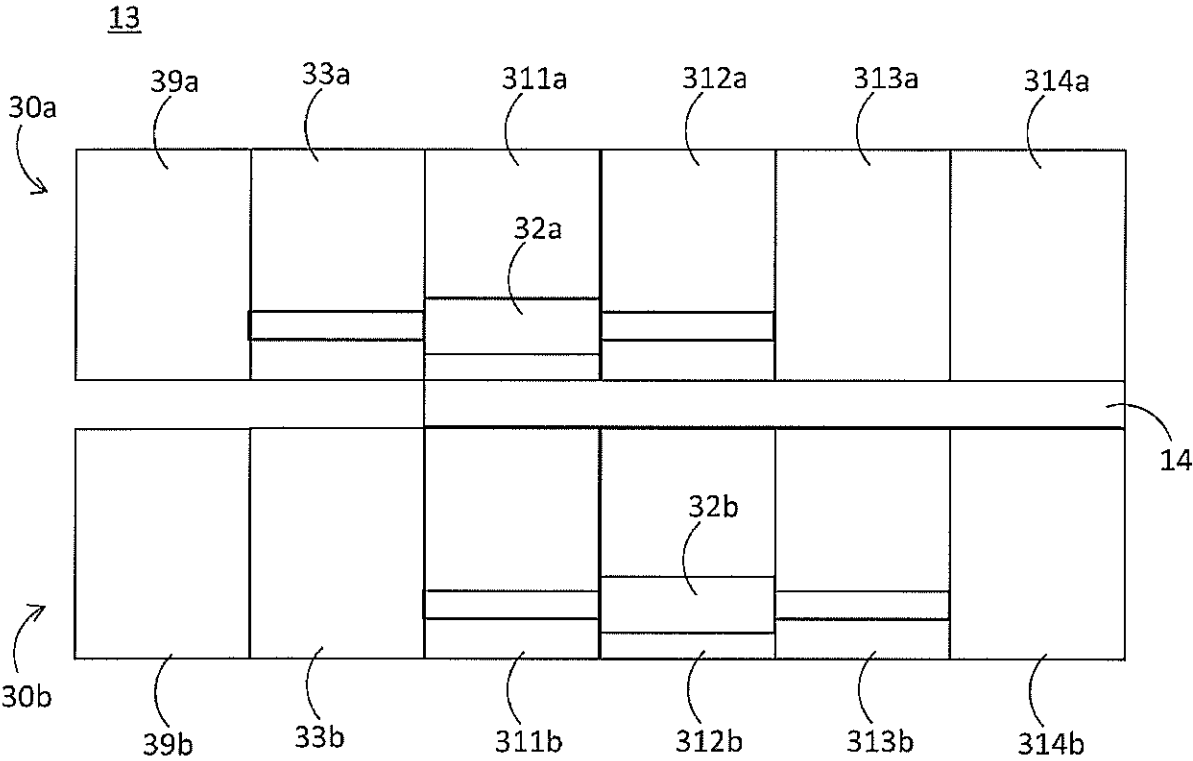


Fig.28A

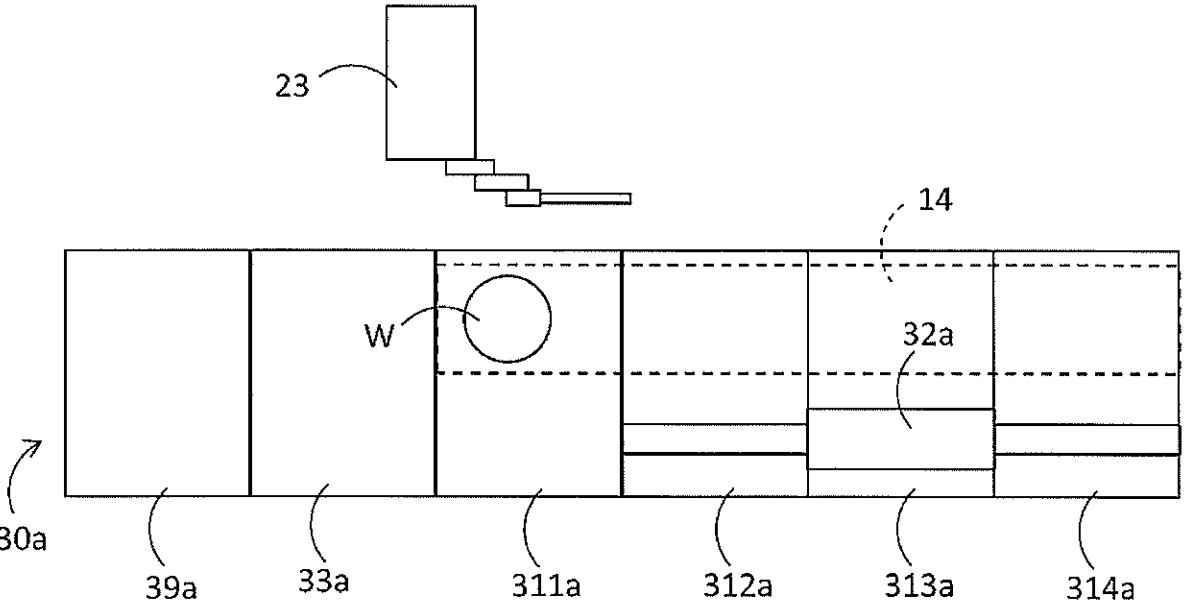


Fig.28B

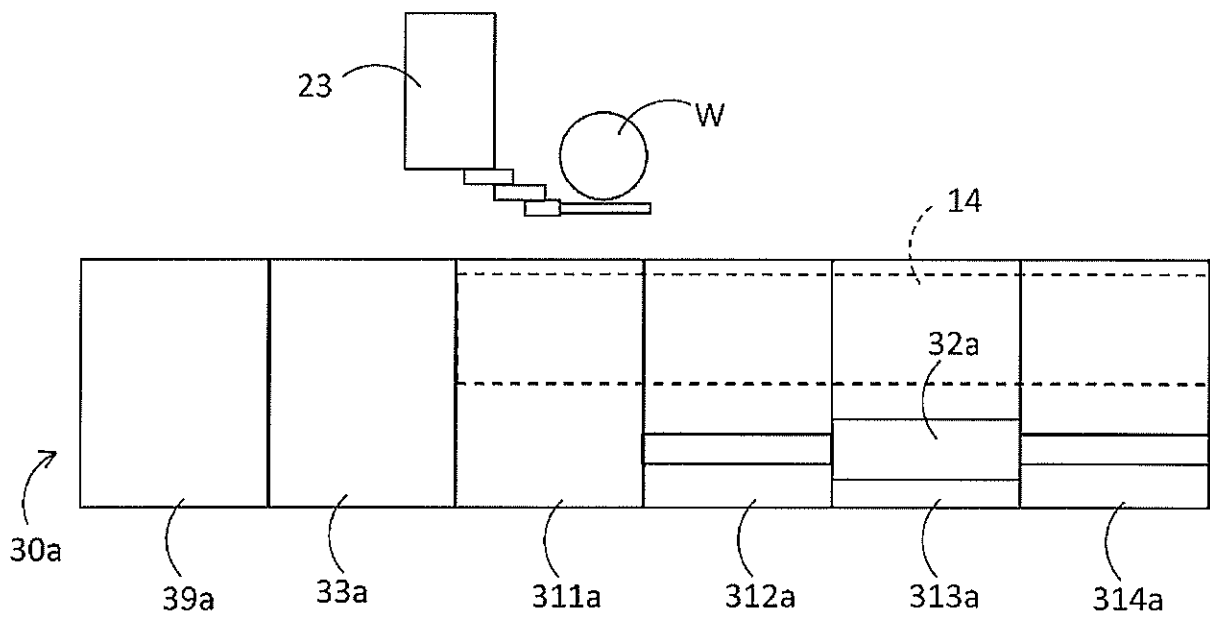


Fig.28C

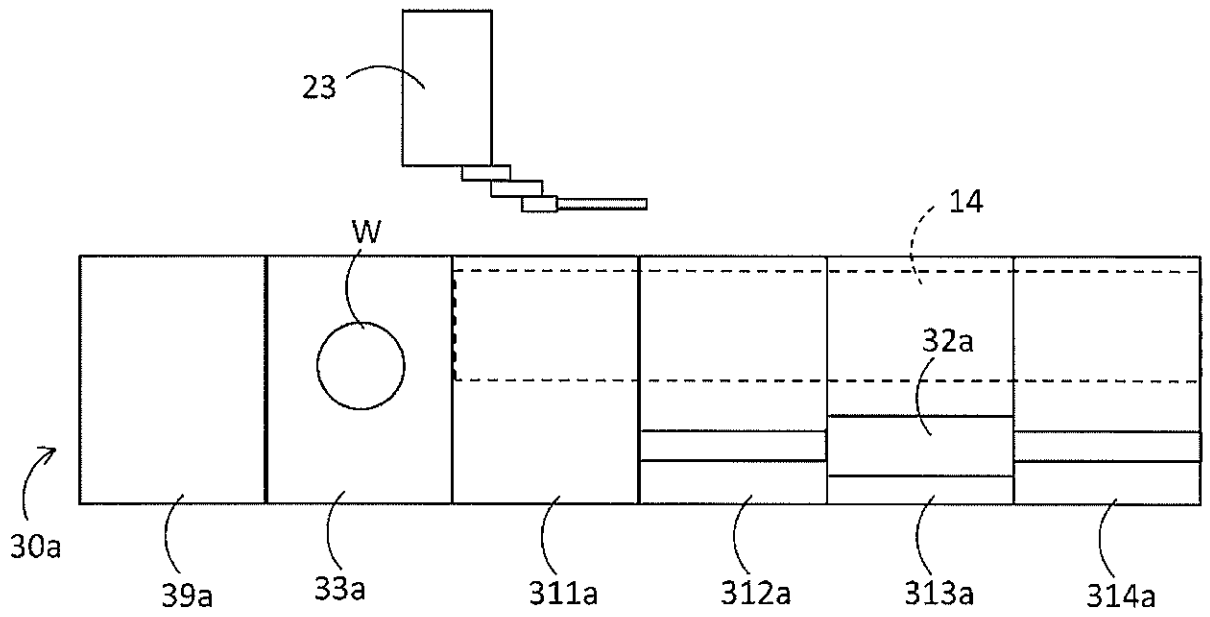


Fig.28D

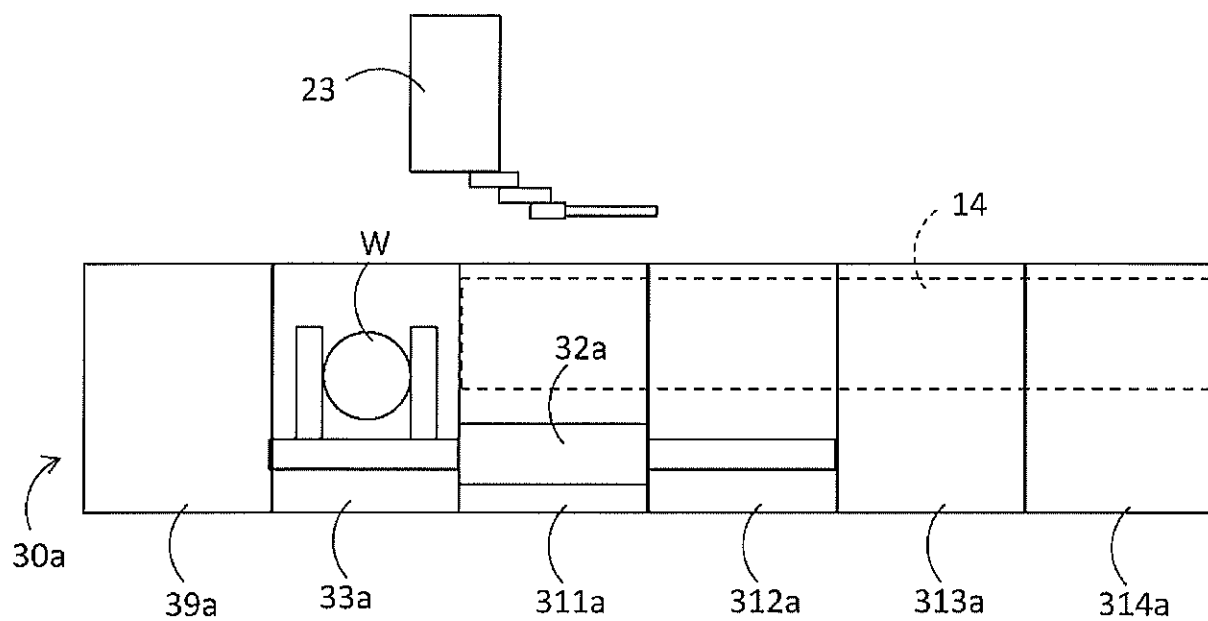


Fig.28E

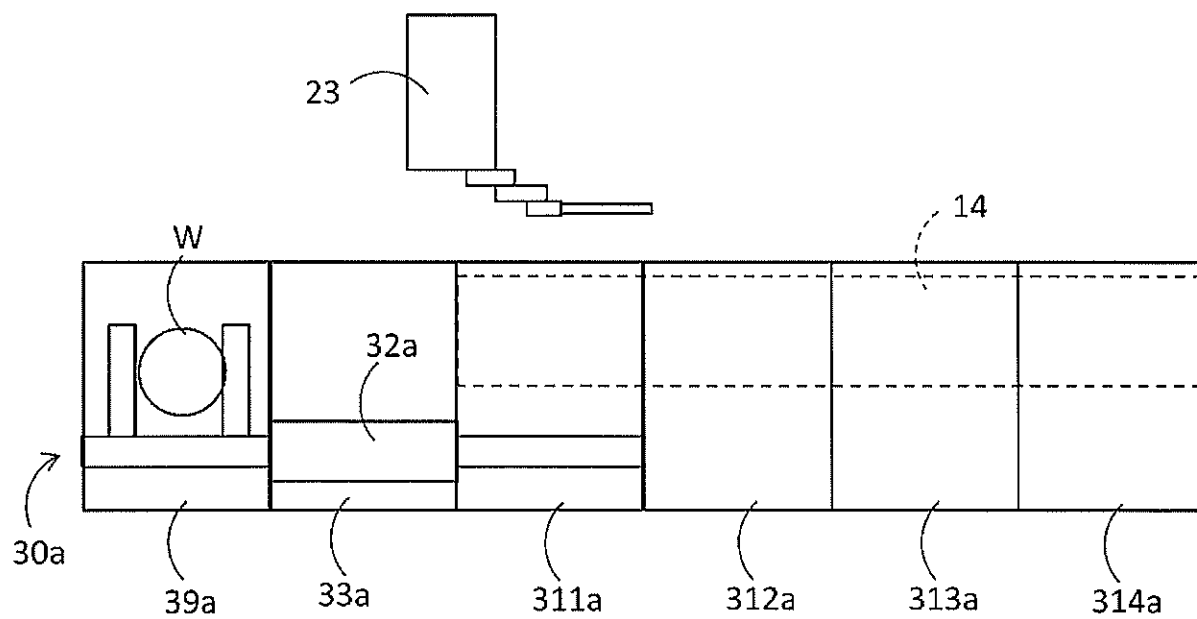


Fig.29A

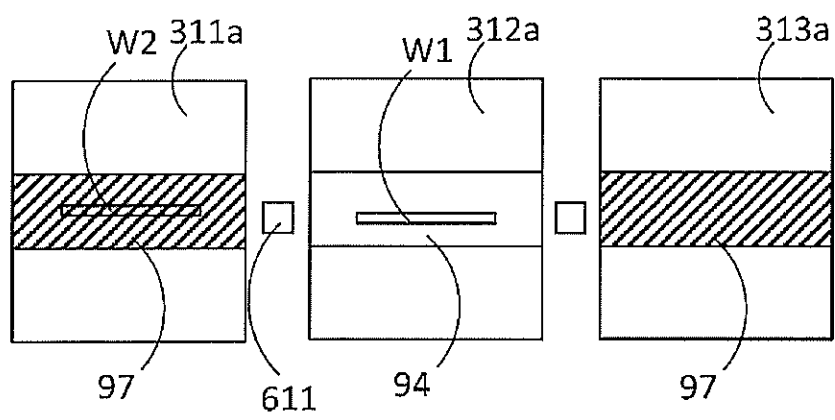


Fig.29B

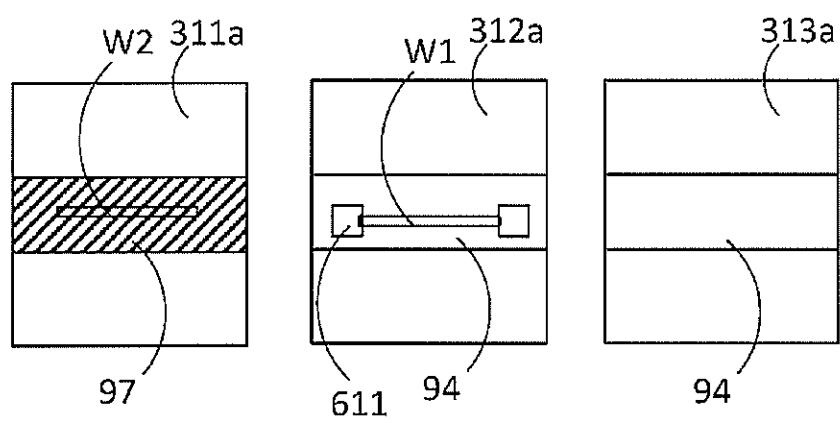


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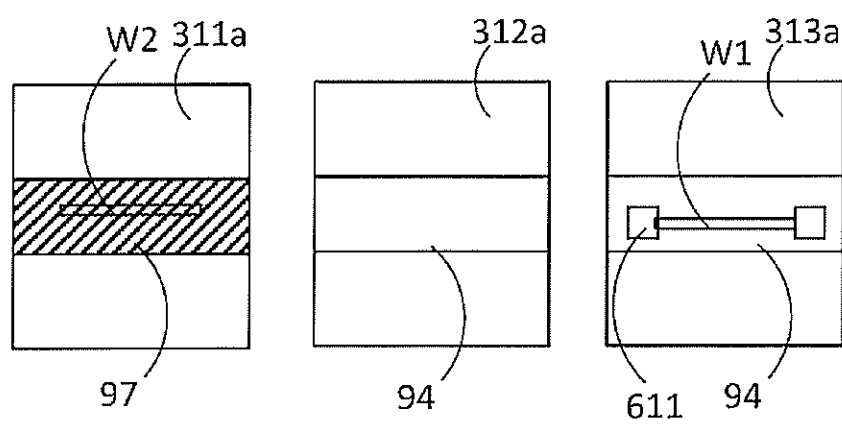


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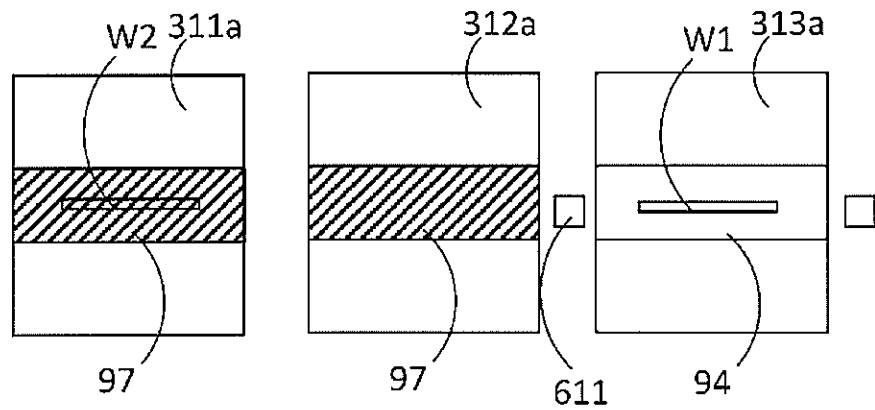


Fig.29E

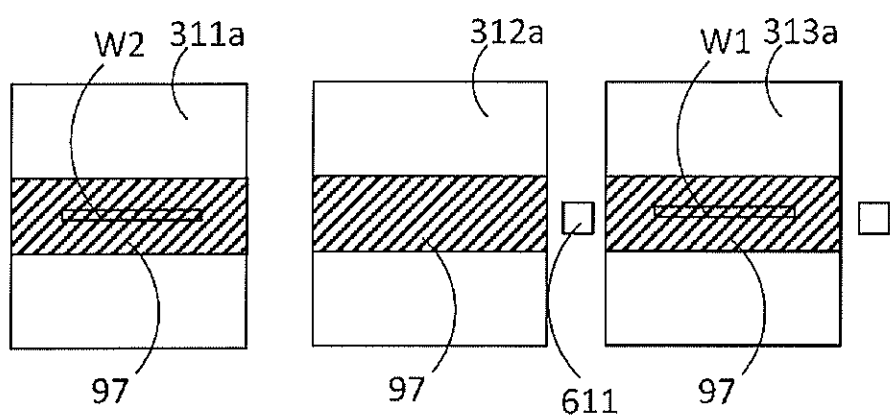


Fig.29F

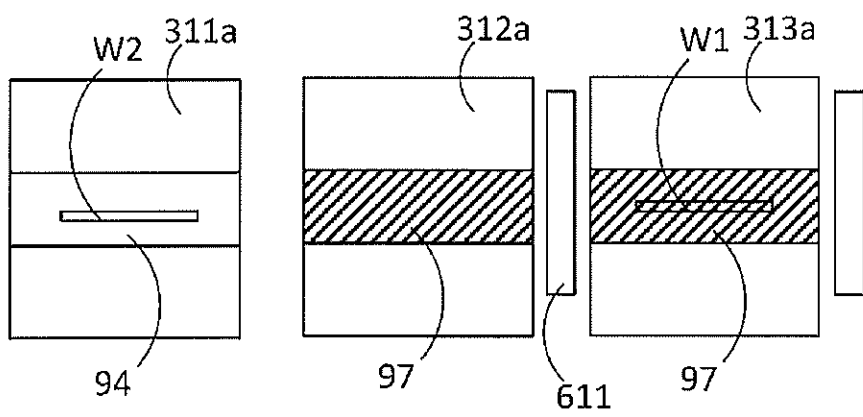


Fig.29G

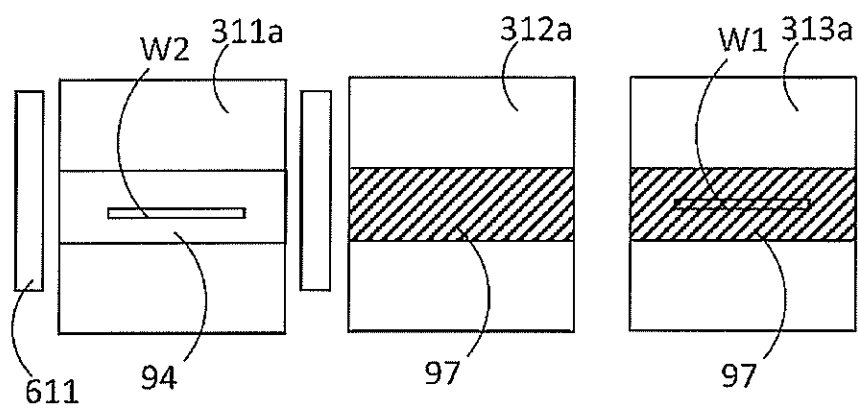


Fig.29H

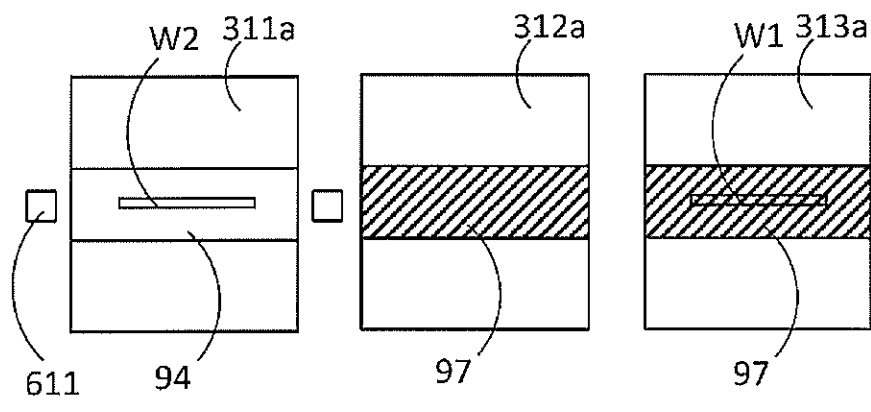


Fig.29I

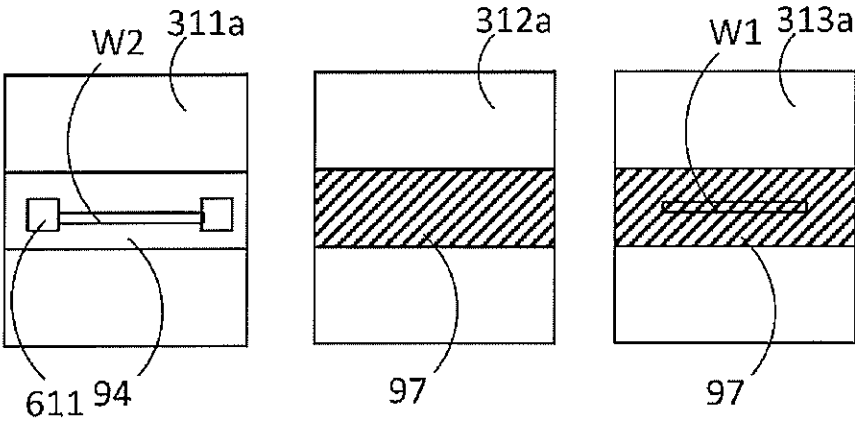


Fig.30A

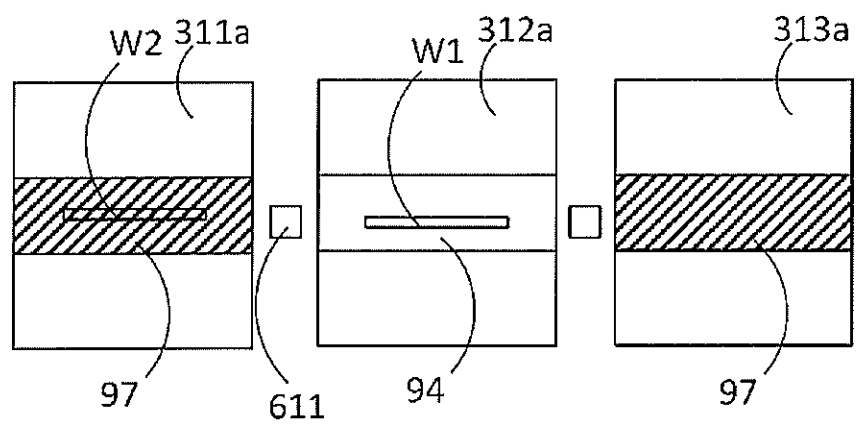


Fig.30B

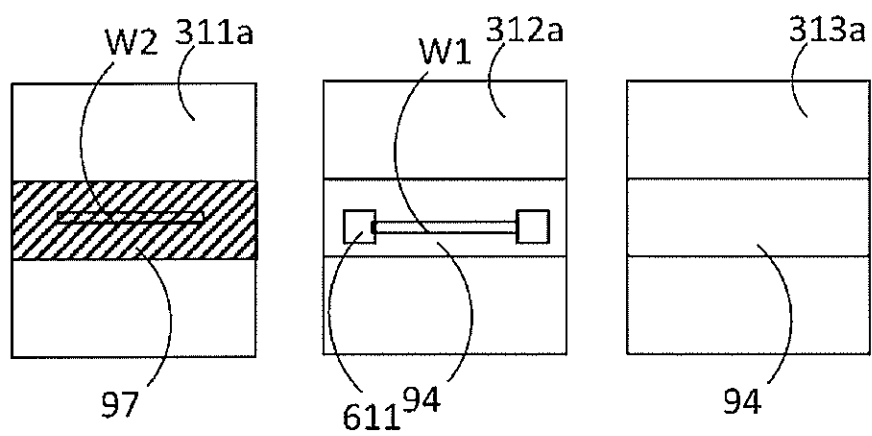


Fig.30C

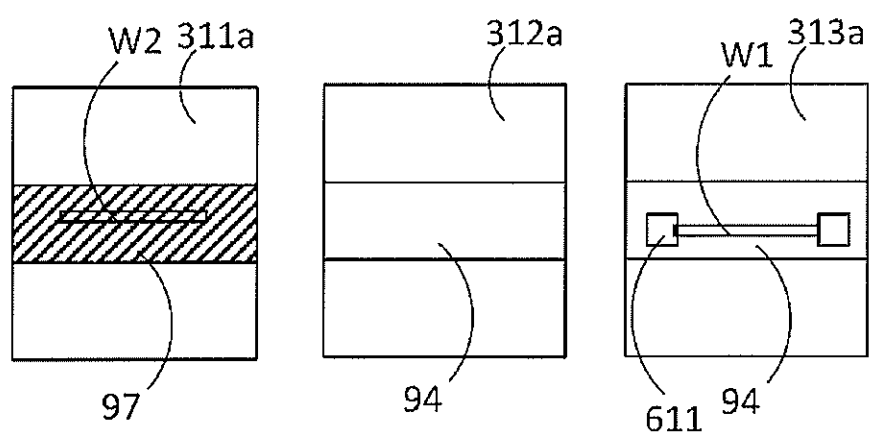


Fig.30D

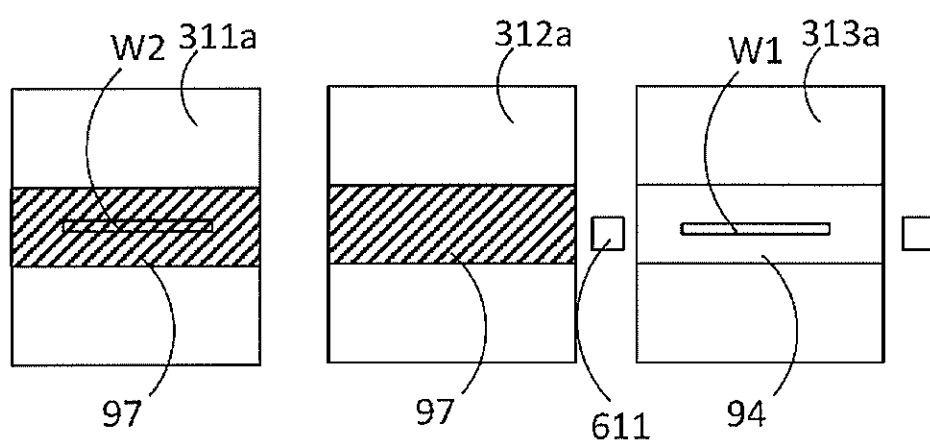


Fig.30E

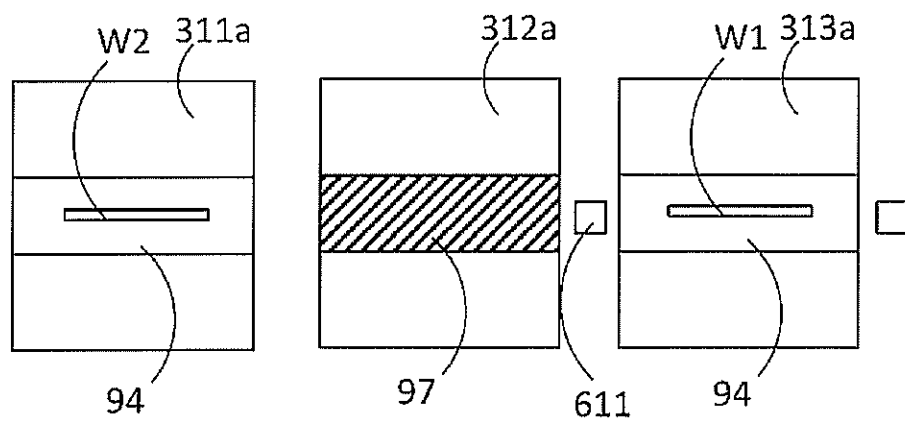


Fig.30F

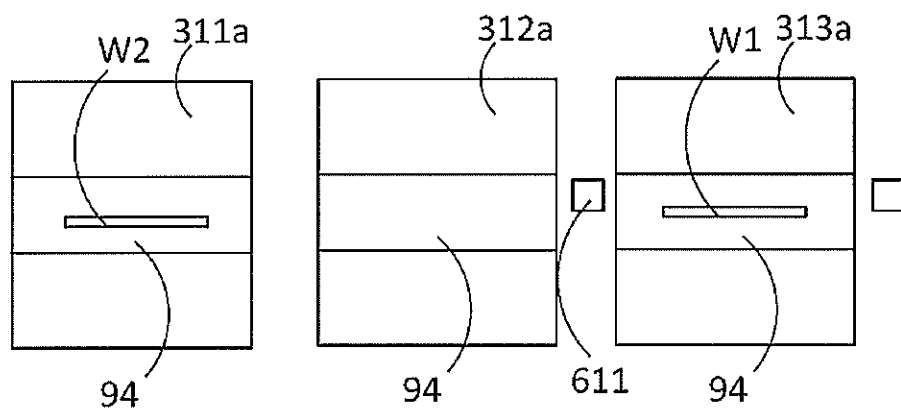


Fig.30G

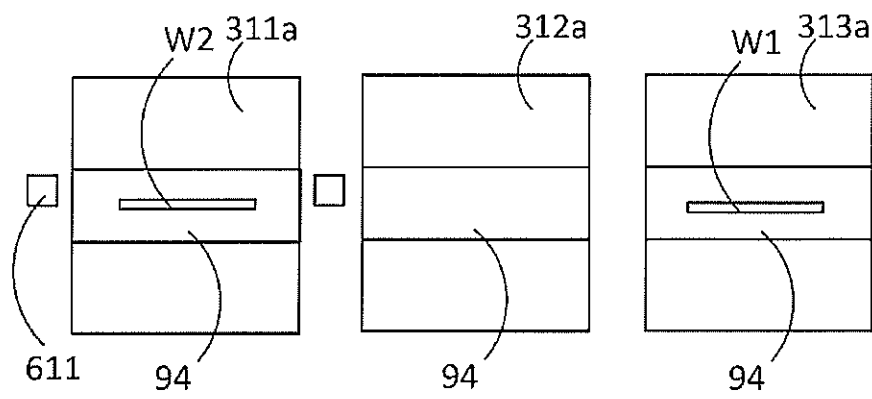


Fig.30H

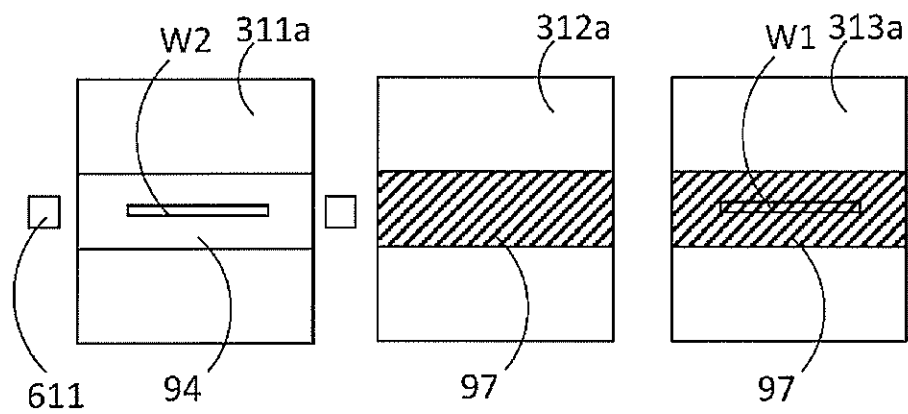


Fig.30I

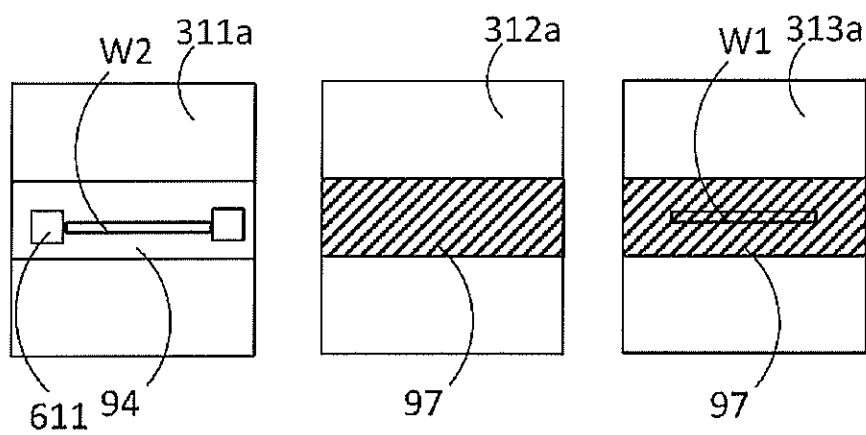


Fig.31

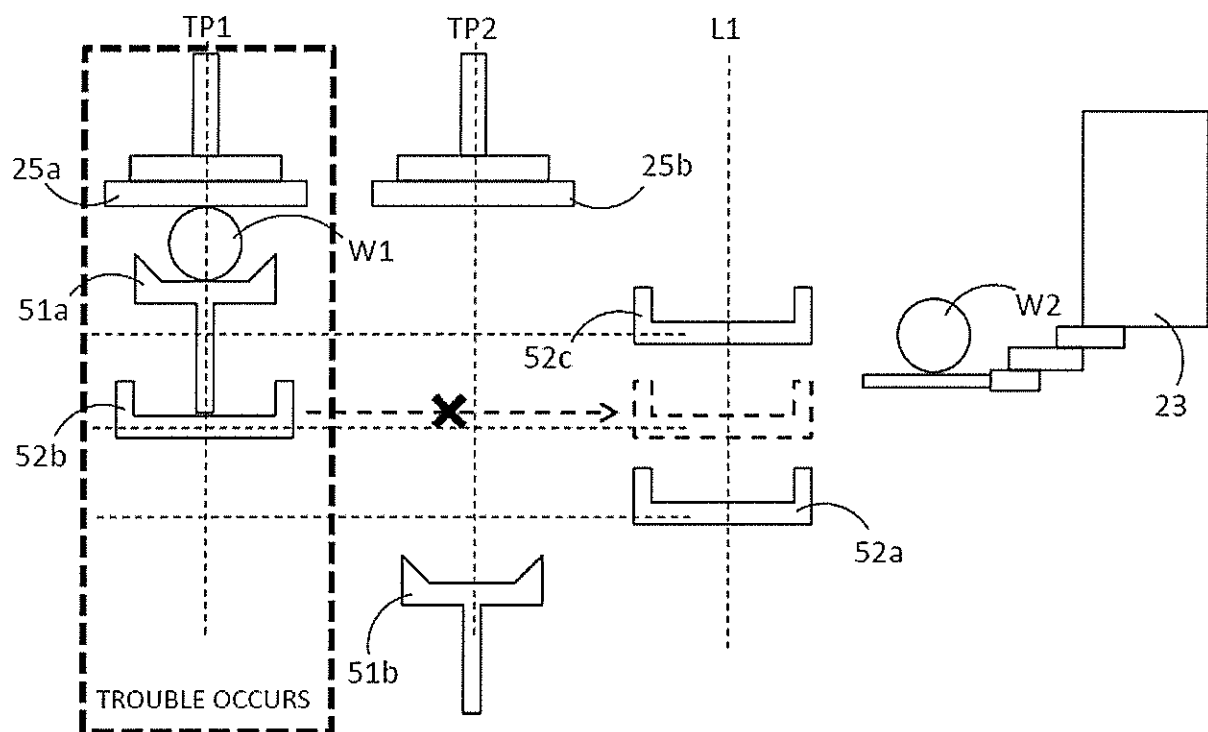


Fig.32

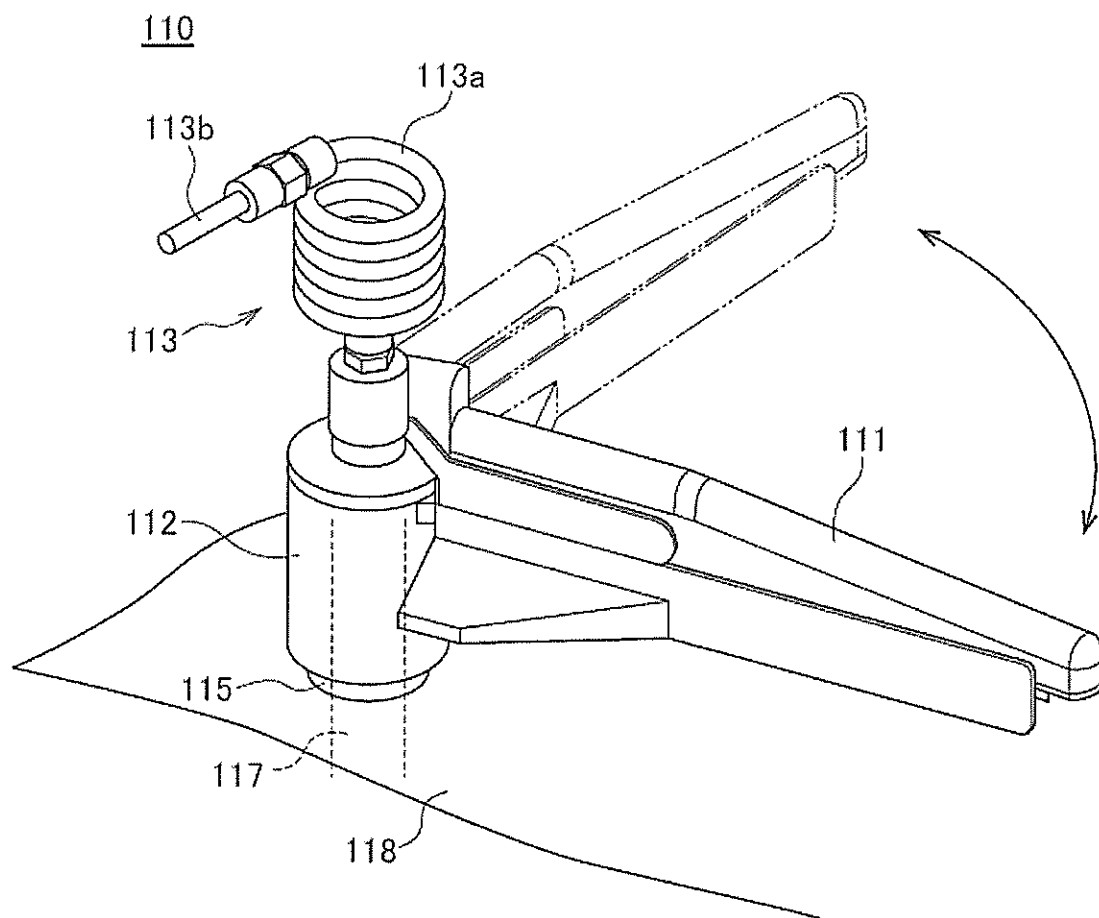


Fig.33

