

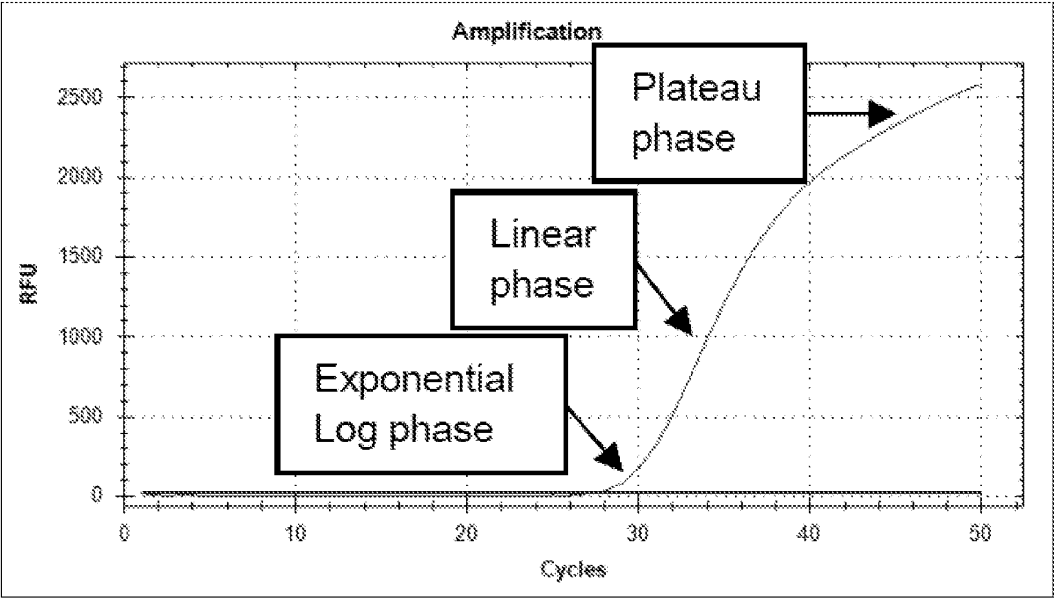


Fig. 1A

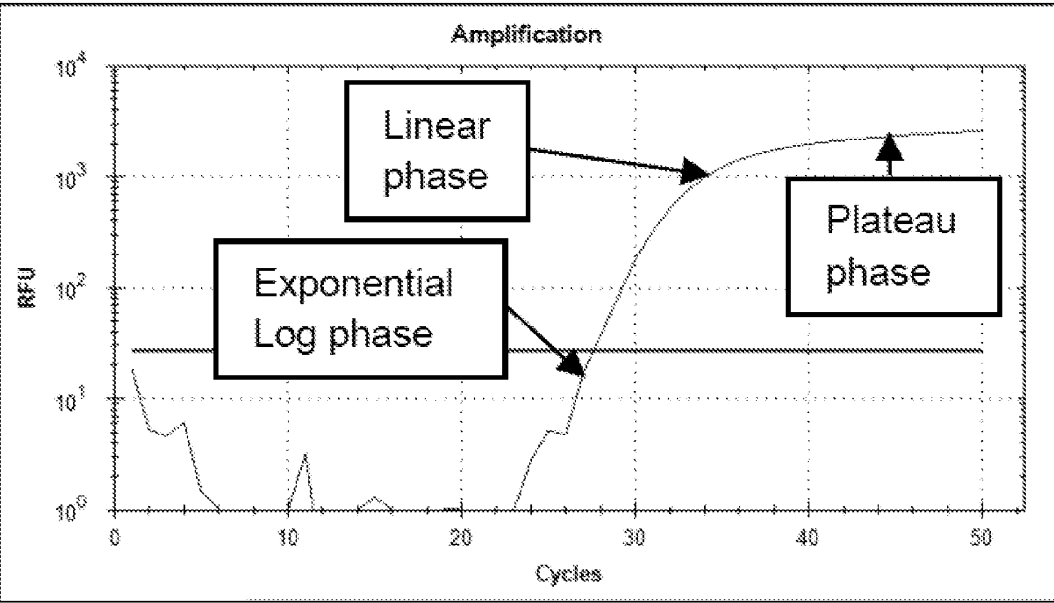


Fig. 1B

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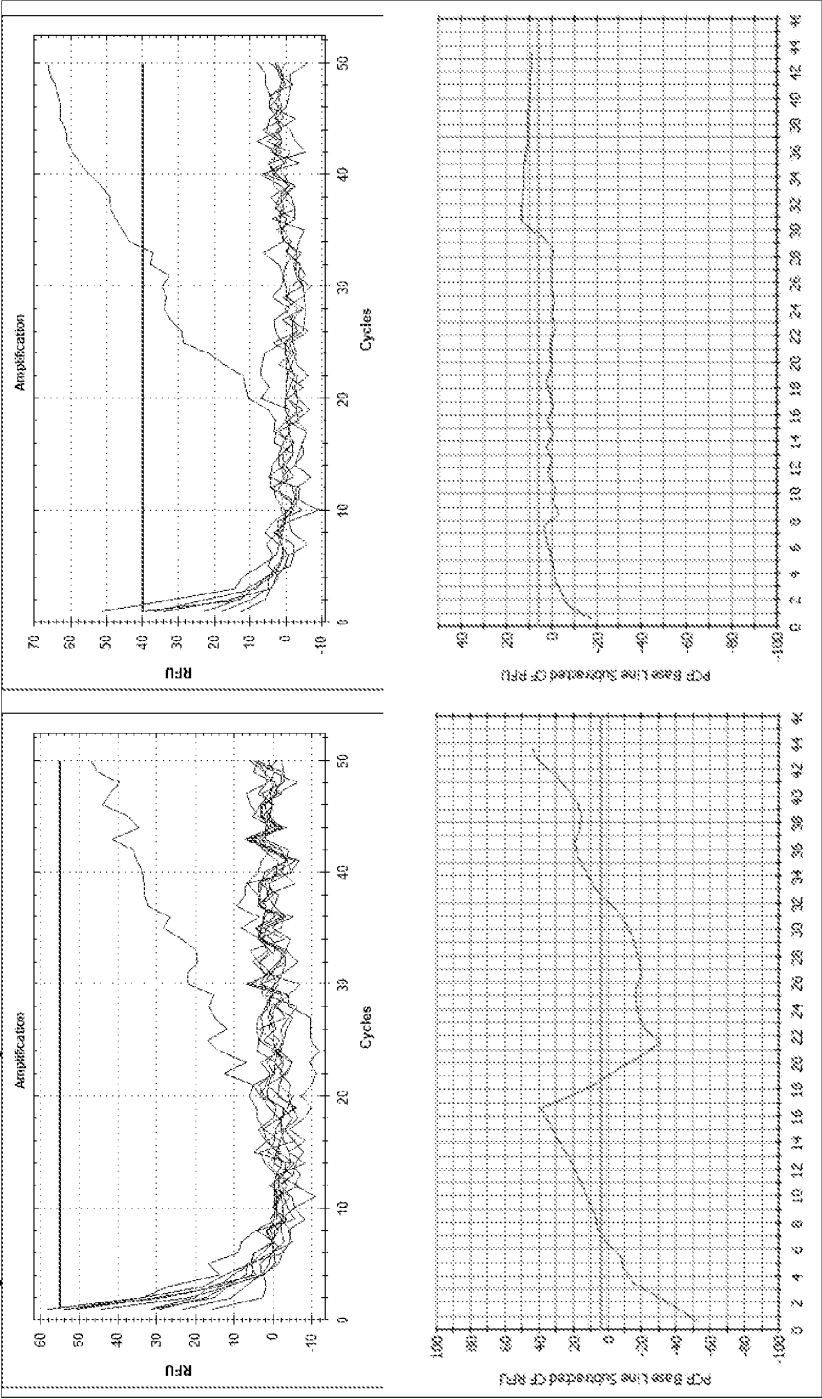


Fig. 2

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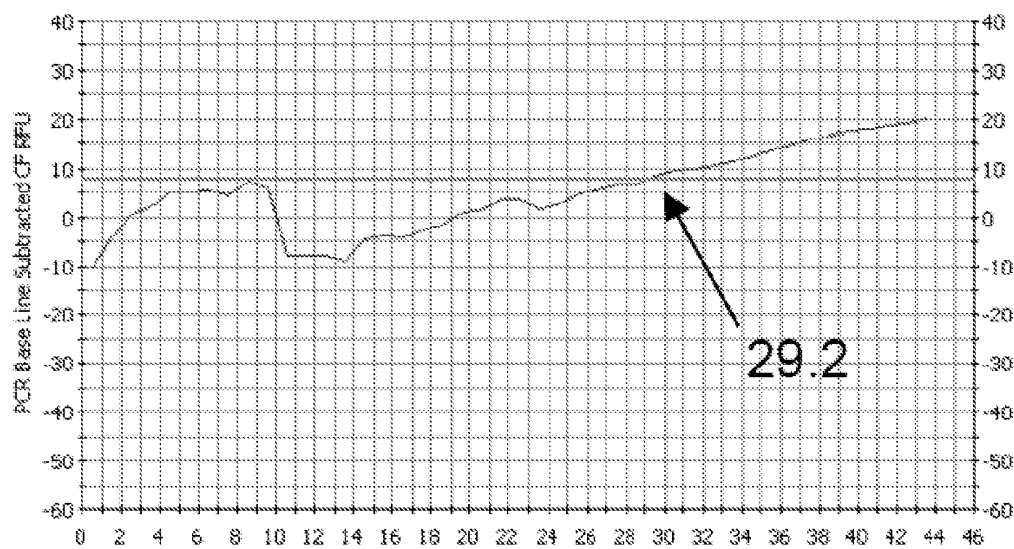


Fig. 3A

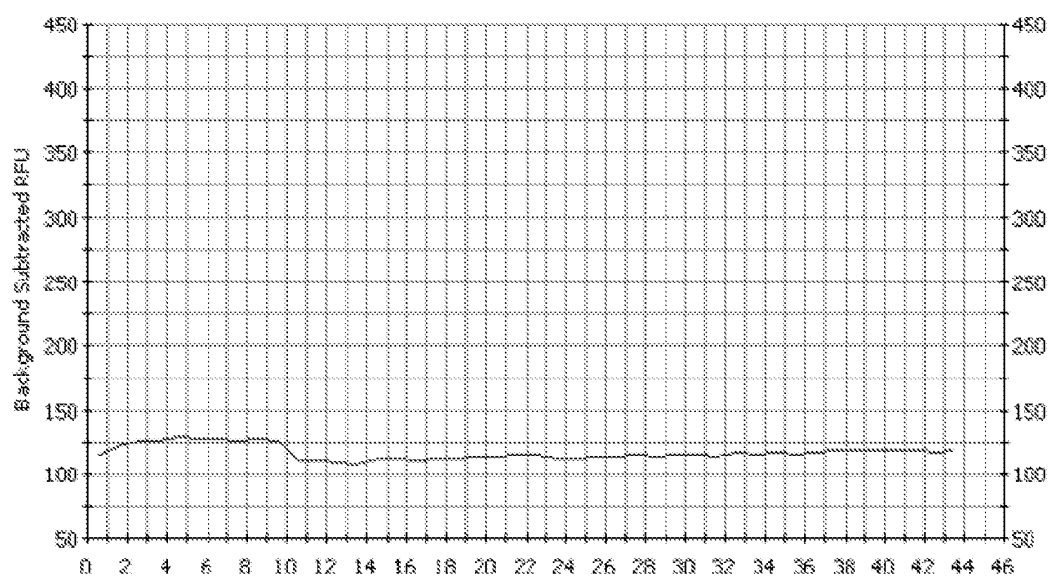


Fig. 3B

SlgN-DxD CHIKV Single-plex FAM (Set 1)

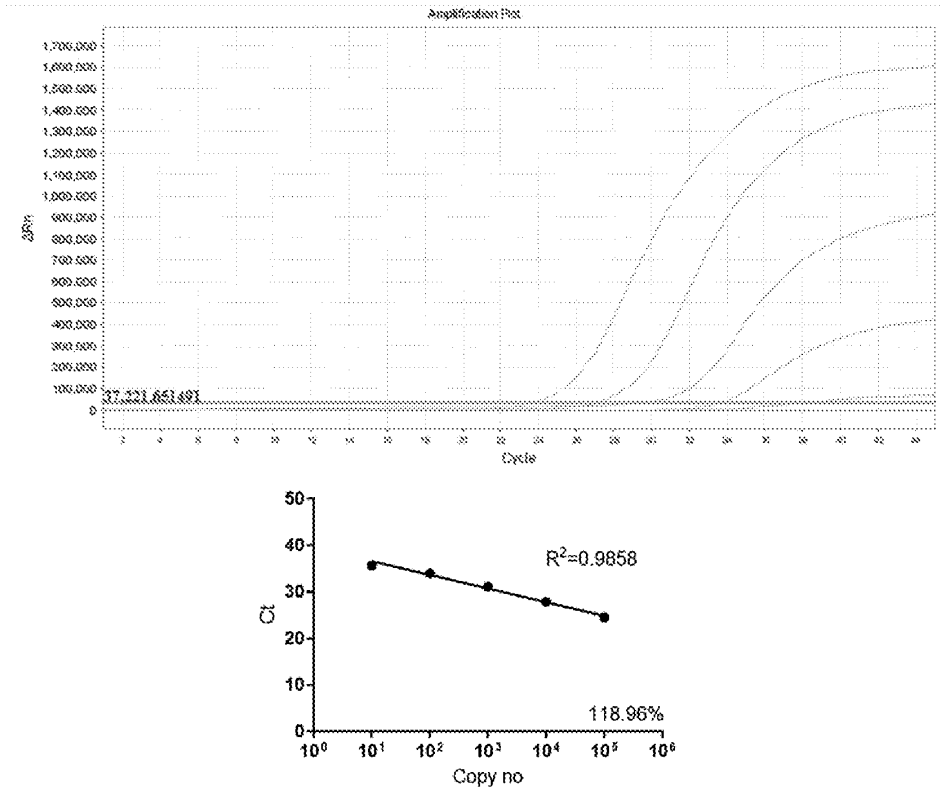


Fig. 4A

SlgN-DxD CHIKV Single-plex FAM (Set 2)

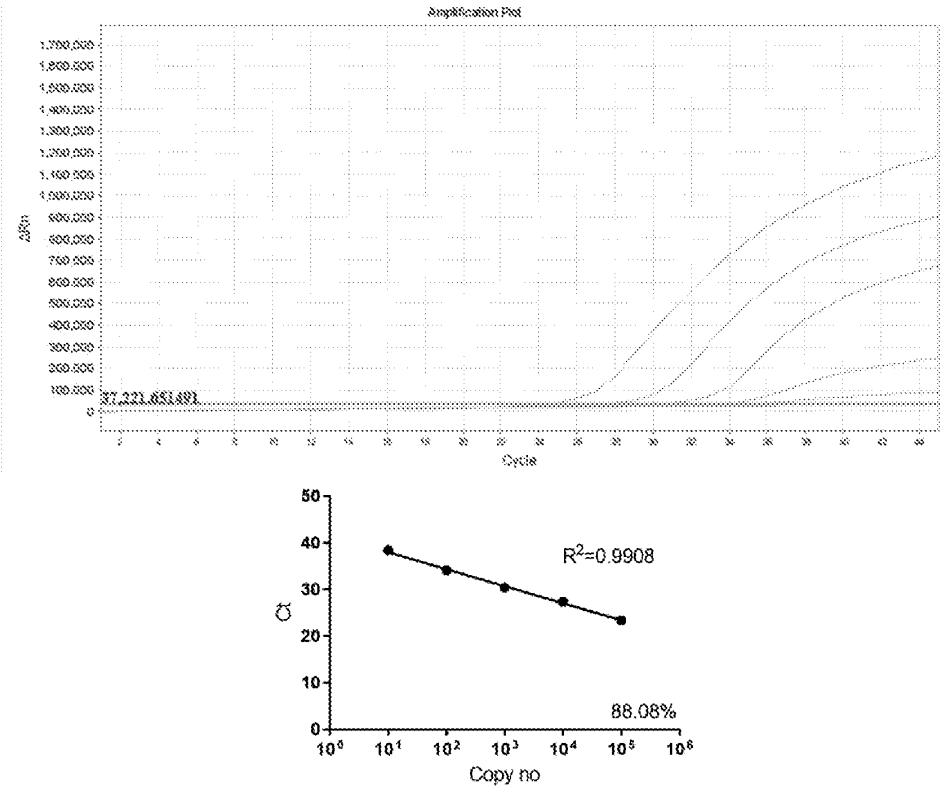


Fig. 4B

CDC CHIKV Single-plex FAM

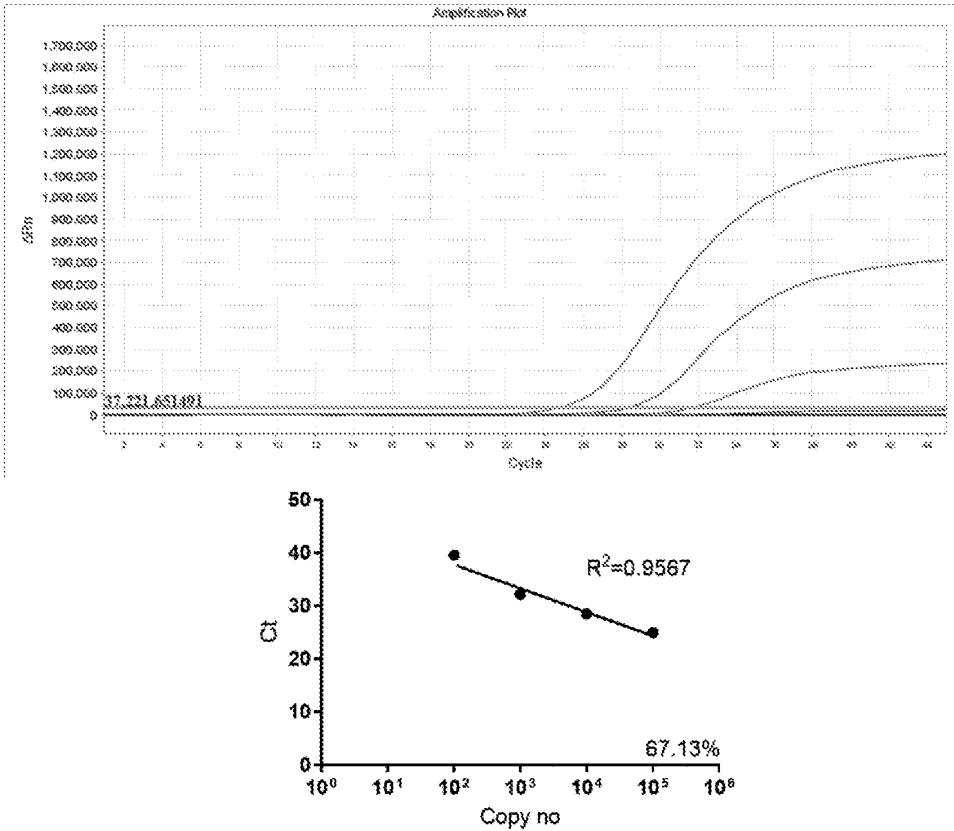


Fig. 4C

SIgN-DxD DENV1 Single-plex Hex (Set 1)

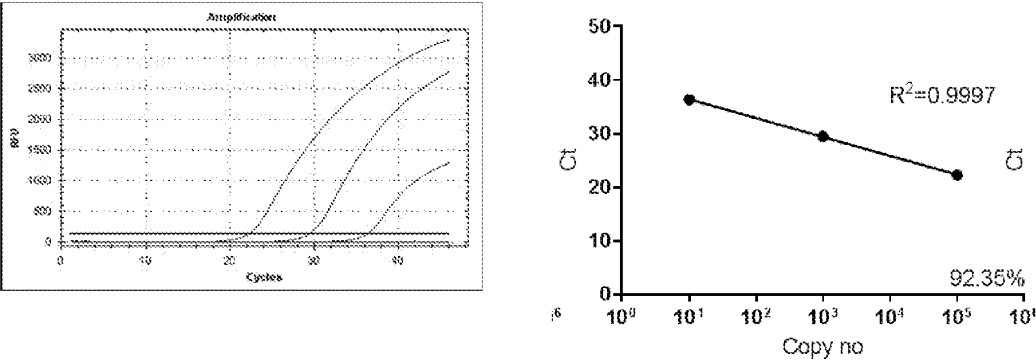


Fig. 5A

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CDC DENV1 Single-plex FAM

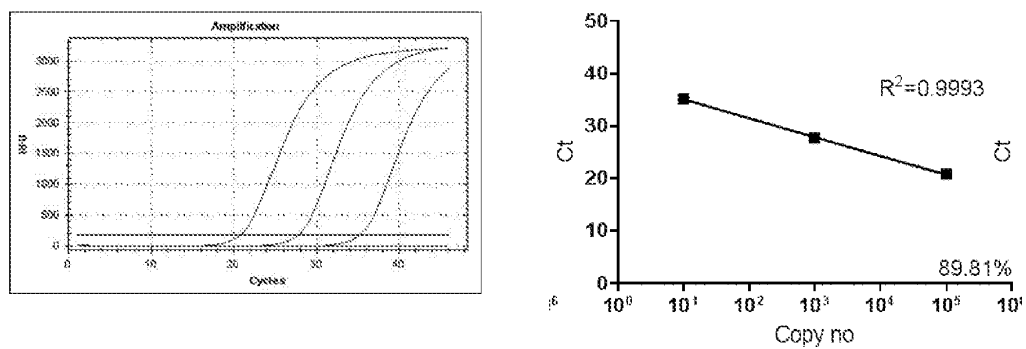


Fig. 5B

SlgN-DxD DENV2 Single-plex Hex (Set 1)

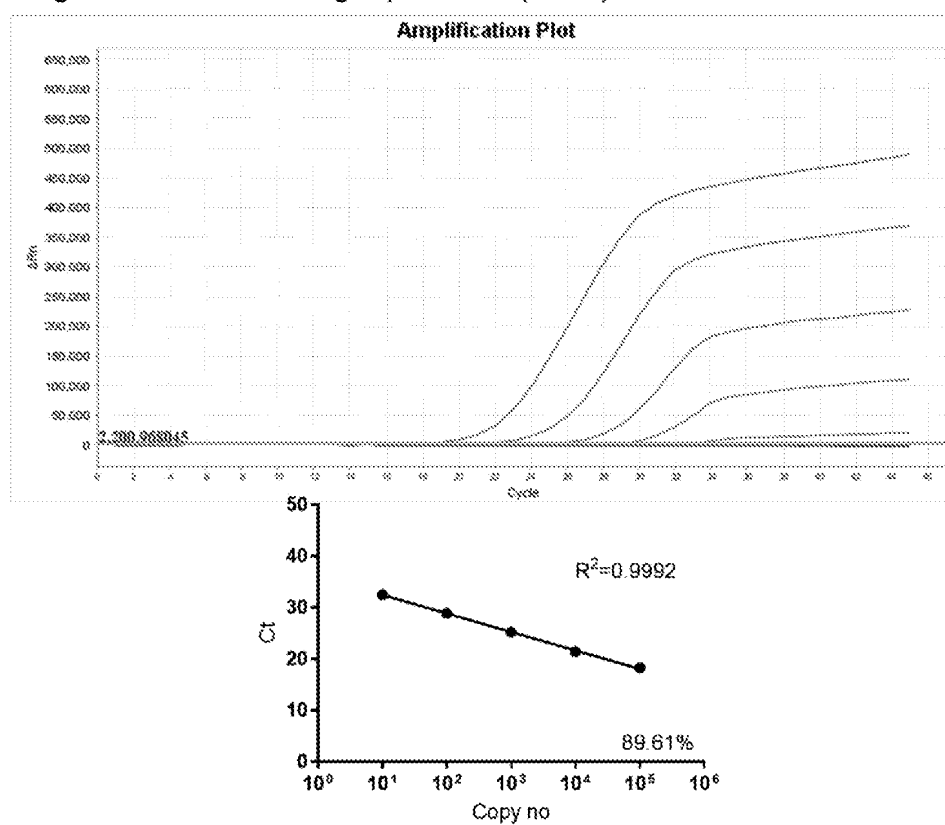


Fig. 6A

SlgN-DxD DENV2 Single-plex Hex (Set 2)

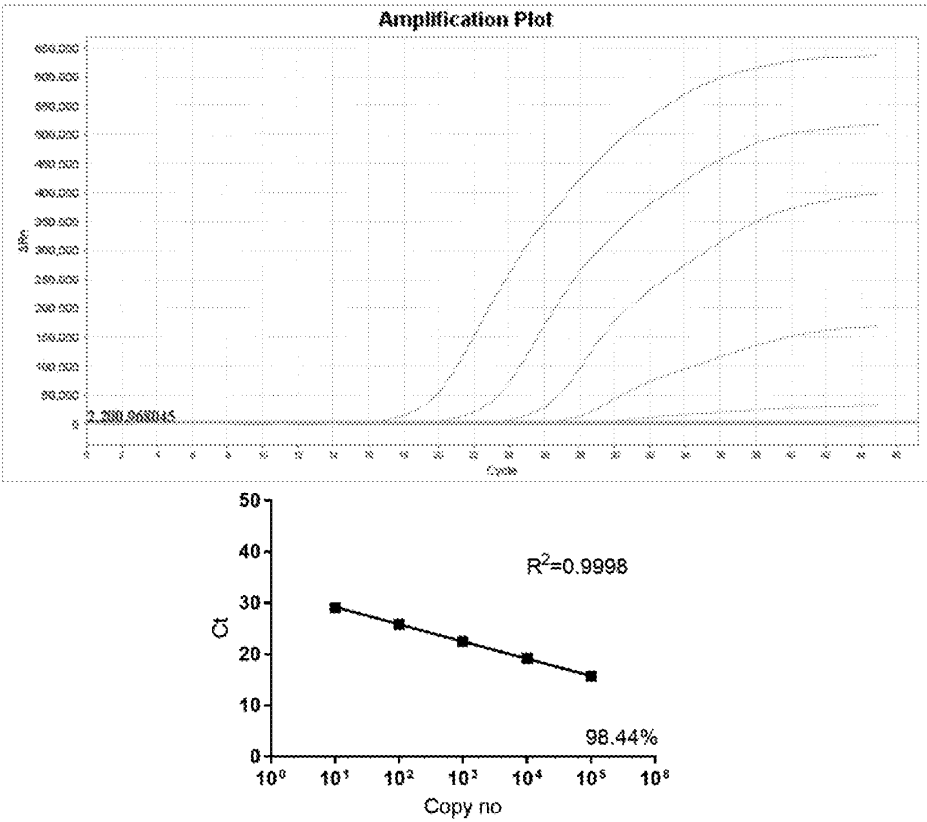


Fig. 6B

SlgN-DxD DENV2 Single-plex Hex (Set 4)

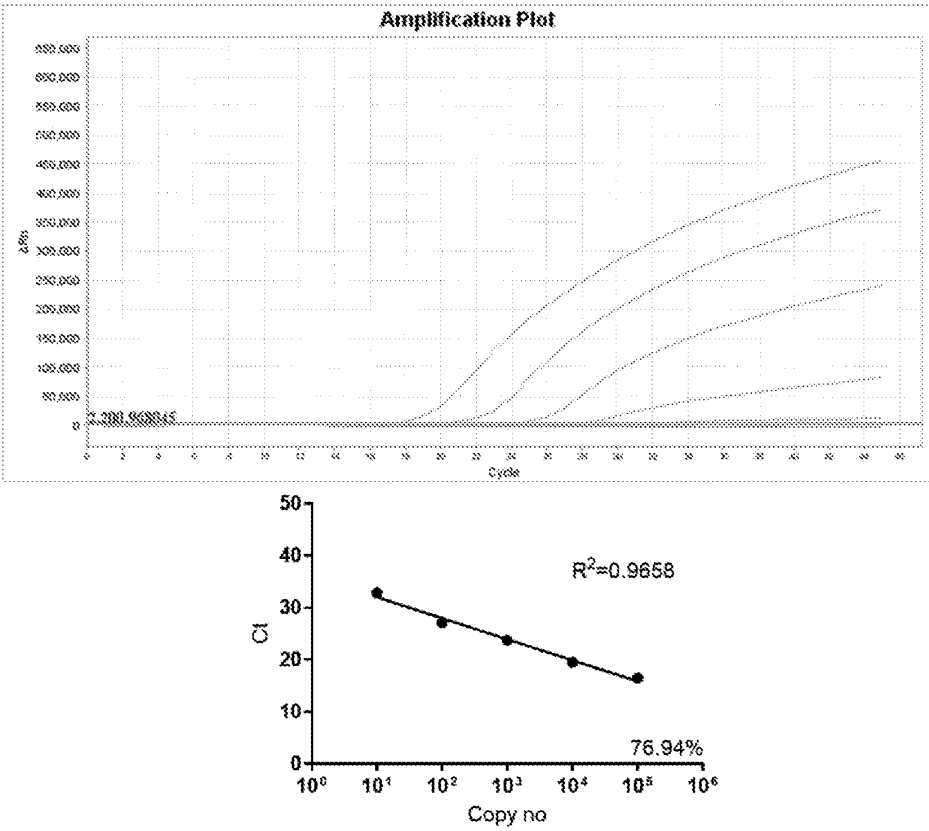


Fig. 6C

CDC DENV2 Single-plex Hex

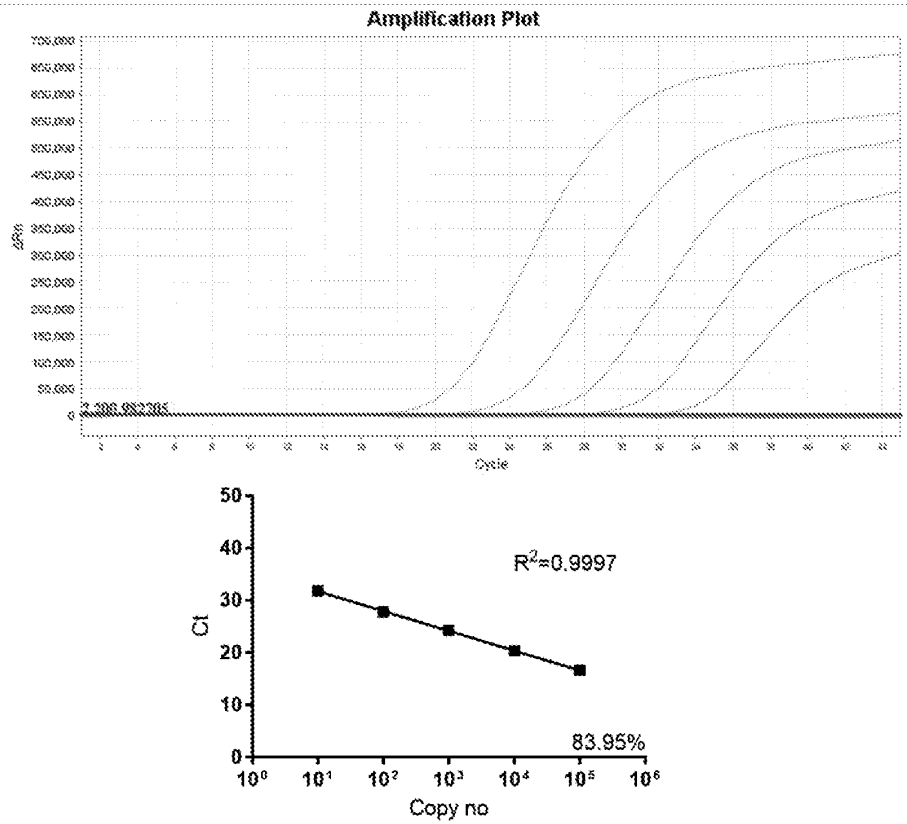


Fig. 6D

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SlgN-DxD DENV3 Single-plex TxR (Set 1)

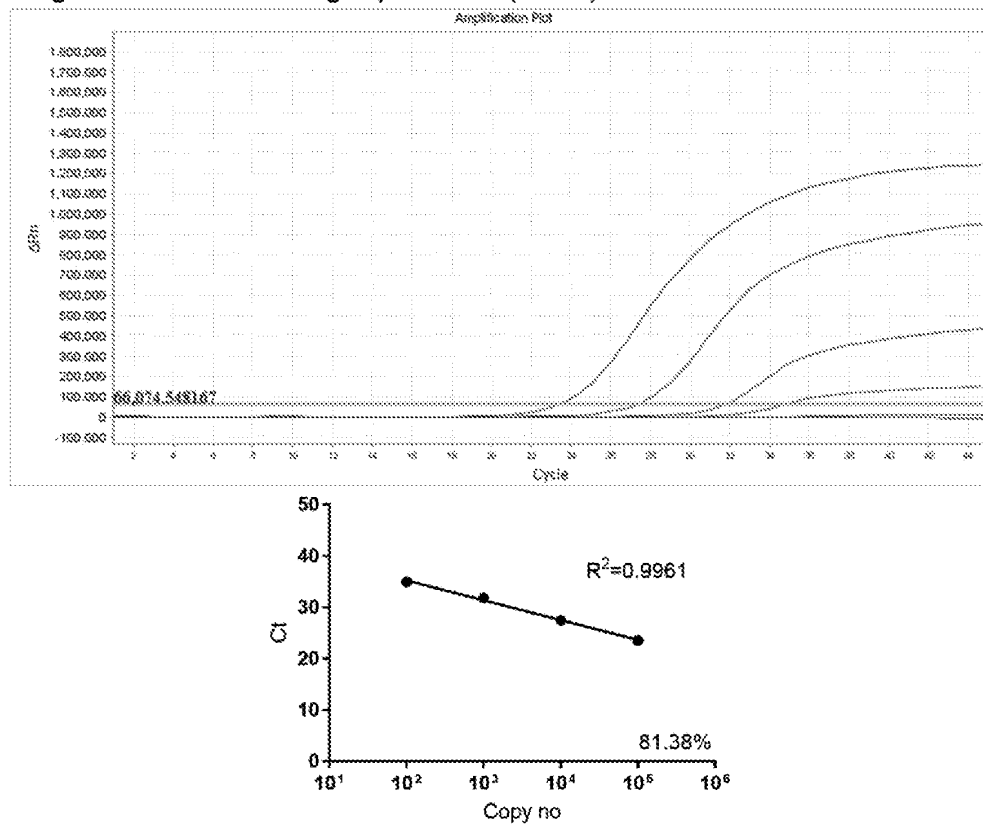


Fig. 7A

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SlgN-DxD DENV3 Single-plex TxR (Set 2)

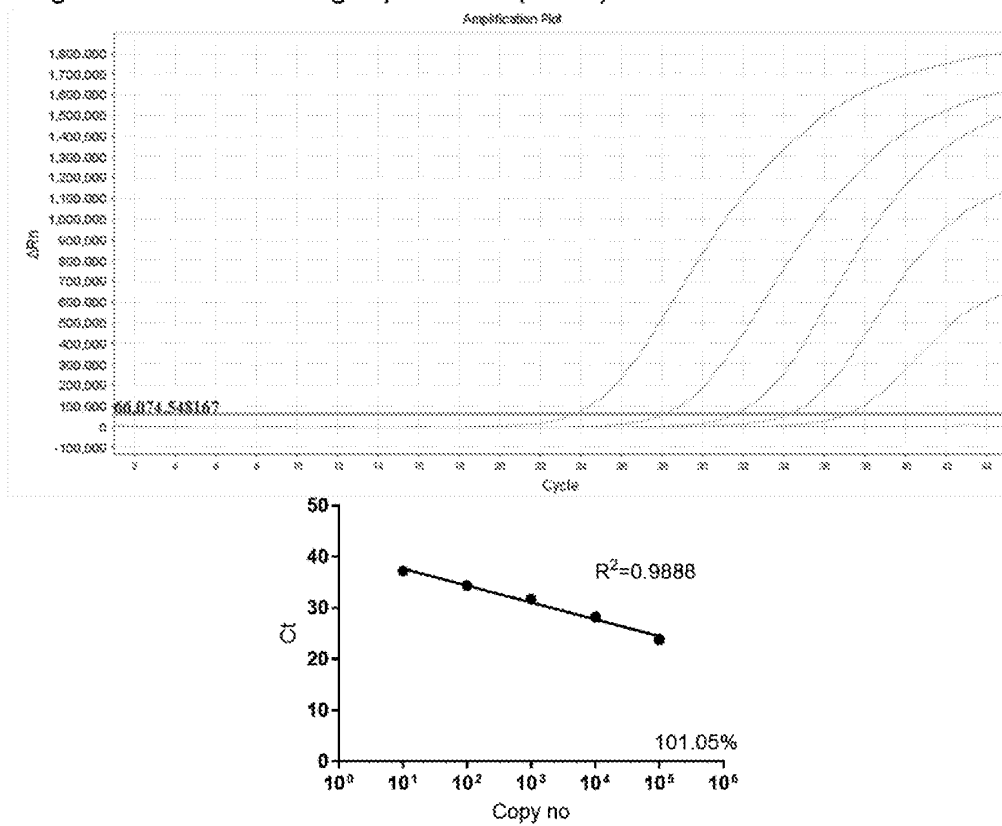


Fig. 7B

SIgN-DxD DENV3 Single-plex TxR (Set 3)

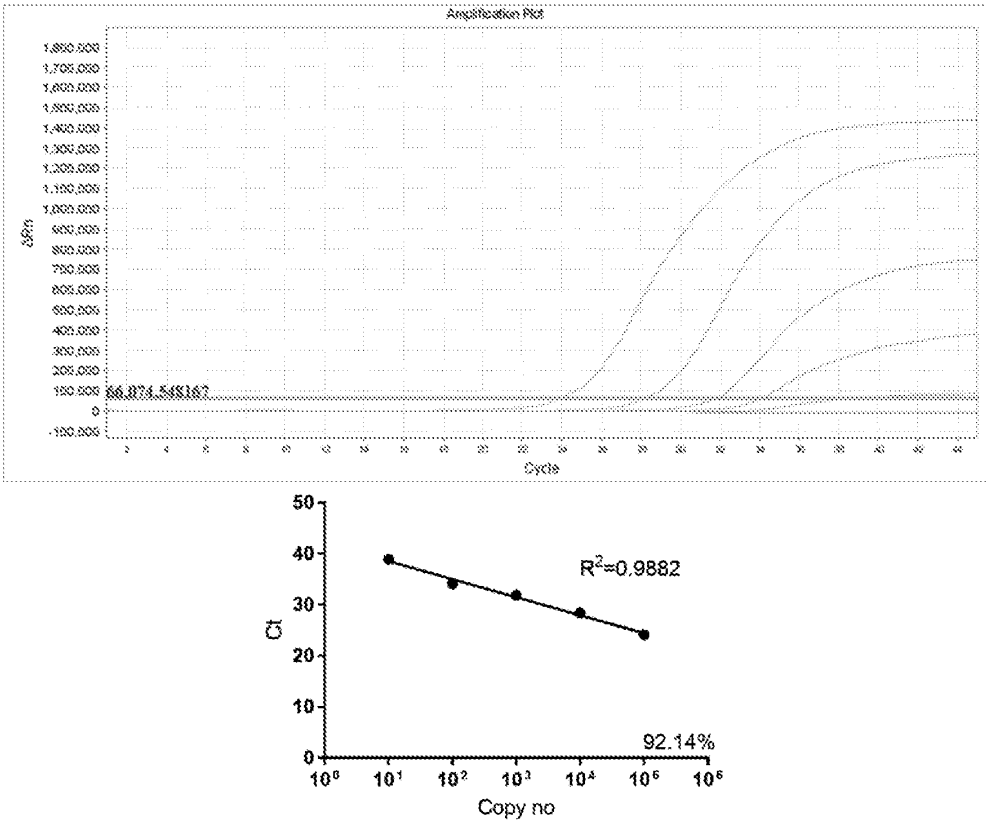


Fig. 7C

SlgN-DxD DENV3 Single-plex TxR (Set 4)

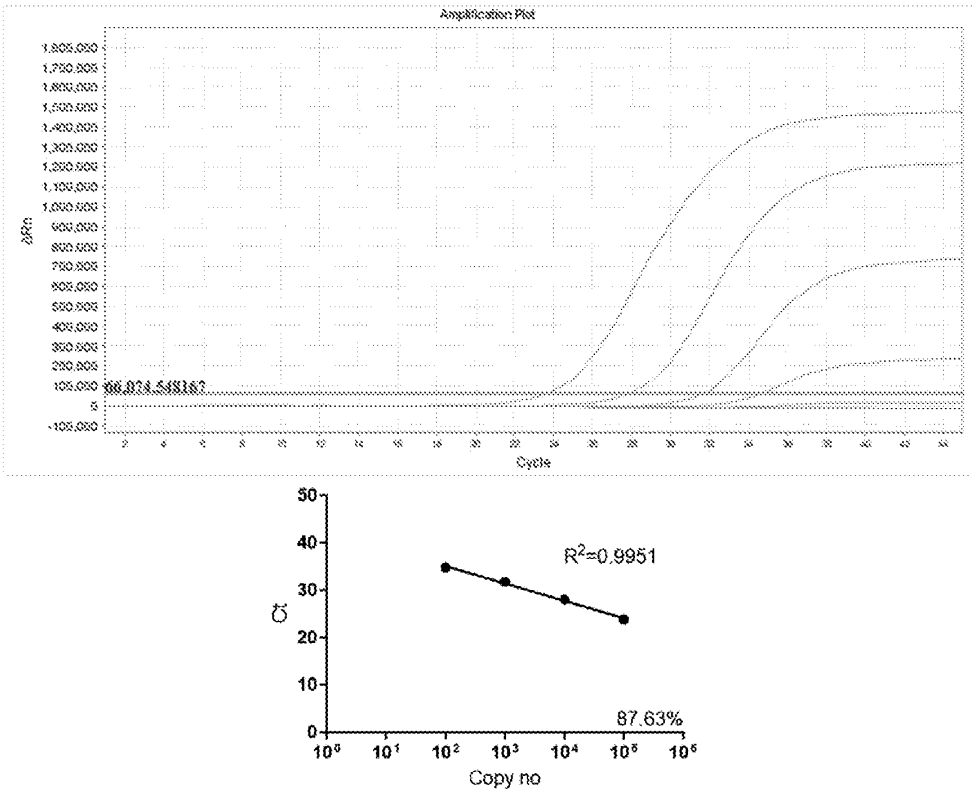


Fig. 7D

CDC DENV3 Single-plex TxR

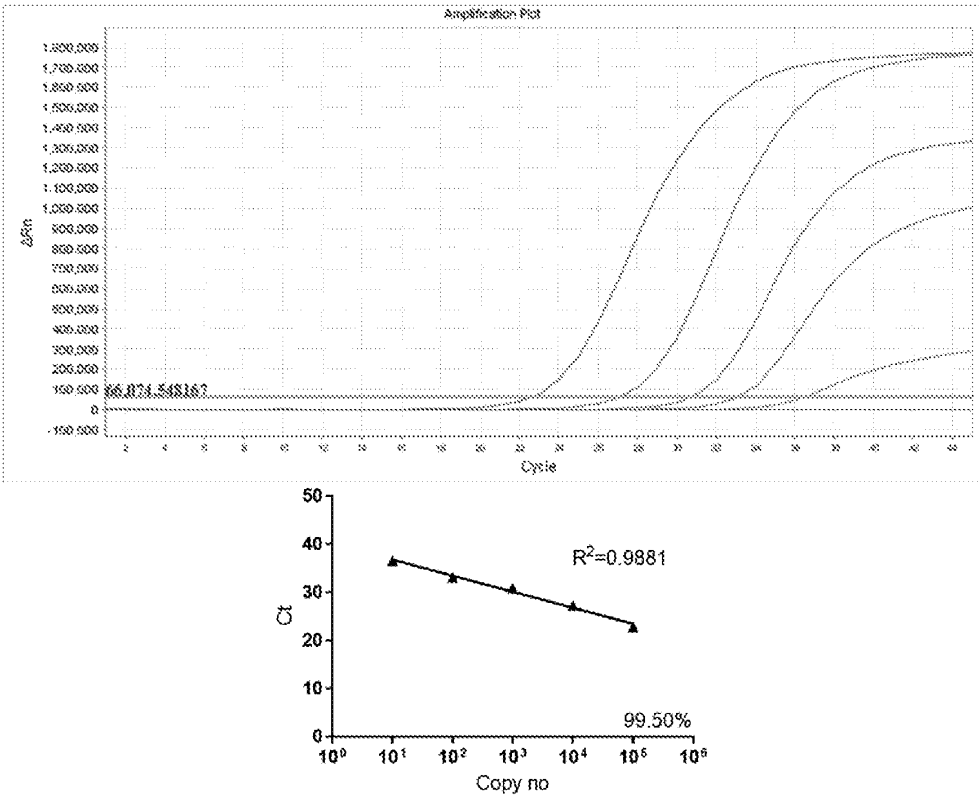


Fig. 7E

SlgN-DxD DENV4 Single-plex TxR (Set 2)

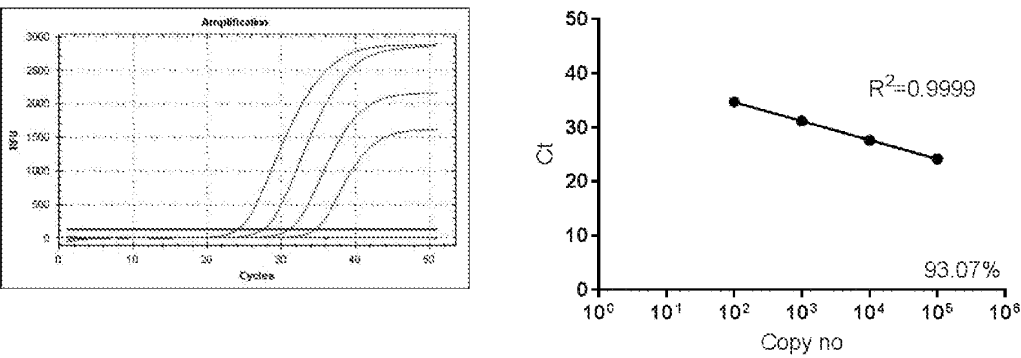


Fig. 8A

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SigN-DxD DENV4 Single-plex TxR (Set 3)

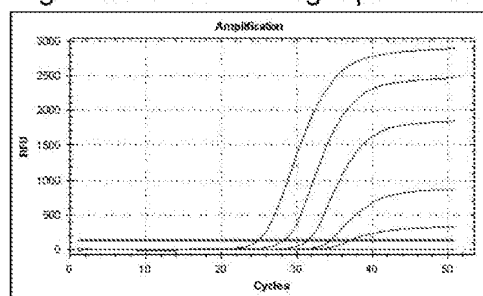
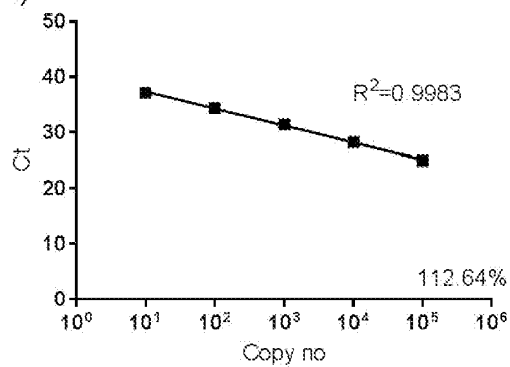


Fig. 8B

SigN-DxD DENV4 Single-plex TxR (Set 4)

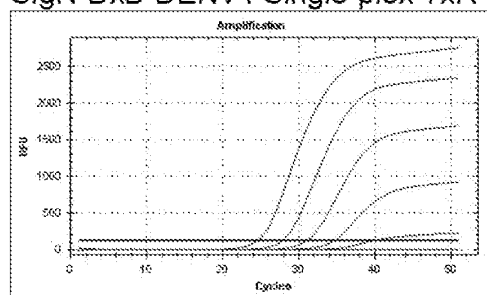
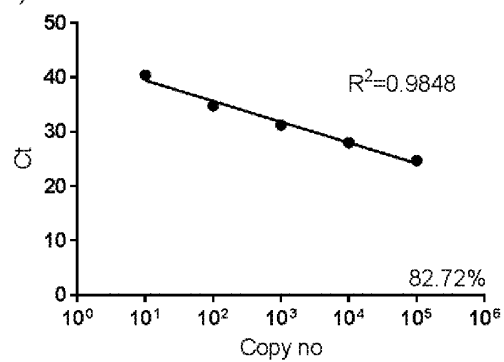


Fig. 8C

CDC DENV4 Single-plex Cy5

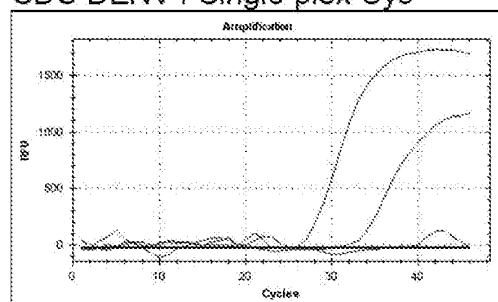
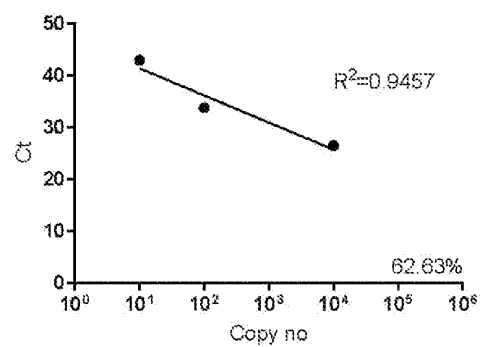


Fig. 8D

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SIgN-DxD ZIKV Single-plex FAM (Set 1)

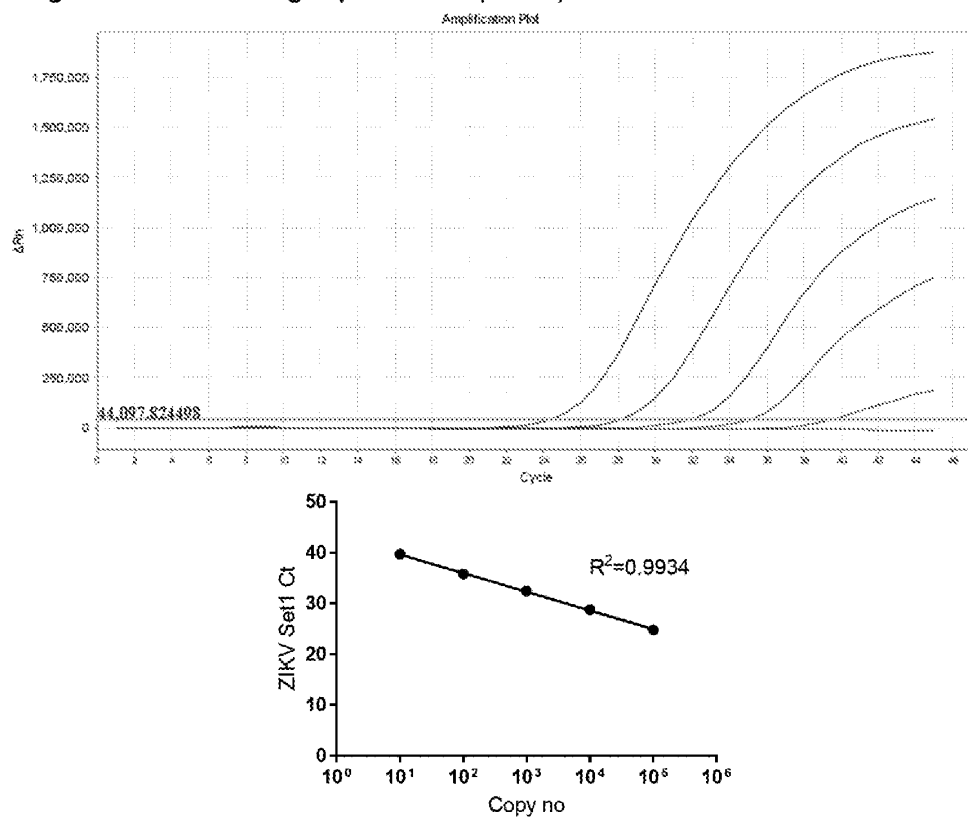


Fig. 9A

CDC ZIKV Single-plex FAM

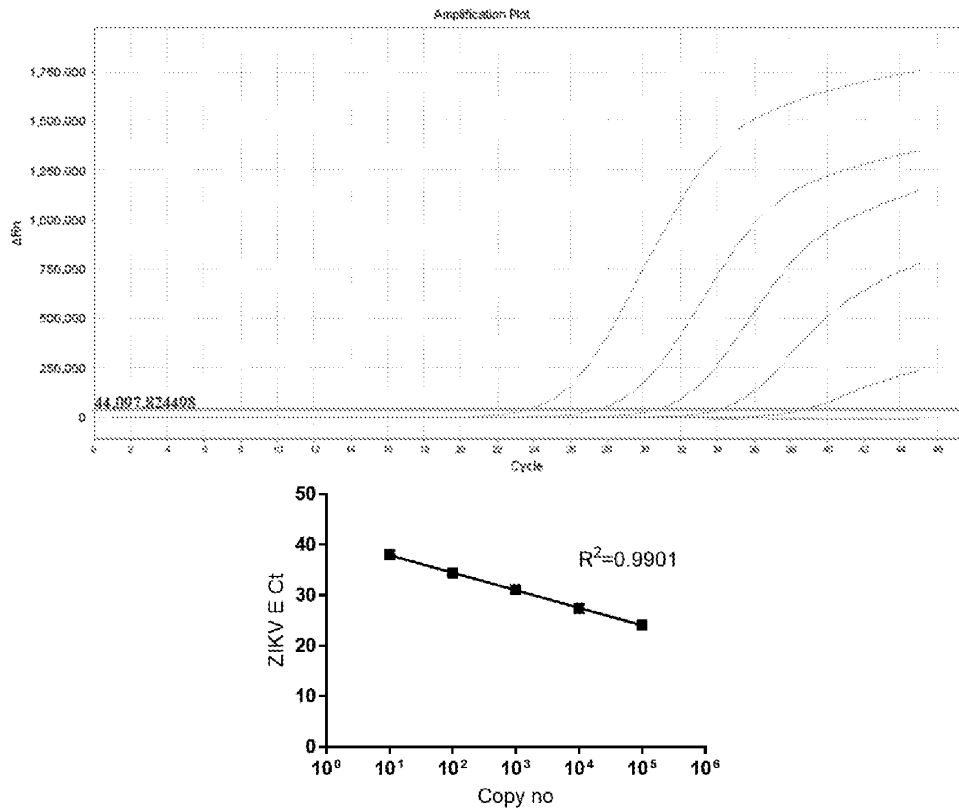


Fig. 9B

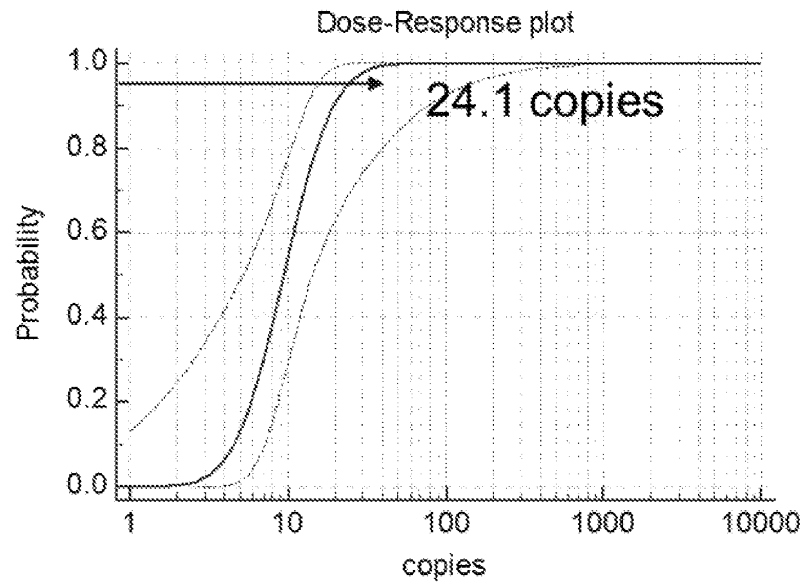


Fig. 10

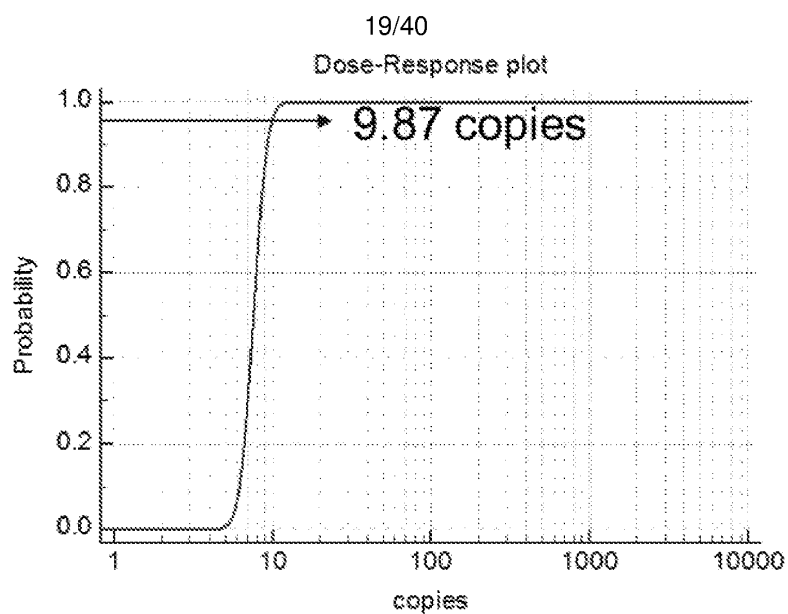


Fig. 11

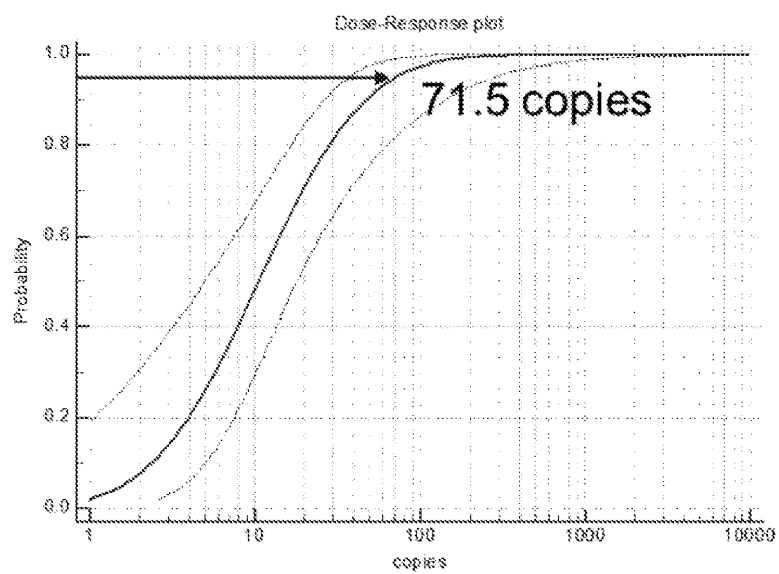


Fig. 12

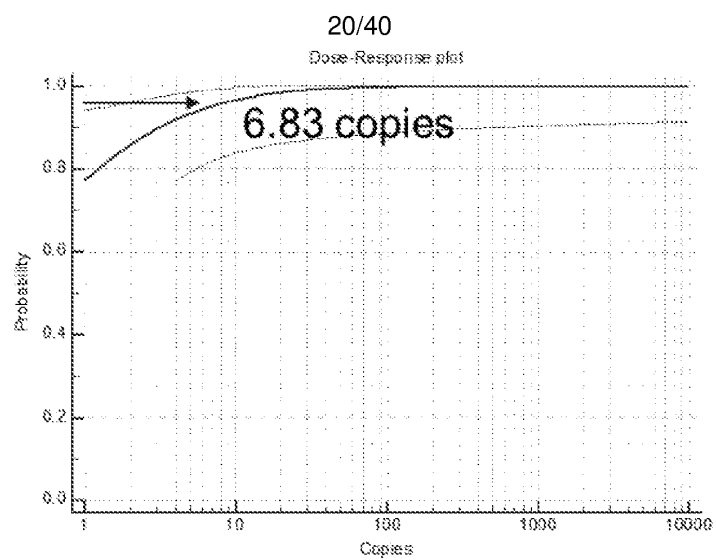


Fig. 13

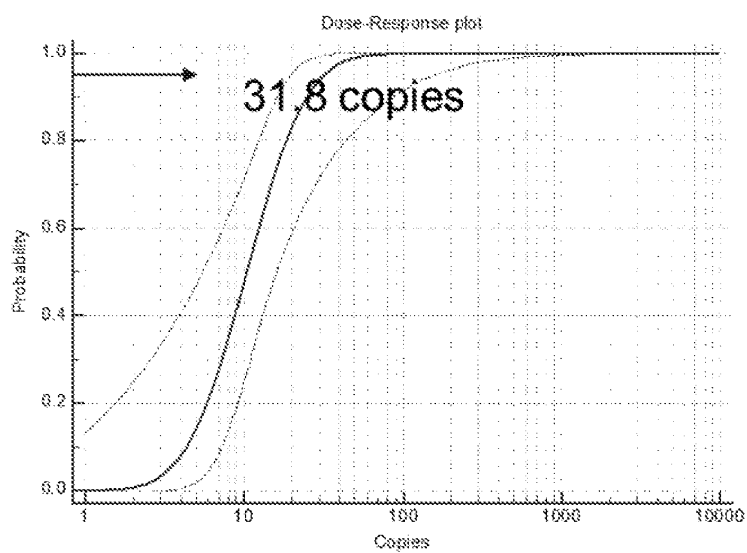


Fig. 14

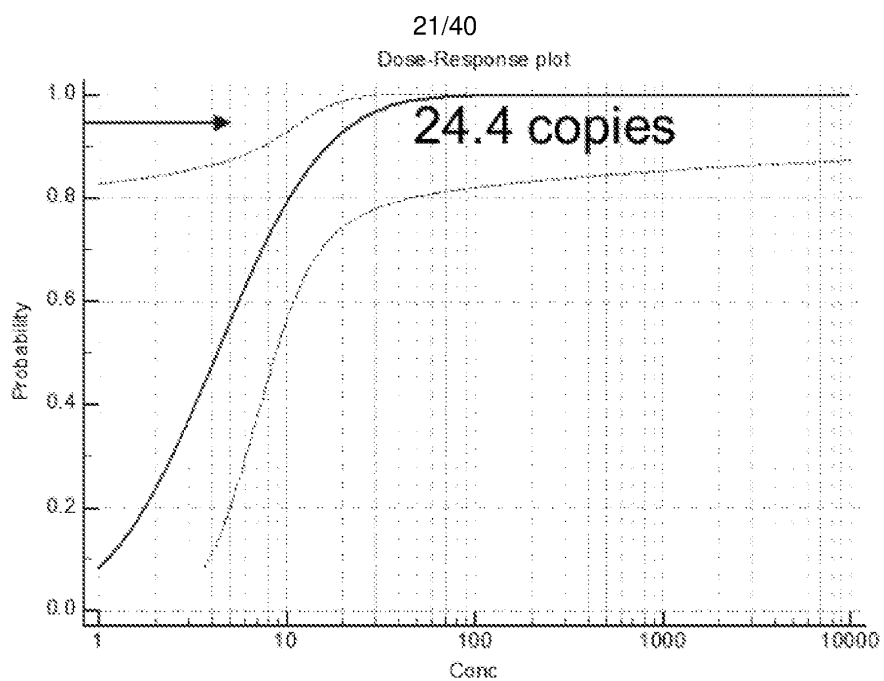


Fig. 15

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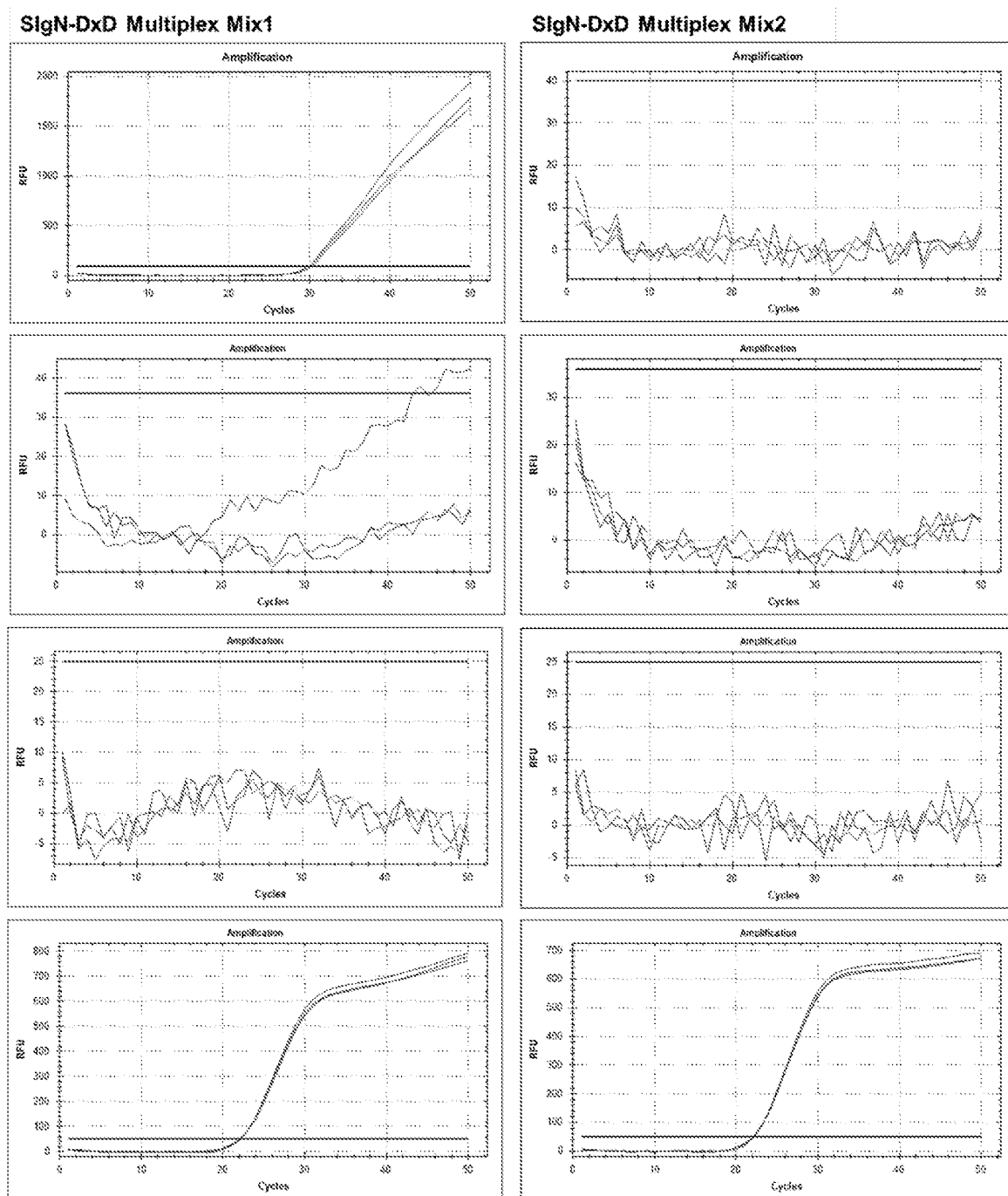
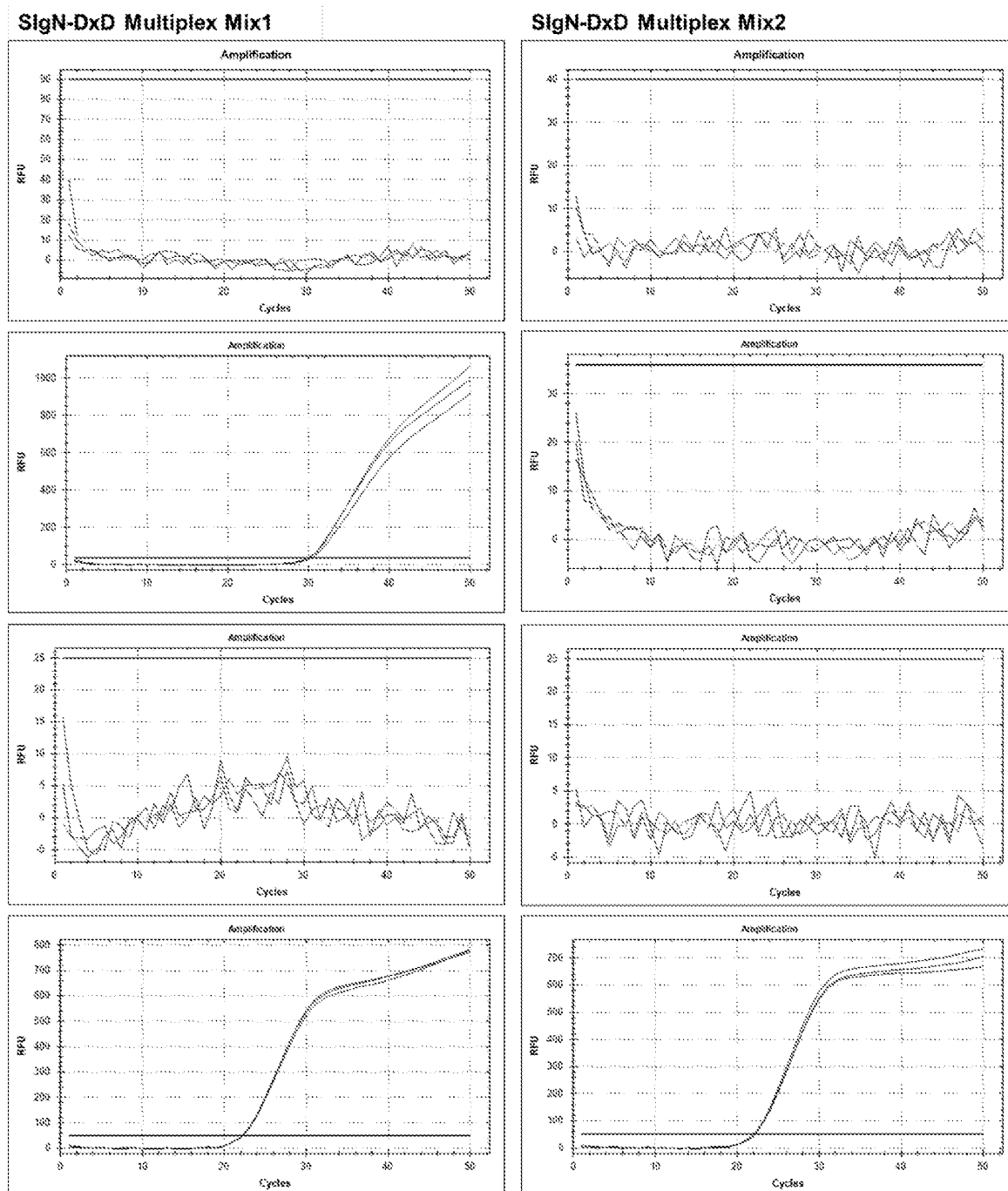


Fig. 16A



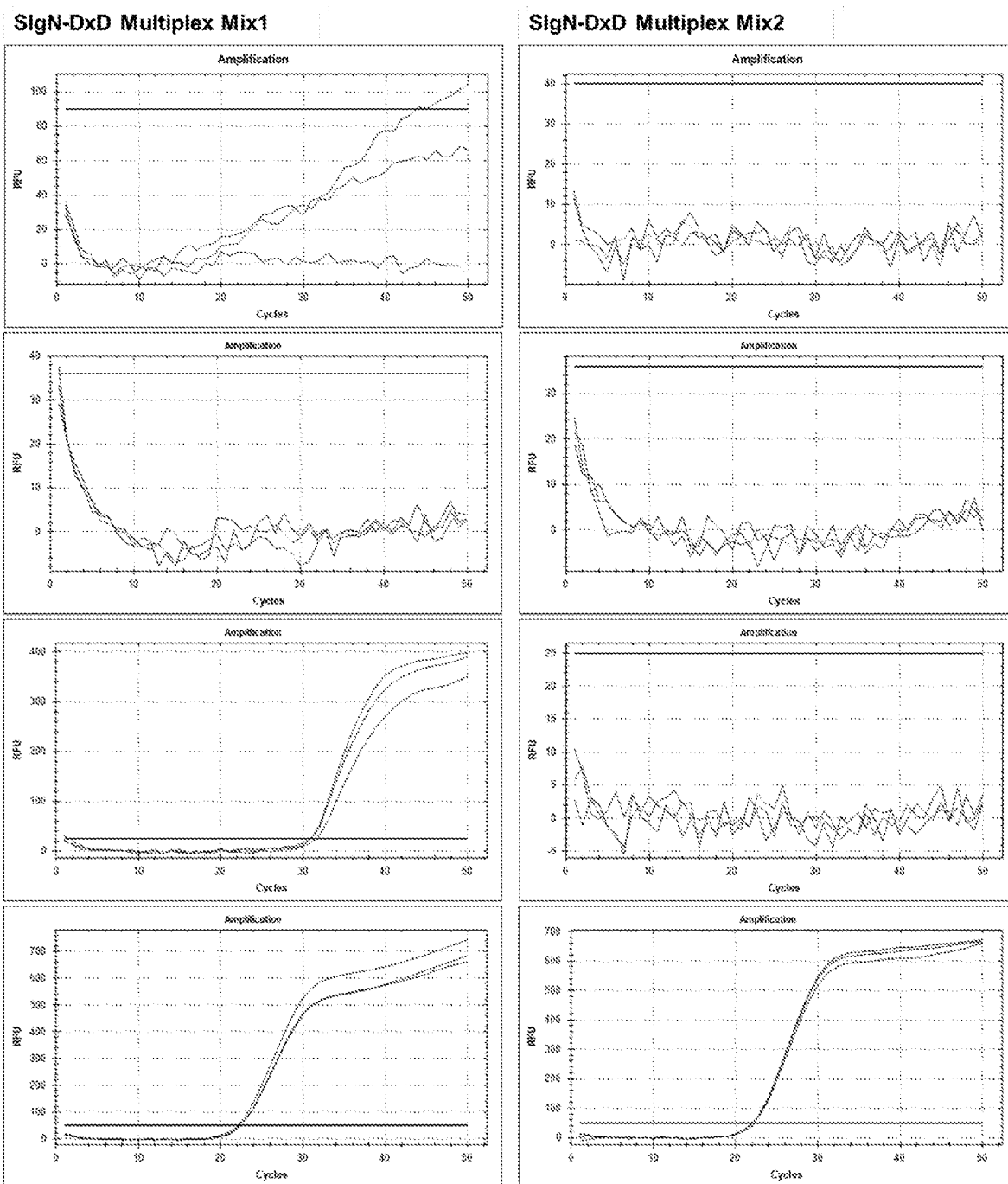
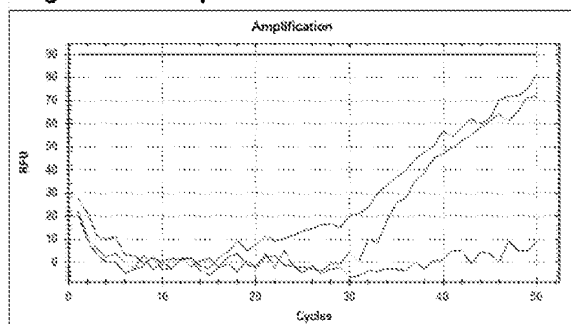


Fig. 16C

SigN-DxD Multiplex Mix1



SigN-DxD Multiplex Mix2

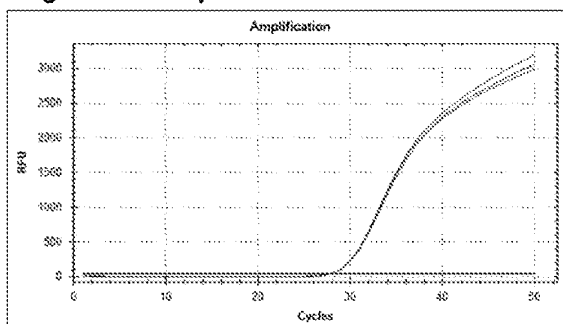
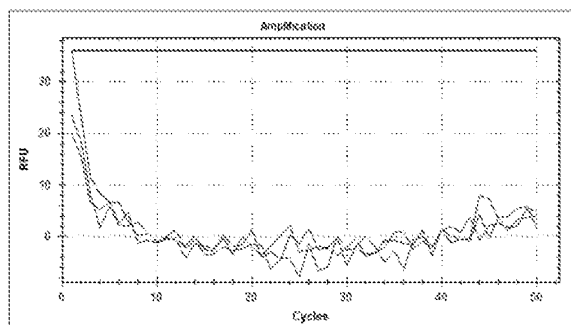
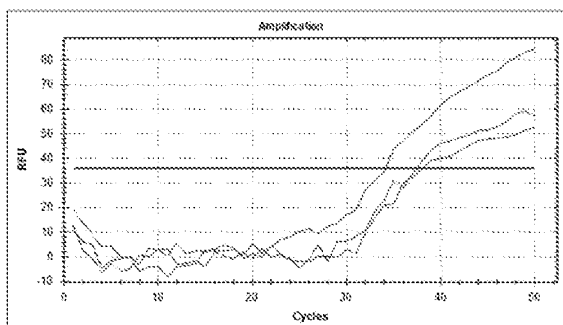
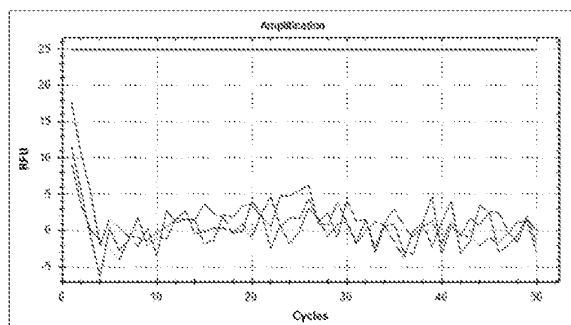
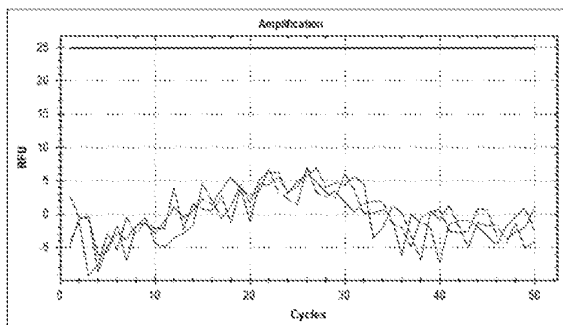
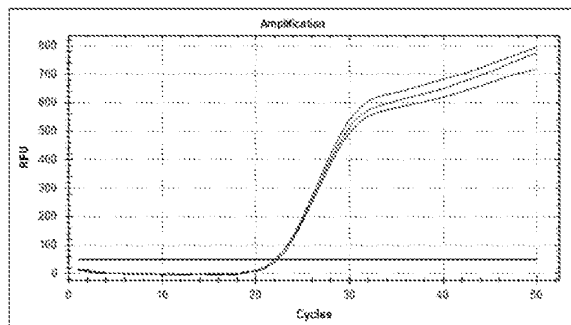
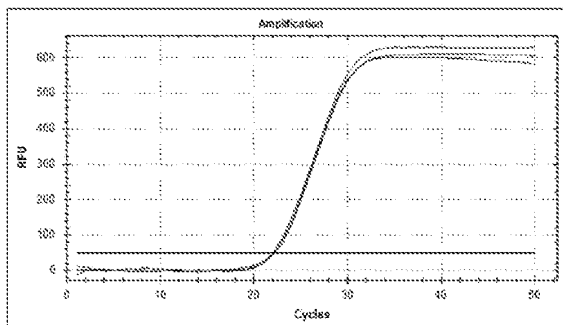


Fig. 16D

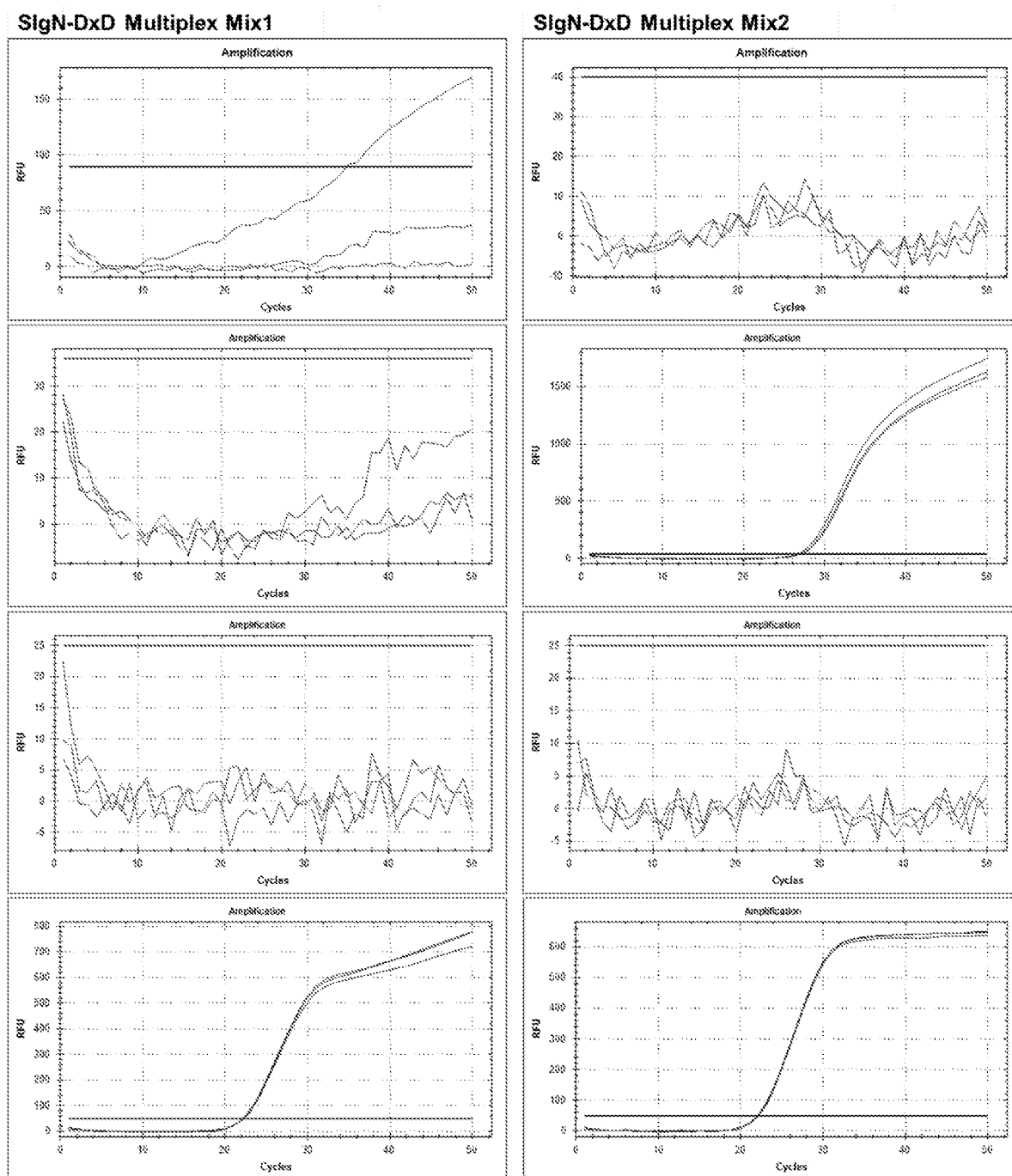
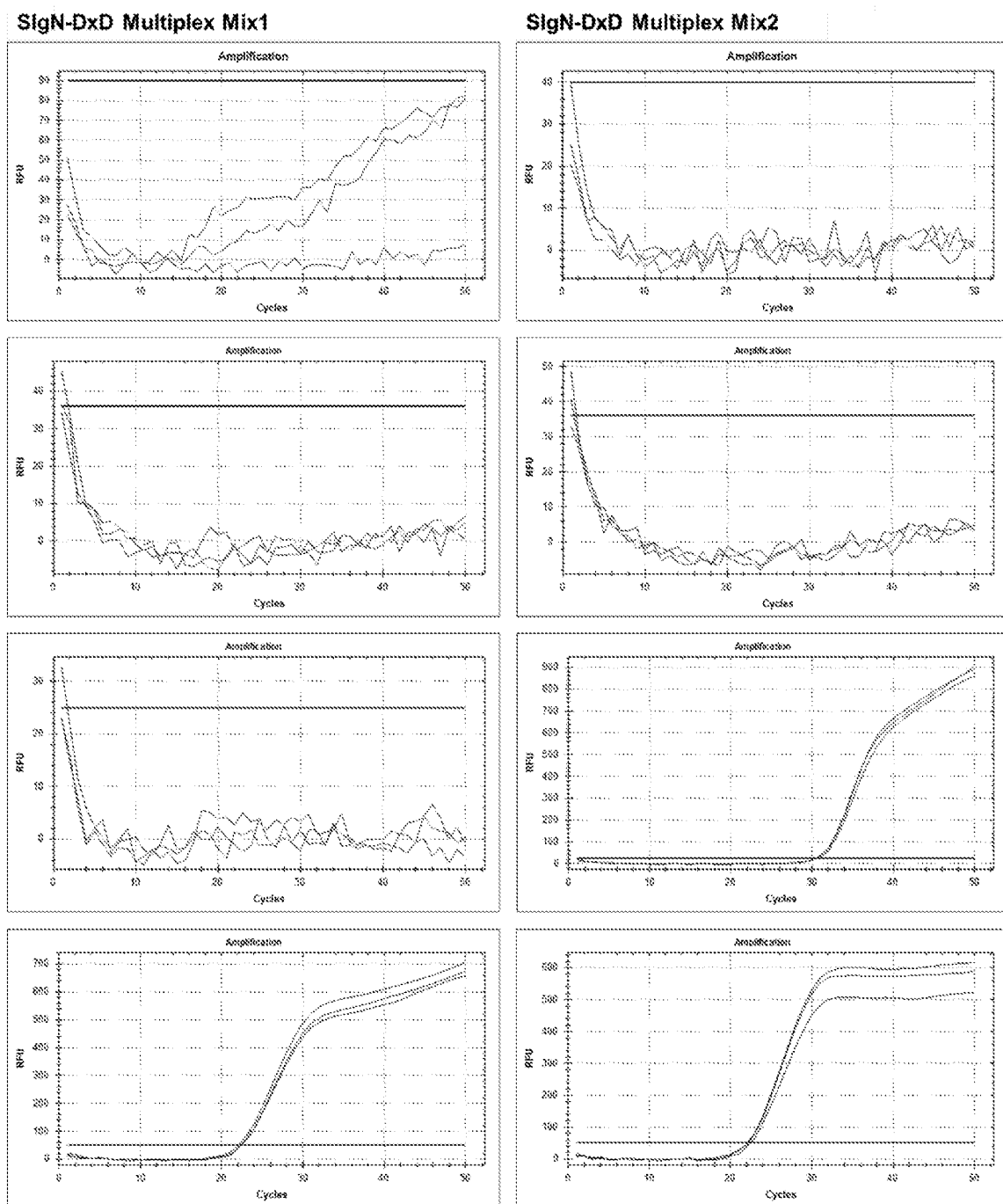


Fig. 16E

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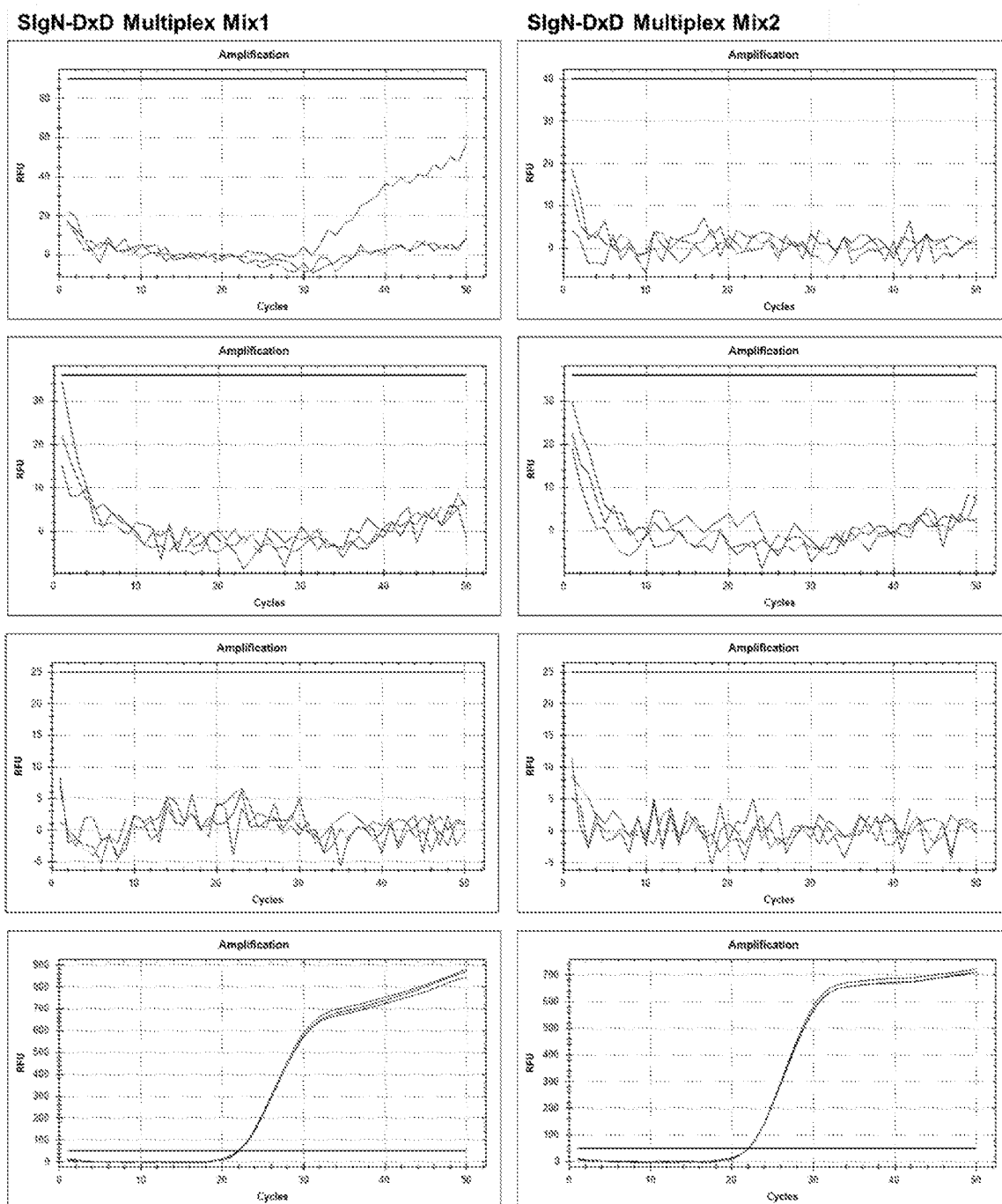
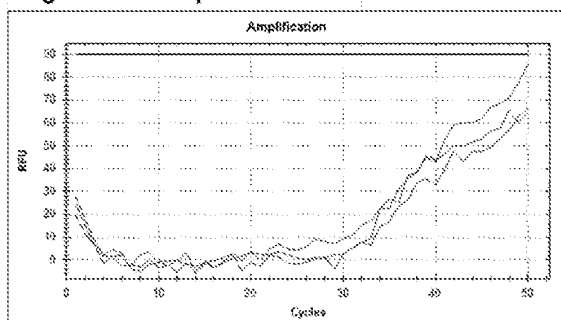


Fig. 16G

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SigN-DxD Multiplex Mix1



SigN-DxD Multiplex Mix2

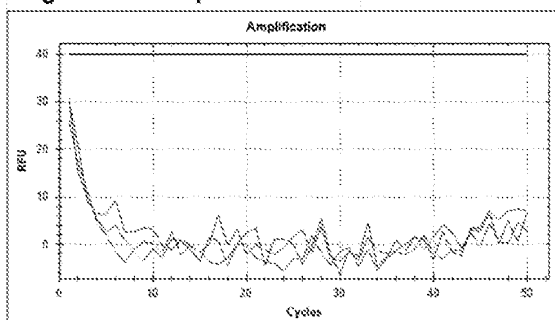
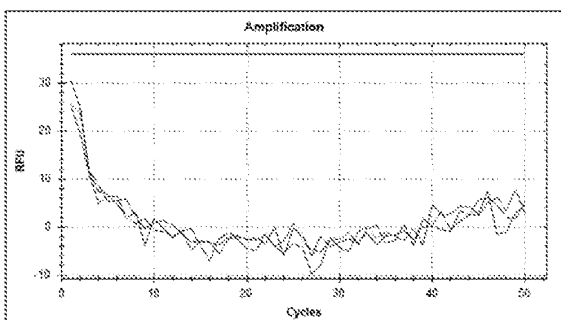
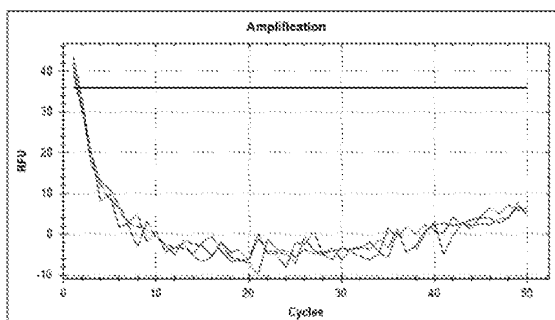
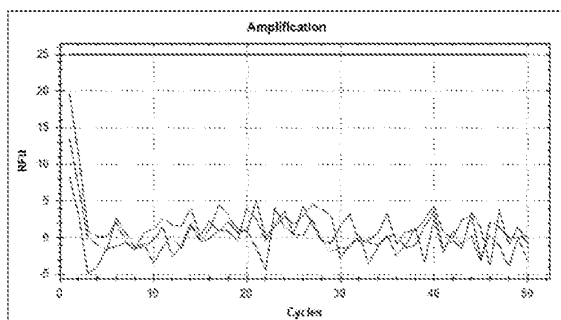
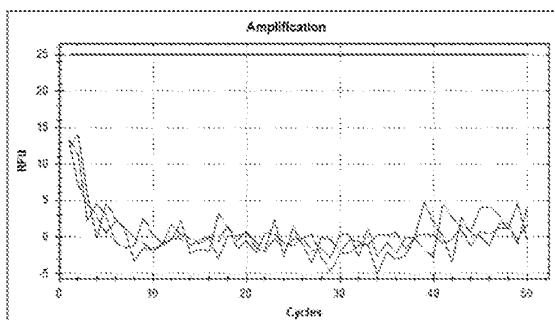
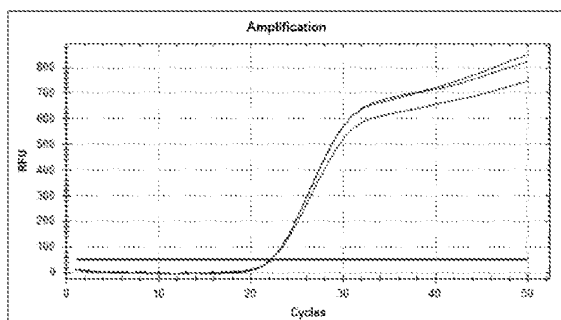
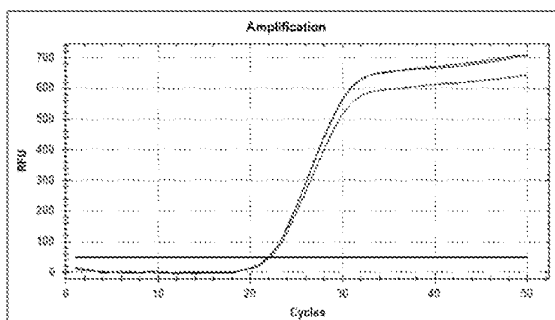
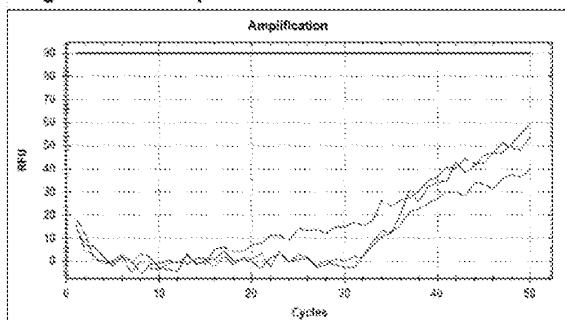


Fig. 16H

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SigN-DxD Multiplex Mix1



SigN-DxD Multiplex Mix2

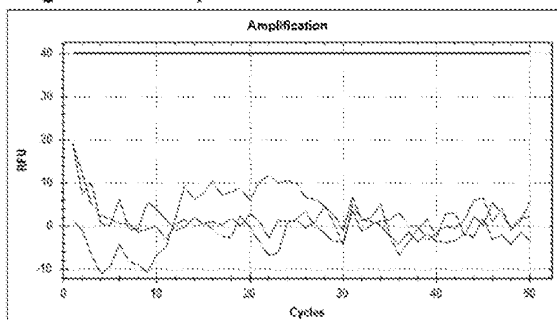
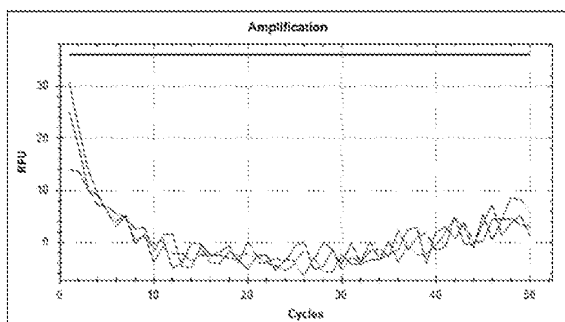
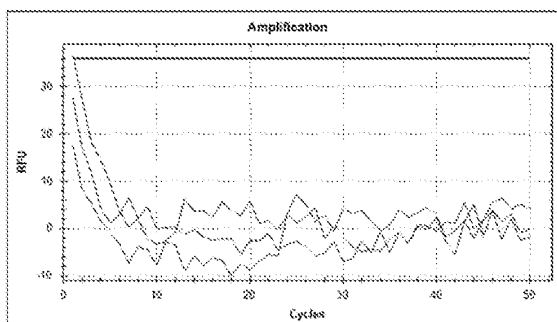
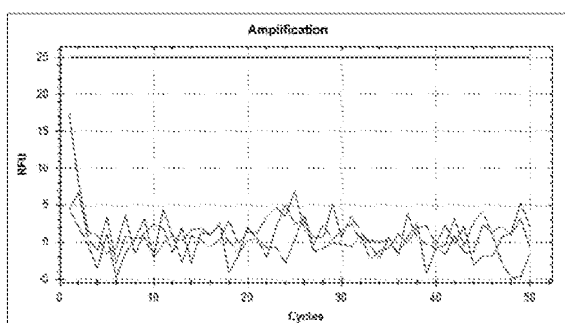
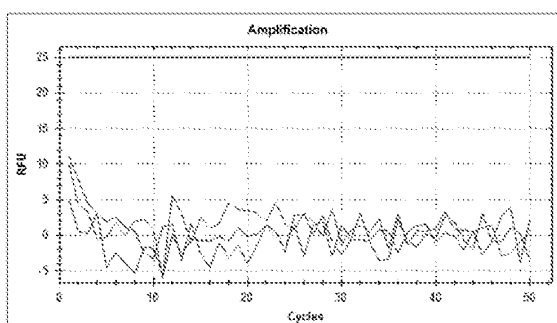
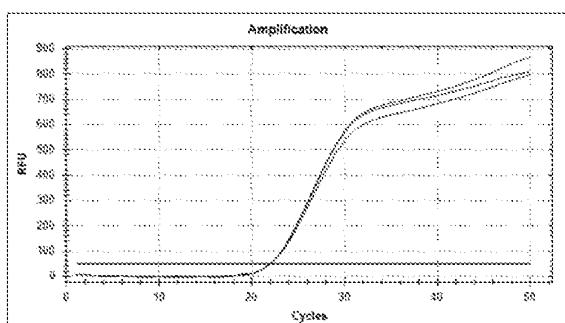
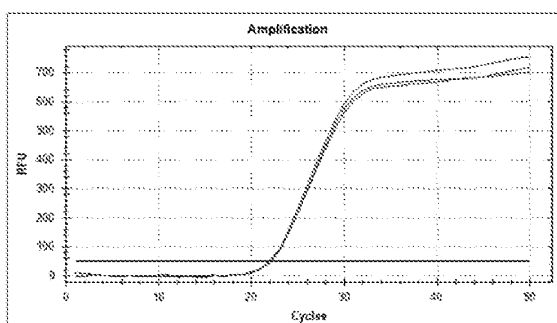


Fig. 16I

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FAM channel to detect ZIKV

Cy5 channel for IC

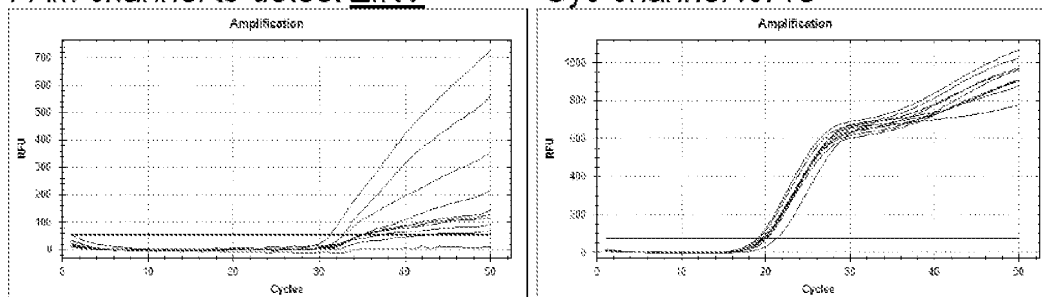


Fig. 17A

Cy5 channel for IC

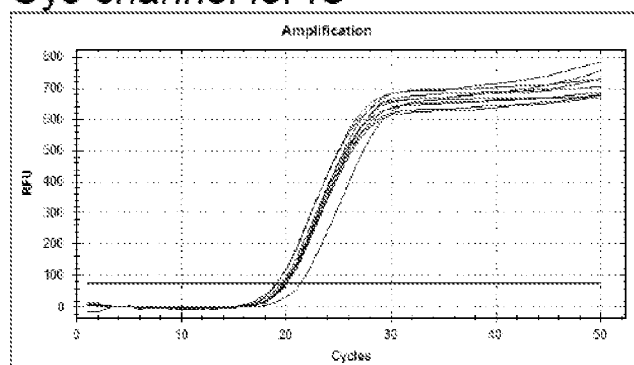


Fig. 17B

5

Hex channel to detect DENV1

Cy5 channel for IC

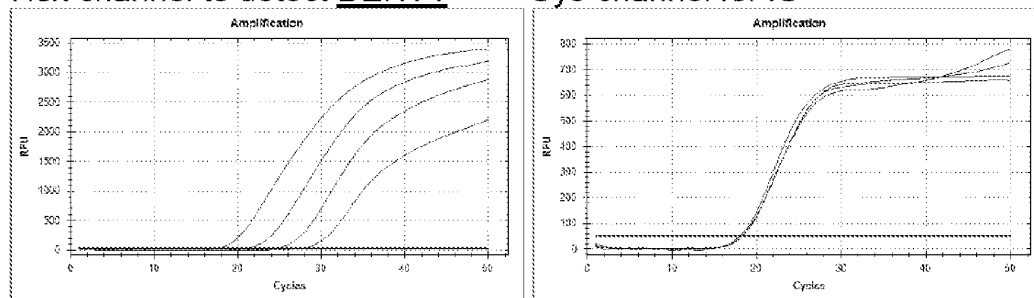


Fig. 18A

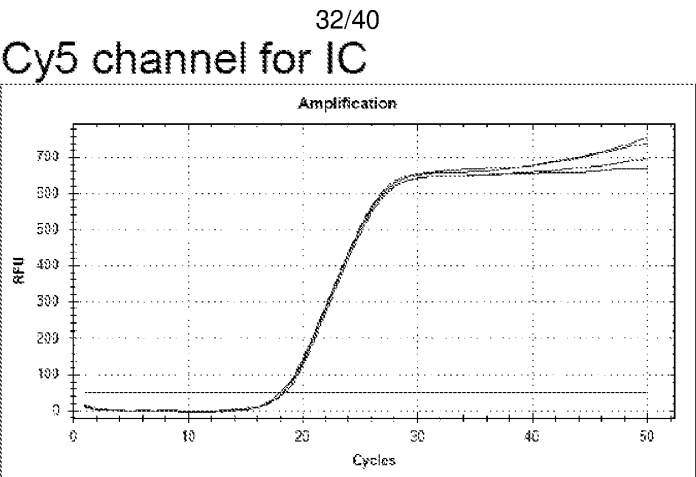


Fig. 18B

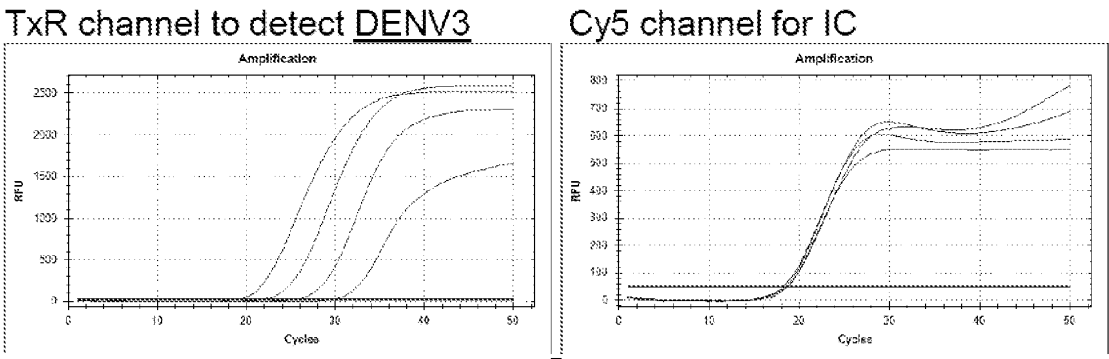


Fig. 19A

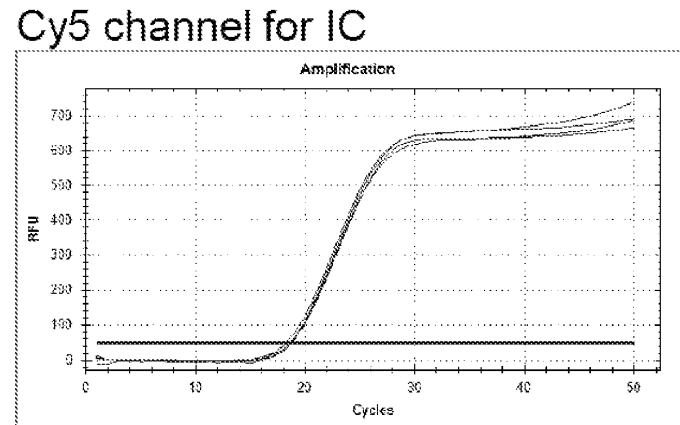


Fig. 19B

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FAM channel to detect CHIKV

Cy5 channel for IC

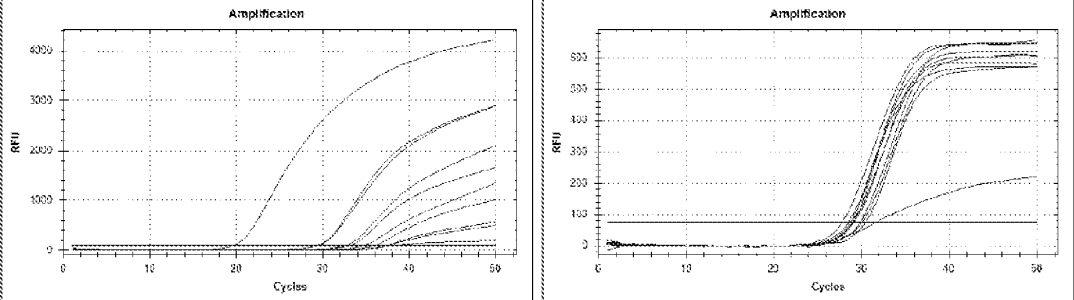


Fig. 20A

Cy5 channel for IC

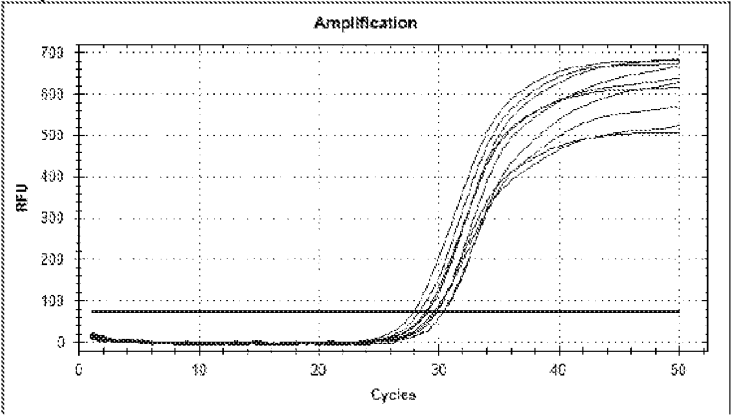


Fig. 20B

5

Hex channel to detect DENV2

Cy5 channel for IC

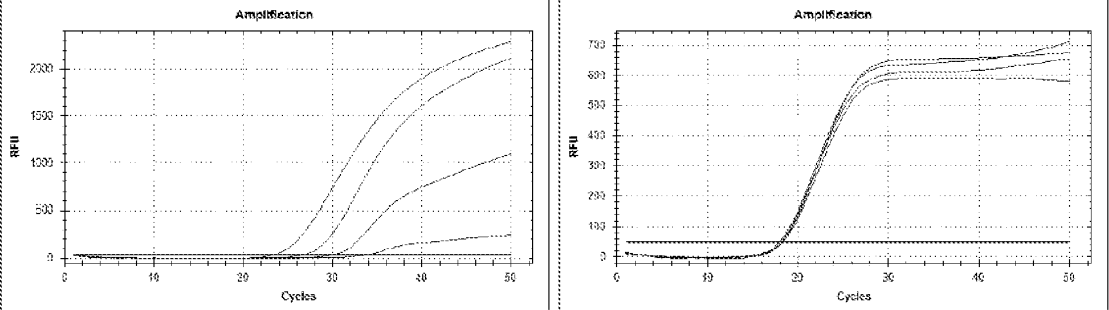


Fig. 21A

10

Cy5 channel for IC

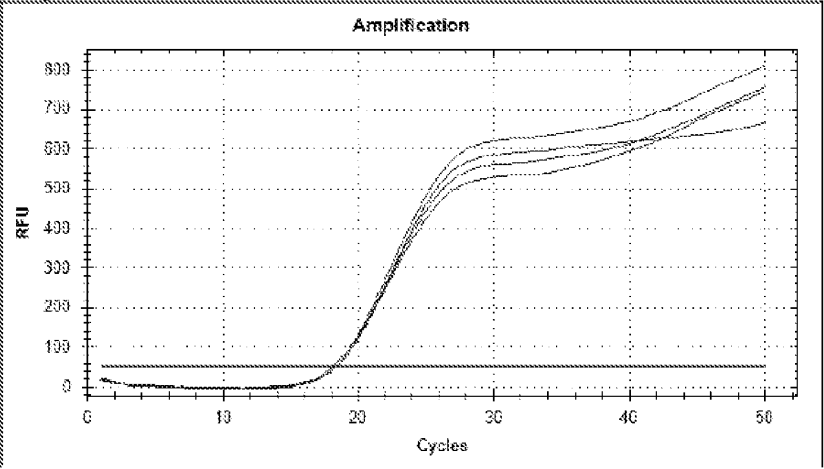
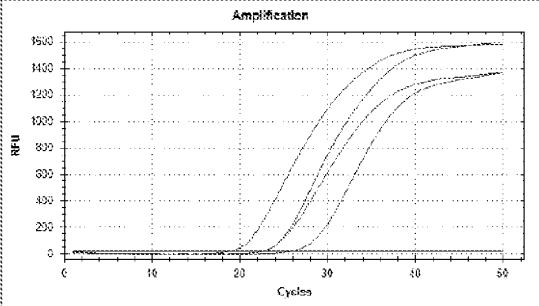


Fig. 21B

5

TxR channel to detect DENV4



Cy5 channel for IC

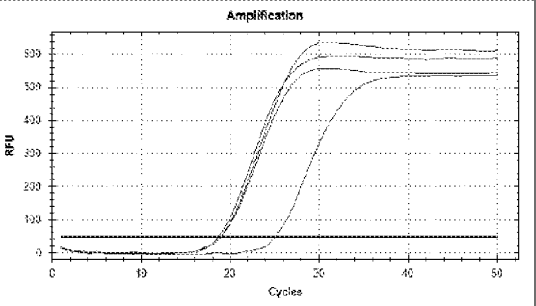


Fig. 22A

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Cy5 channel for IC

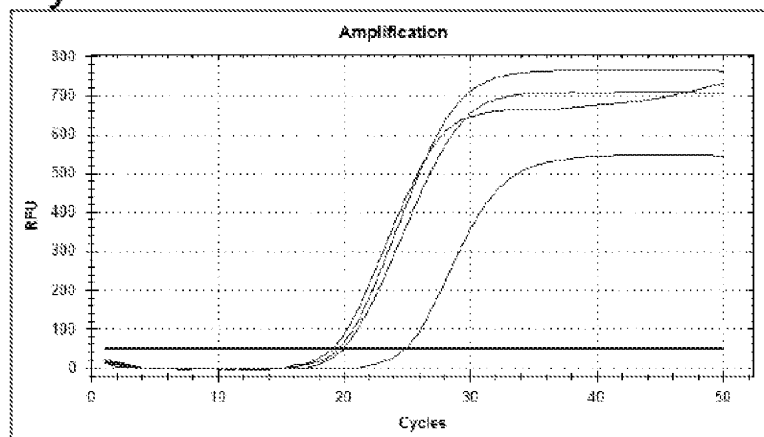


Fig. 22B

CHINA E1 Consensus Final 22Jun2017

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Fig. 23

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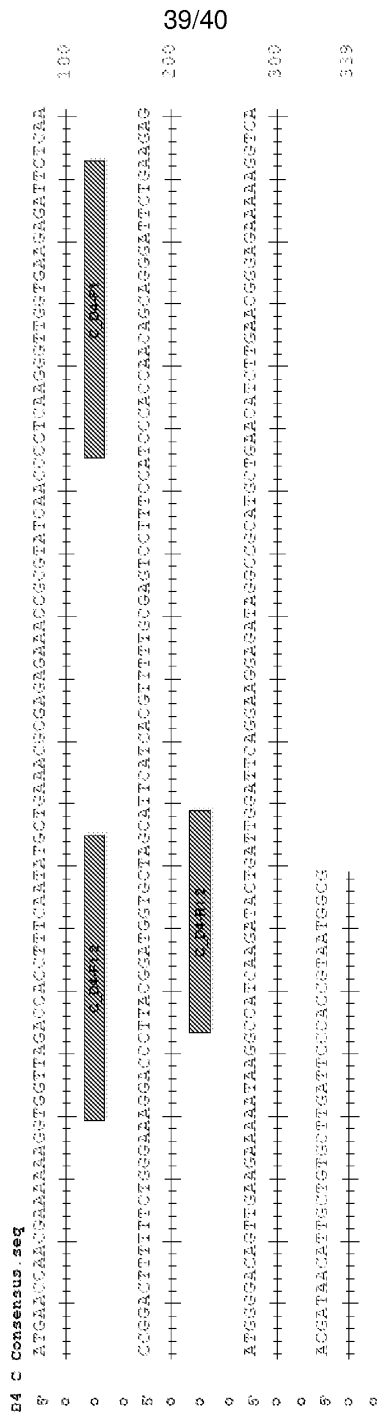
Fig. 24

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Fig. 26



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Fig. 28

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