

CONTROL CIRCUIT AND SEMICONDUCTOR MEMORY

ABSTRACT

The embodiments of the present disclosure provide a control circuit and a semiconductor memory. The control circuit includes a bias module. The bias module is configured to provide a bias current for a functional module. The bias module includes a first bias module and a second bias module. The first bias module is configured to provide a first bias current, and the second bias module is configured to provide a second bias current. Herein, the first bias current is smaller than the second bias current, the first bias module is configured to be in a normally open state after being powered on, and the second bias module is configured to receive a bias enabling signal and provide the second bias current based on the bias enabling signal.

[Fig. 3]