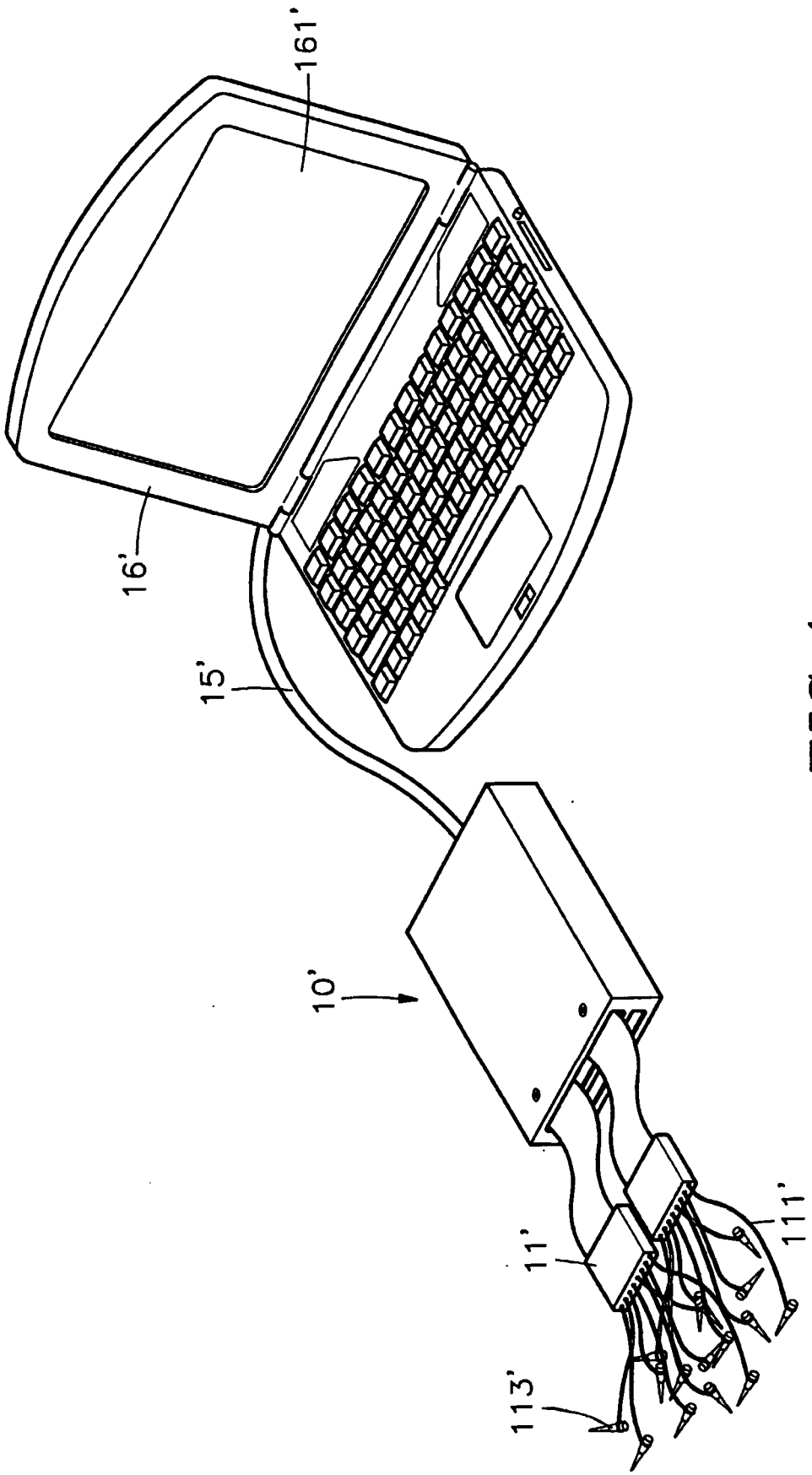


FIG. 1

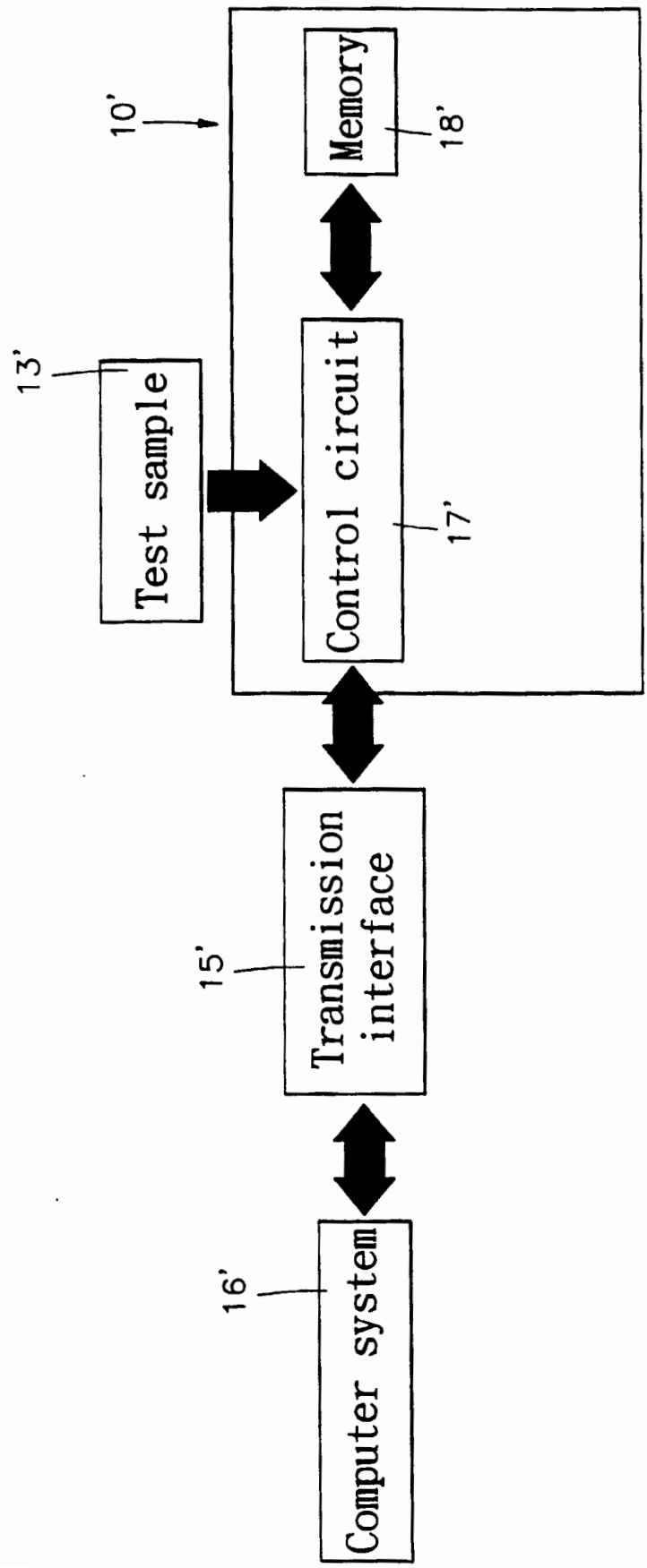

10'
Memory
18'
13'
Test sample
Control circuit
17'
15'
Transmission interface
16'
Computer system

FIG.2

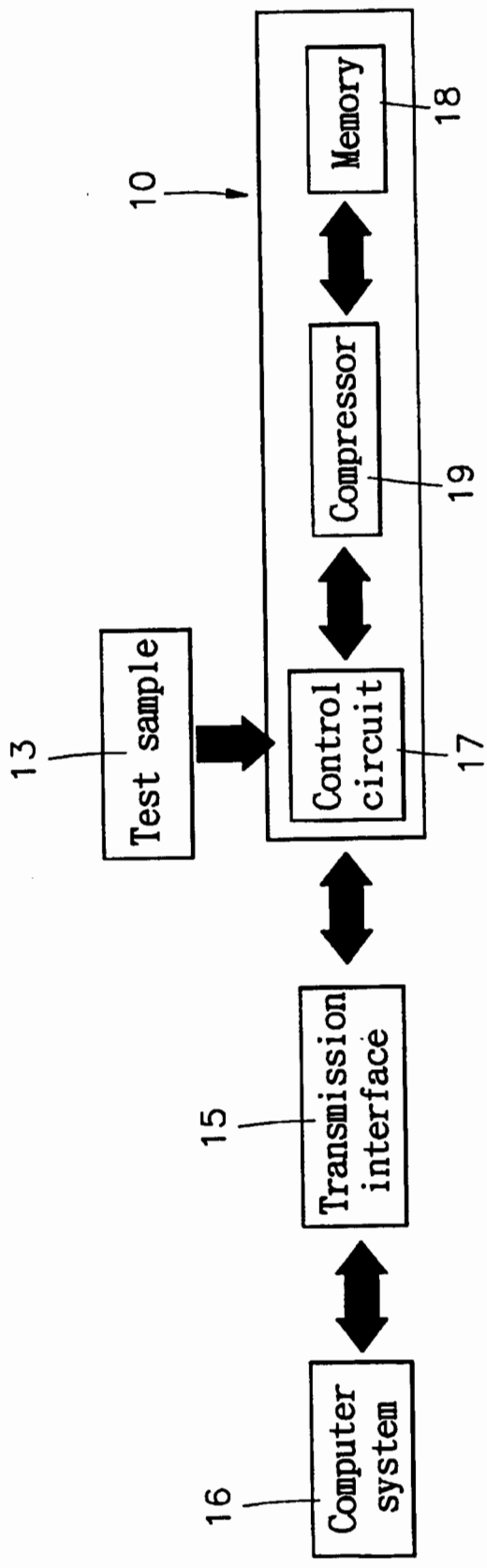

10
Memory
18
Compressor
19
Control circuit
17
13
Test sample
15
Transmission interface
16
Computer system

FIG. 3

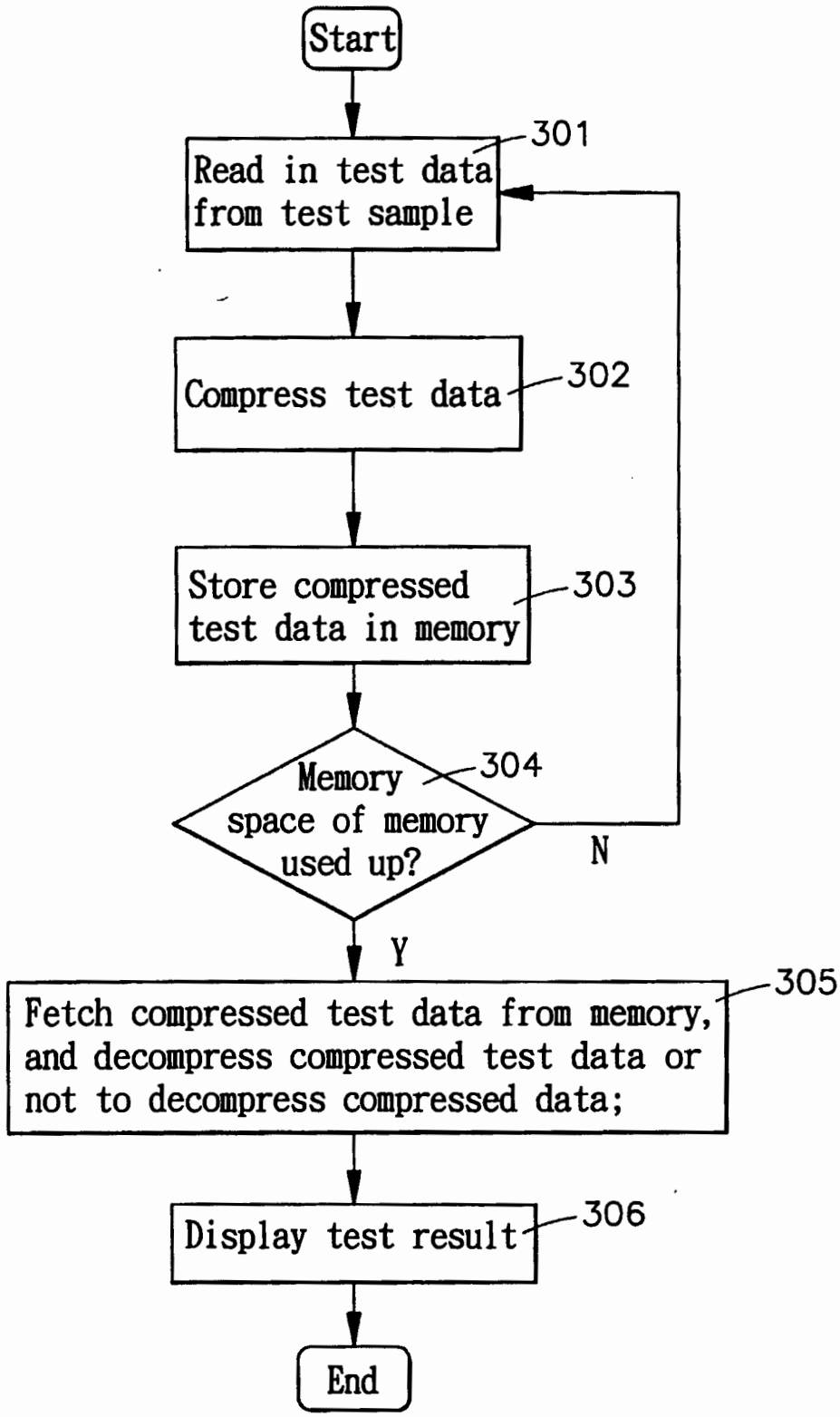

Start
Read in test data from test sample
301
Compress test data
302
Store compressed test data in memory
303
Memory space of memory used up?
304
N
Y
Fetch compressed test data from memory, and decompress compressed test data or not to decompress compressed data;
305
Display test result
306
End

FIG. 4

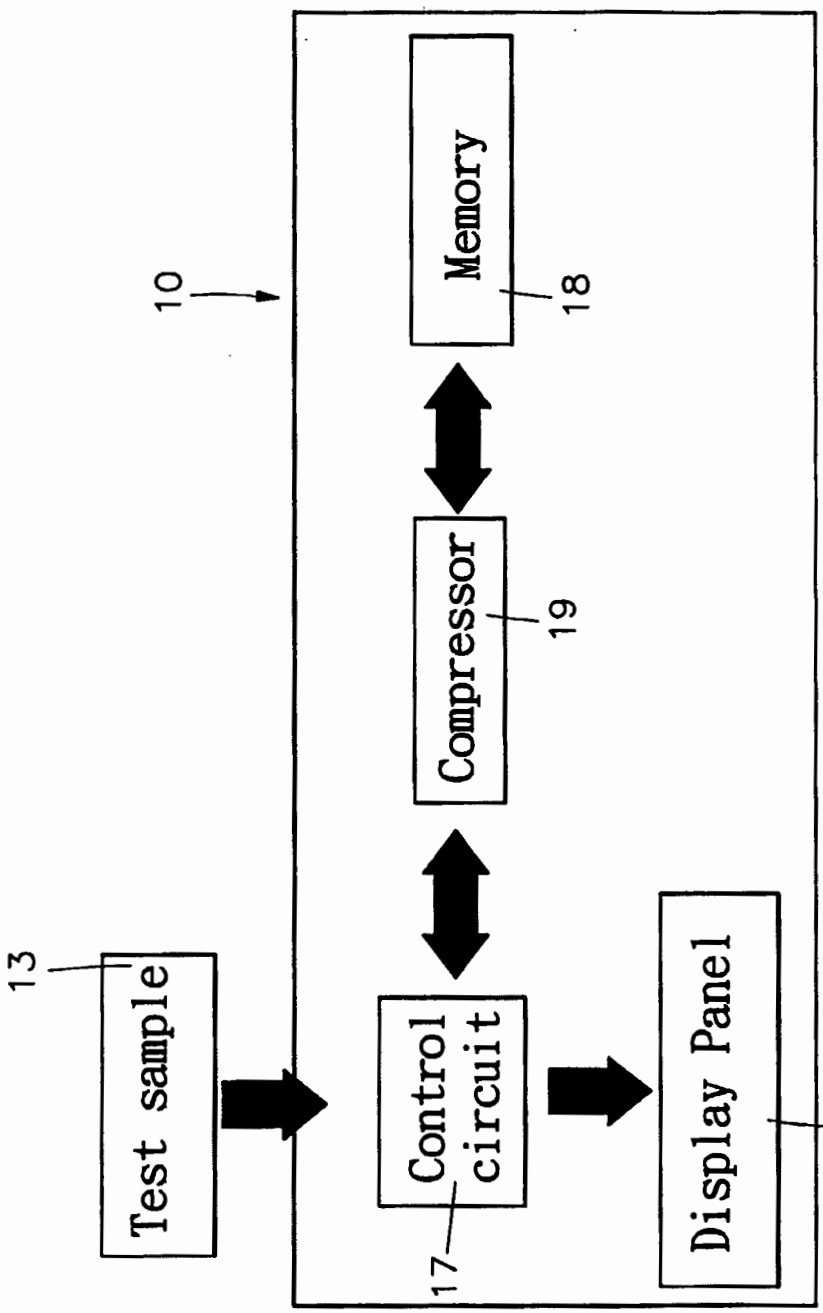

10
Memory
18
Compressor
19
Control circuit
17
Test sample
13
Display Panel
161

FIG.5