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[Continued on next page]

(54) Title: VECTOR EXCEPTION CODE

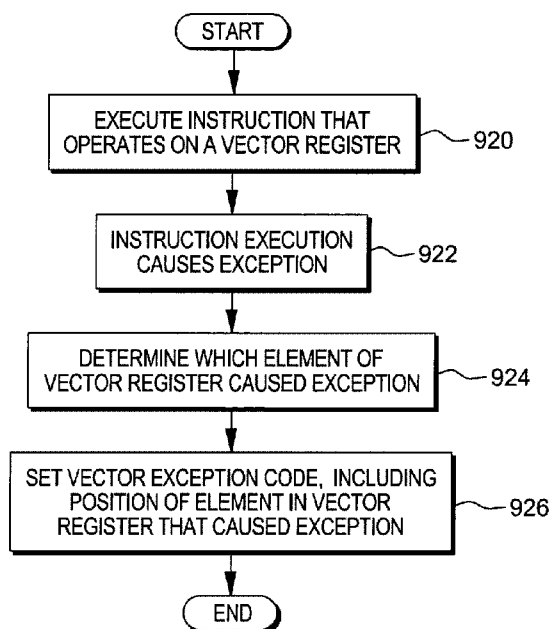


FIG. 9B

(57) Abstract: Vector exception handling is facilitated. A vector instruction is executed that operates on one or more elements of a vector register. When an exception is encountered during execution of the instruction, a vector exception code is provided that indicates a position within the vector register that caused the exception. The vector exception code also includes a reason for the exception.



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