CLAIMS

1. A thermosetting resin sheet containing an inorganic filler, an epoxy resin having a softening point of 50 to 130°C and a phenolic resin, and being in a B-stage state with a projected area of 17663 mm^2 or more as viewed in a plan view, wherein

the thermosetting resin sheet does not contain a liquid epoxy resin, and

an elastic modulus before heat curing is 2.5×10^3 Pa or more and 3×10^4 Pa or less in a temperature range of 90°C to 130°C.

2. The thermosetting resin sheet according to claim 1, wherein an average particle size of the inorganic filler is 54 μm or less.

3. The thermosetting resin sheet according to claim 1 or 2, wherein a content of the inorganic filler is 70 wt% or more and 95 wt% or less.

4. The thermosetting resin sheet according to any one of claims 1 to 3, containing an elastomer.

5. A thermosetting resin sheet wound body formed by winding a long body of the thermosetting resin sheet according to any one of claims 1 to 4 in a roll form.

6. A method for manufacturing an electronic component package, comprising:

a laminating step of laminating the thermosetting resin sheet according to any one of claims 1 to 4 onto one or plural electronic components so as to cover the one or plural electronic components; and

a sealed body forming step of forming a sealed body by heat-curing the thermosetting resin sheet,

wherein a ratio of a projected area of the electronic component as viewed in a plan view relative to a projected area of the thermosetting resin sheet as viewed in a plan view is 50% or more.

7. The method for manufacturing an electronic component package according to claim 6, wherein the laminating step is carried out by a thermal pressing process.

8. The method for manufacturing an electronic component package according to claim 6 or 7, further comprising a dicing step of forming an electronic component module by dicing the sealed body.

9. The method for manufacturing an electronic component package according to any one of claims 6 to 8, wherein the electronic component is a semiconductor chip or a semiconductor wafer.