

(43) International Publication Date
29 October 2015 (29.10.2015)

- (51) International Patent Classification:
G06F 3/06 (2006.01) *G06F 21/70* (2013.01)
- (21) International Application Number:
PCT/US2015/027242
- (22) International Filing Date:
23 April 2015 (23.04.2015)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:
61/983,430 23 April 2014 (23.04.2014) US
- (71) Applicant: ENSCONCE DATA TECHNOLOGY, LLC.
[US/US]; 120 Aviation Avenue, P.O. Box 240, Portsmouth, NH 03802 (US).
- (72) Inventors: CASPERSON, Daniel, H.; 27 Taft Road, Portsmouth, NH 03801 (US). MACKENSEN, David, Christopher; 90 Cutter Road, Temple, NH 03084 (US).
- (74) Agent: WALSH, Edmund, J.; Wolf, Greenfield & Sacks, P.C., 600 Atlantic Avenue, Boston, MA 02210-2206 (US).

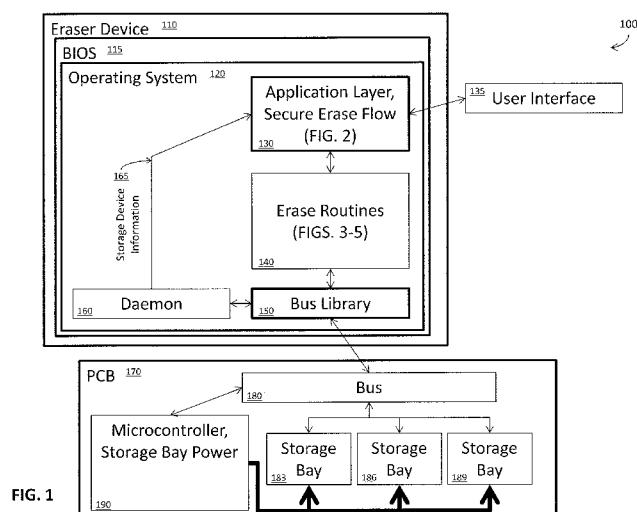
(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IR, IS, JP, KE, KG, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LI, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

Published:

— with international search report (Art. 21(3))

(54) Title: METHOD FOR COMPLETING A SECURE ERASE OPERATION



(57) Abstract: A system that may reliably erase a storage device, such as a solid state drive. The system issues an erasure command to the storage device. Such a command may be issued over a bus connecting a processing unit to one or more storage devices to be erased. The system, including the storage device, may be prepared for the erasure operation via performing one or more operations. Those operations may include: using hardware of the system to initiate a hard reset of the storage device; preventing access to the storage device while the erasure operation is being performed; and/or erasing hidden areas on the storage device. The system may be configured to perform the hard reset and may be configured not to alter a command to perform secure erase. Further, the erasure process may include writing a signature to certain areas of the storage device to confirm that erasure was performed.