

ABSTRACT

FAULT TOLERANT DATA PROCESSING SYSTEMS

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A clock signal source is described for providing three mutually independent clock signals to three processing subsystems of a triplicated data processing system.

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The source comprises a master reference oscillator 10, a slave reference oscillator 12 phase locked to the master oscillator 10 and three mutually independent clock circuits 14a to 14c. Each of the clock phase circuits 14 is phase locked to the output signal of the reference oscillator 10 when it is functioning correctly, or to the slave reference oscillator 12 if a malfunction is detected in the master reference oscillator 10.

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Figure 1