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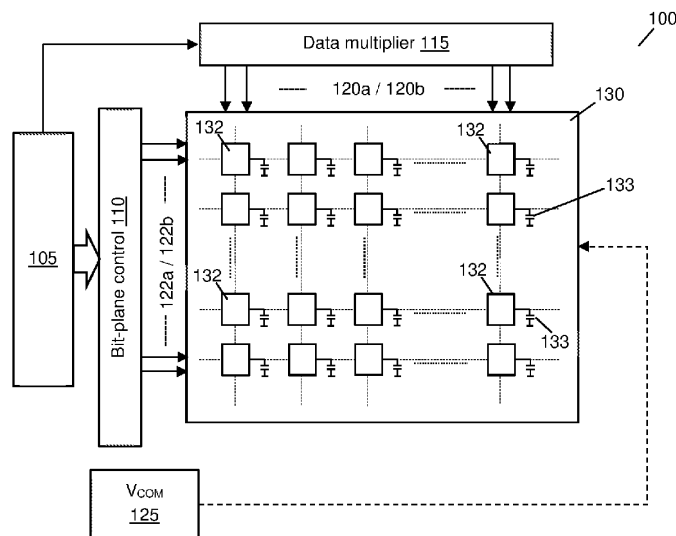


FIGURE 1

(57) Abstract: This document describes a spatial light modulator that comprises a pixel array having a plurality of pixels whereby each pixel in this array is communicatively coupled to a digital pixel circuit which are in turn all coupled to a bit-plane control circuit. The bit-plane control circuit is configured to generate, using a Pulse Code Modulation (PCM) algorithm, bit-plane signals that are used to control the sample and hold functions performed by each of the digital pixel circuits so that the pixel array may carry out its scanning function accurately and in a power efficient manner.

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