

DATA PATH INTERFACE CIRCUIT, MEMORY AND STORAGE SYSTEM

ABSTRACT

A data path interface circuit, a memory, and a storage system. The data path interface circuit comprises: a write path module (100), which is separately connected to an internal port (12) and an external port (11), and is used to transfer storage data from the external port (11) to the internal port (12); a read path module (200), which is separately connected to the internal port (12) and the external port (11), and is used to transfer storage data from the internal port (12) to the external port (11); a first delay module (300), which is separately connected to the external port (11) and internal port (12), and is used to obtain storage data from the external port (11) or the internal port (12), perform delay processing on the storage data, and transfer the processed storage data to the write path module (100) and/or the read path module (200); a delay control module (400), which is connected to the first delay module (300), and is used to receive an externally inputted signal instruction, and according to the signal instruction, control the delay time for the first delay module (300) to perform the delay processing.

[Fig. 1]