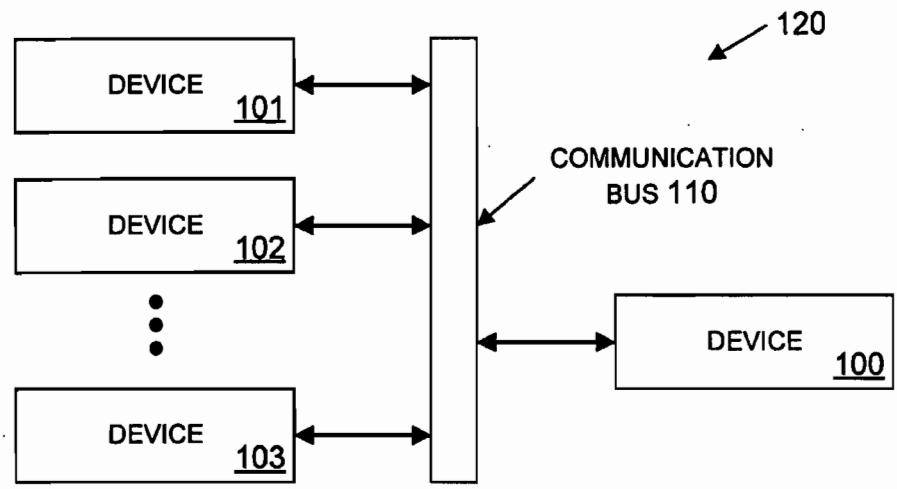


FIG. 1

CHAN	DEVICE-SPECIFIC ADDRESS	BROADCAST ADDRESS
0	EOM (END OF MESSAGE)	RFU (RESERVED FOR FUTURE USE)
1	DATA CHANNEL 1	RFU
2	DATA CHANNEL 2	RFU
3	DATA CHANNEL 3	RFU
4	DATA CHANNEL 4	RFU
5	DATA CHANNEL 5	RFU
6	DATA CHANNEL 6	RFU
7	DATA CHANNEL 7	RFU
8	RFU	ENABLE REQ
9	RFU	DISABLE REQ
10	SNOOP	RFU
11	IDLE	RFU
12	WAKE-UP	RFU
13	VIRTUAL GPIO CHANNEL	RFU
14	BOA (BEGINNING OF ARBITRATION)	RFU
15	RFU	EOA (END OF ARBITRATION)

FIG. 2

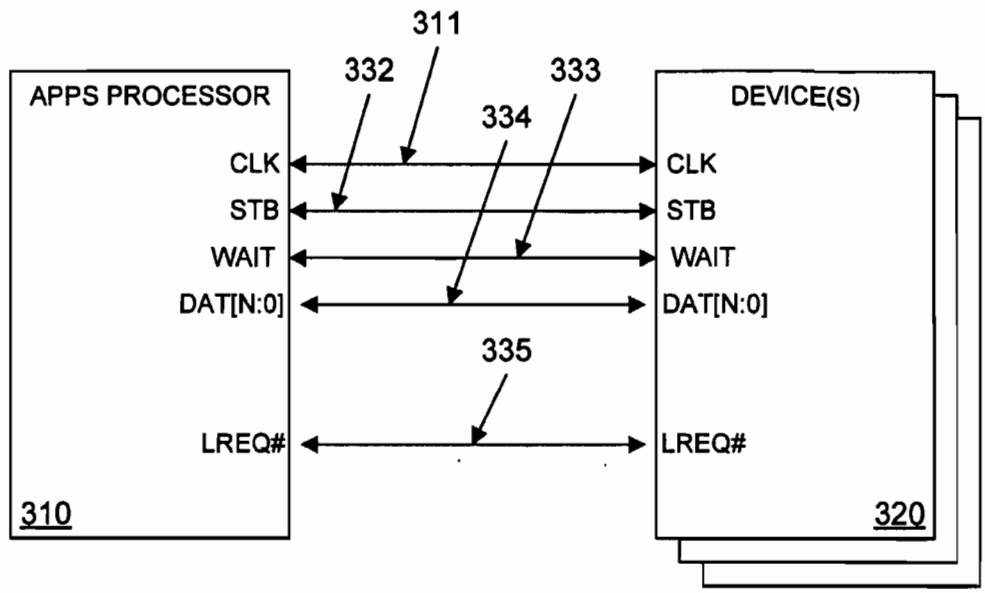

311
332
333
334
335
APPS PROCESSOR
DEVICE(S)
CLK
STB
WAIT
DAT[N:0]
LREQ#
CLK
STB
WAIT
DAT[N:0]
LREQ#
310
320

FIG. 3

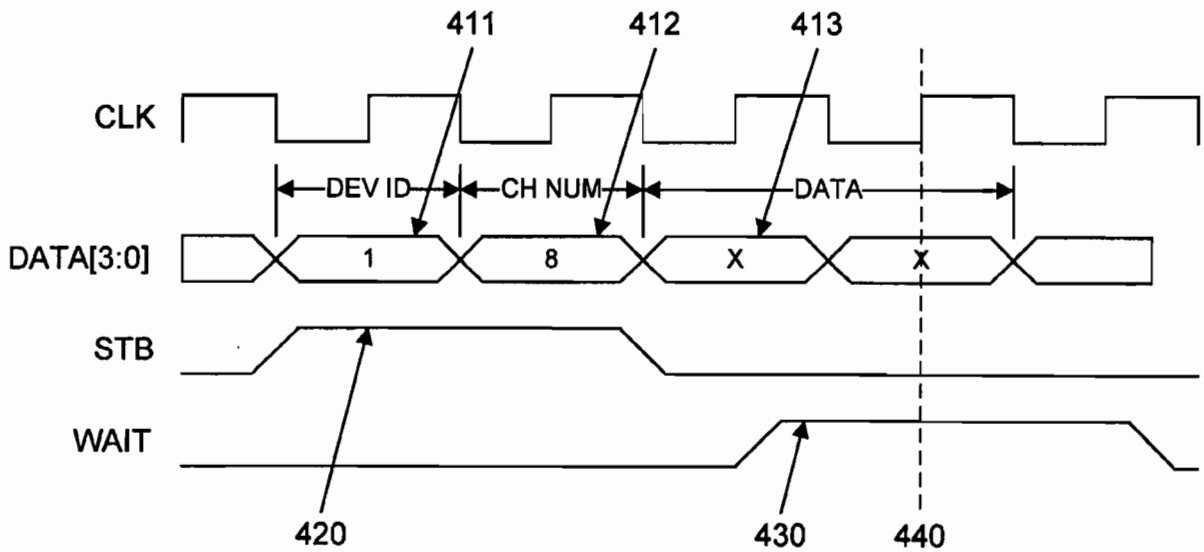

411
412
413
CLK
DEV ID
CH NUM
DATA
DATA[3:0]
1
8
X
X
STB
WAIT
420
430
440

FIG. 4

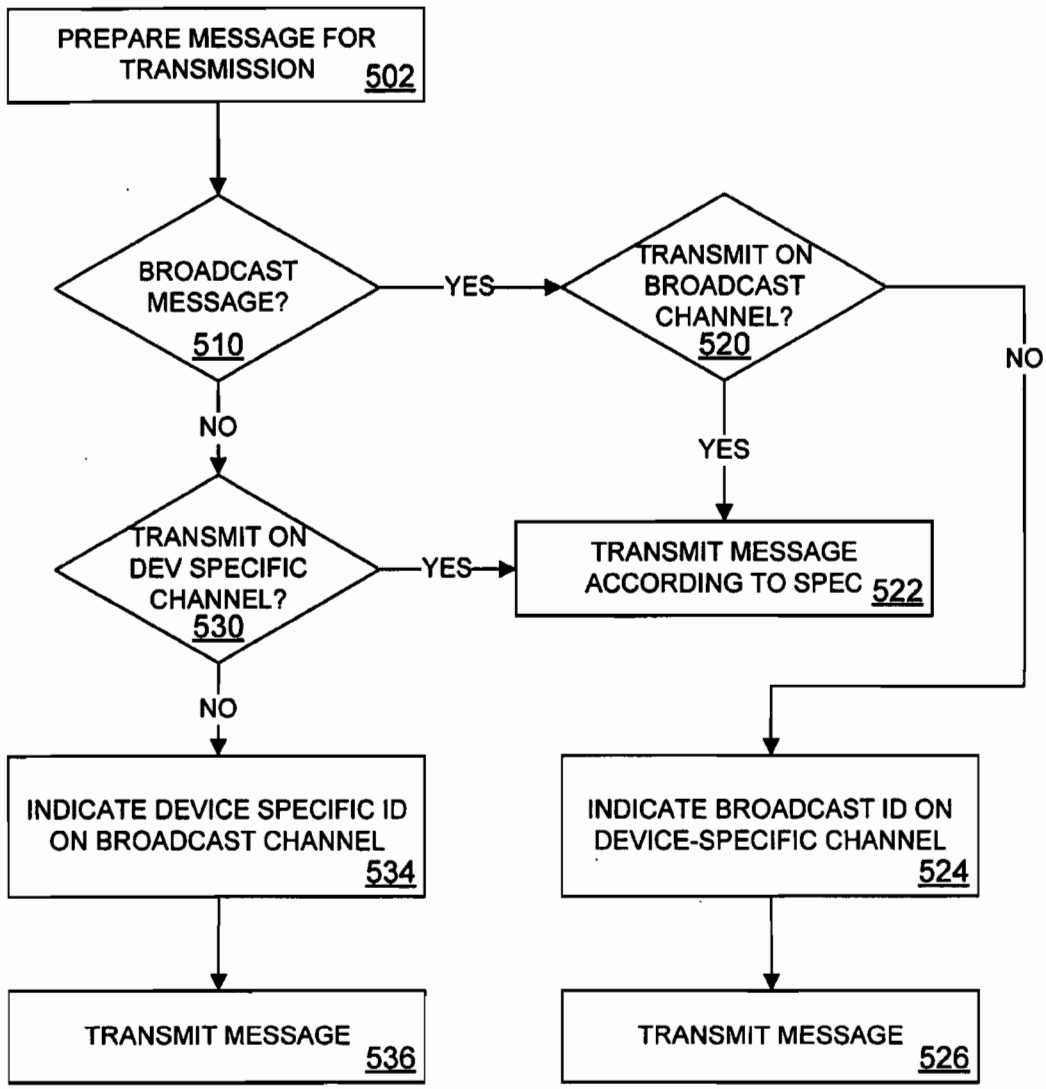

PREPARE MESSAGE FOR TRANSMISSION 502
BROADCAST MESSAGE? 510
YES
TRANSMIT ON BROADCAST CHANNEL? 520
NO
NO
YES
TRANSMIT ON DEV SPECIFIC CHANNEL? 530
YES
TRANSMIT MESSAGE ACCORDING TO SPEC 522
NO
INDICATE DEVICE SPECIFIC ID ON BROADCAST CHANNEL 534
INDICATE BROADCAST ID ON DEVICE-SPECIFIC CHANNEL 524
TRANSMIT MESSAGE 536
TRANSMIT MESSAGE 526

FIG. 5

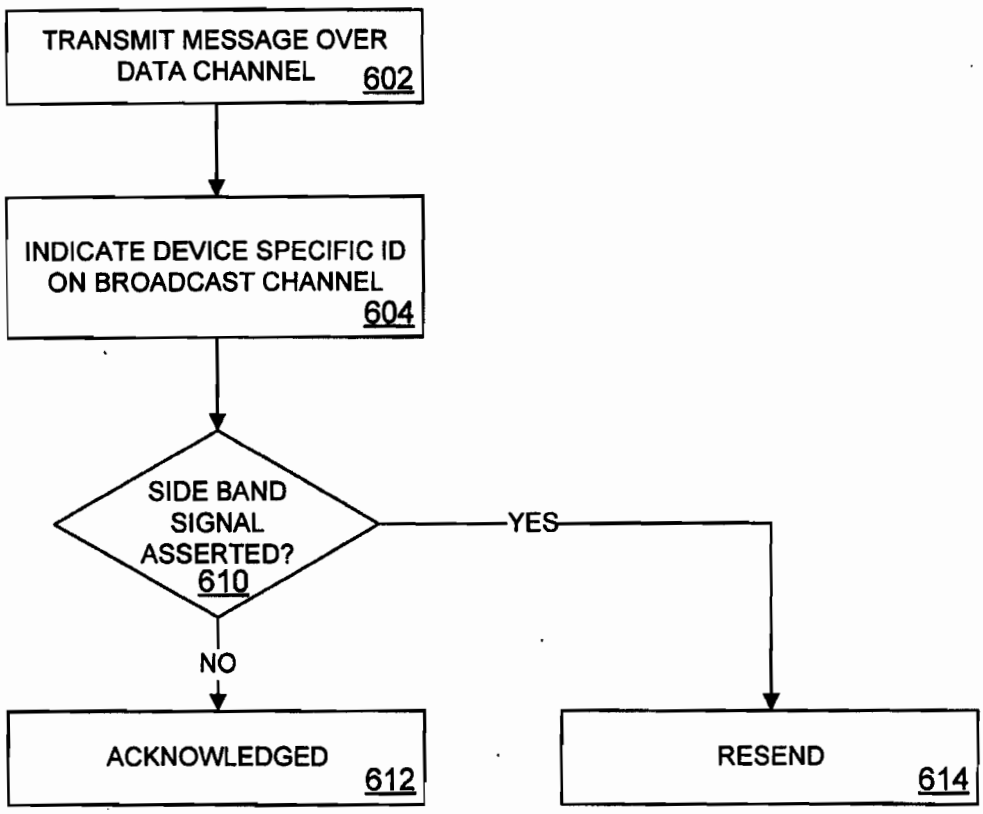

TRANSMIT MESSAGE OVER DATA CHANNEL 602
INDICATE DEVICE SPECIFIC ID ON BROADCAST CHANNEL 604
SIDE BAND SIGNAL ASSERTED? 610
YES
NO
ACKNOWLEDGED 612
RESEND 614

FIG. 6

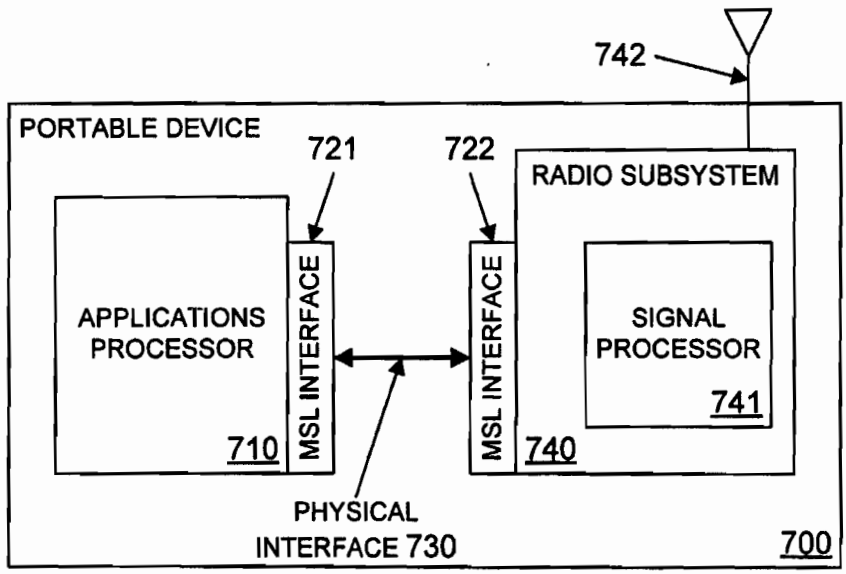

742
PORTABLE DEVICE
721
722
RADIO SUBSYSTEM
APPLICATIONS PROCESSOR
710
MSL INTERFACE
MSL INTERFACE
740
SIGNAL PROCESSOR
741
PHYSICAL INTERFACE 730
700

FIG. 7

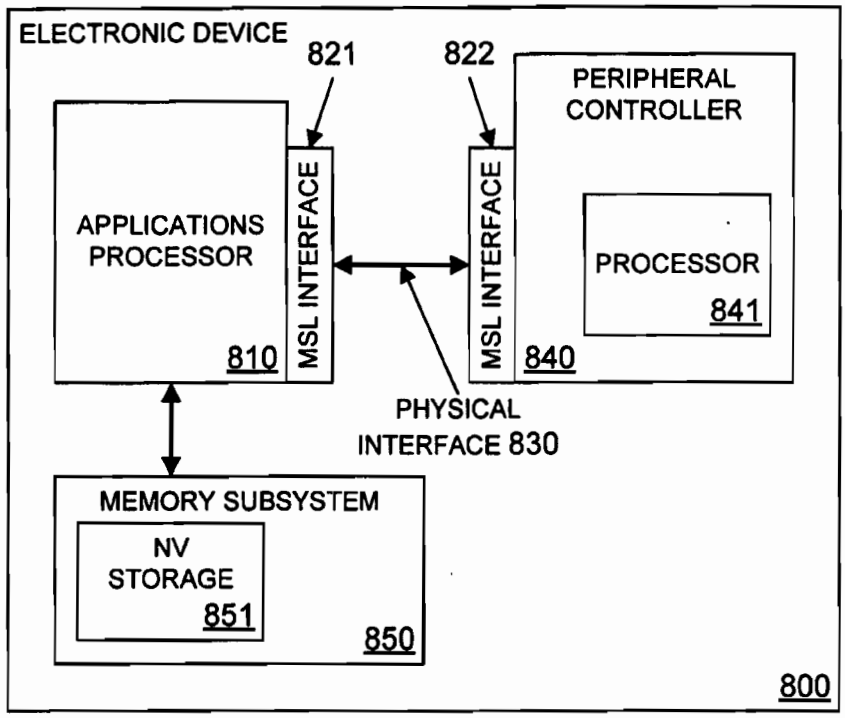

ELECTRONIC DEVICE
821
822
PERIPHERAL CONTROLLER
APPLICATIONS PROCESSOR
MSL INTERFACE
MSL INTERFACE
PROCESSOR
841
810
840
PHYSICAL INTERFACE 830
MEMORY SUBSYSTEM
NV STORAGE
851
850
800

FIG. 8