ABSTRACT

In a digital audio interface signal demodulating apparatus (DDAp) that adds a preamble (PA) and additional information (V, U, C, P) to a digital audio signal (Sda) and demodulates into a digital audio interface signal (Sdai) after subjected to bi-phase modulation for transmission, based on both of positive and negative edges of a reference clock (Src) having a frequency higher than twice a minimum inverse frequency (1/T) of said digital audio interface signal (Sdai) and not necessarily synchronizing with said digital audio interface signal (Sdai), a modulation period (nT) of the digital audio interface signal (Sdai) is decided, and a decision signal (Sj) is generated. Furthermore, based on the decision signal (Sj), the preamble (PA) is detected, and based on the detected preamble (PA), an audio signal (Sda) is obtained from said decision signal (Sj) through demodulation.