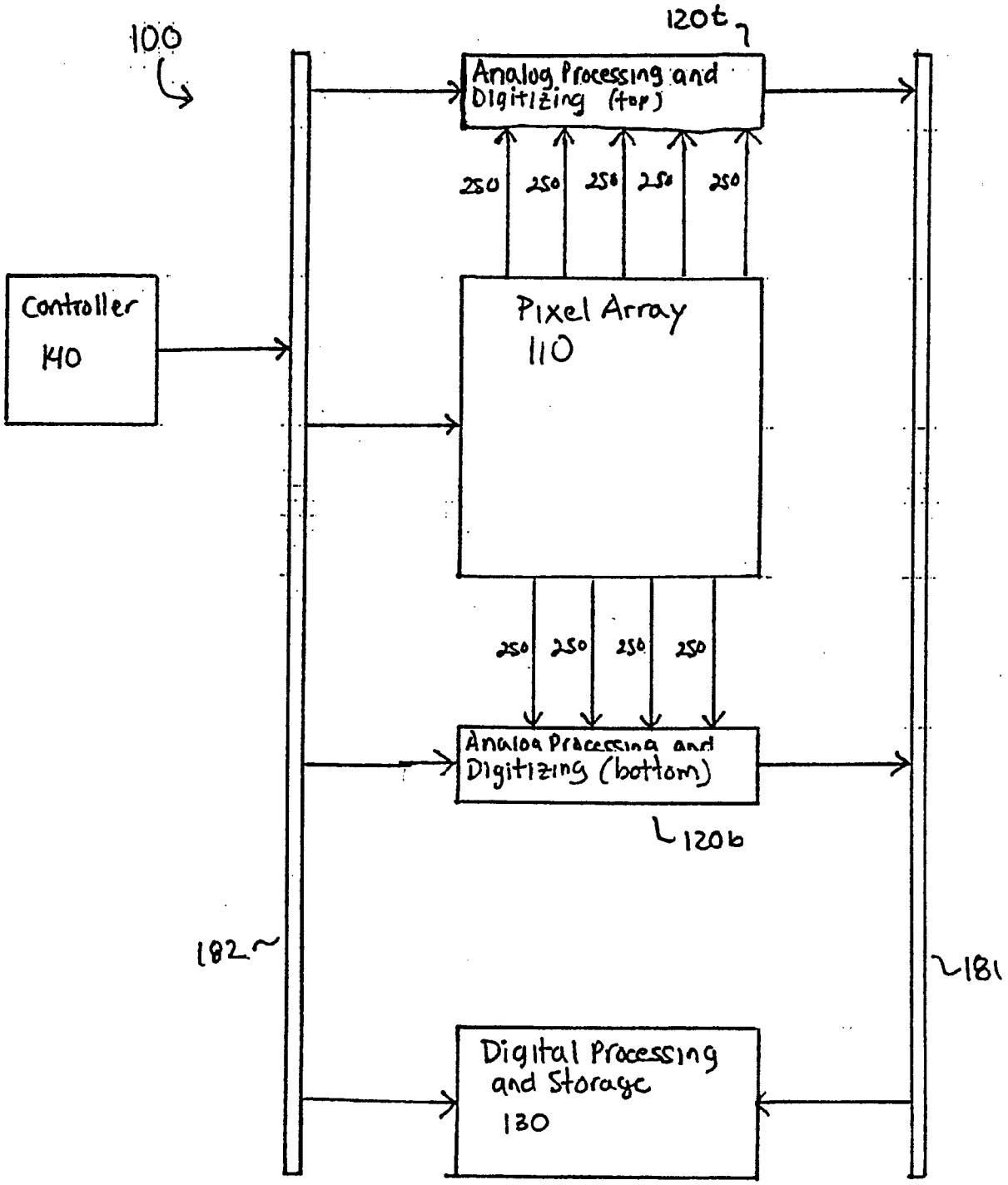


PRIOR ART

FIG. 1

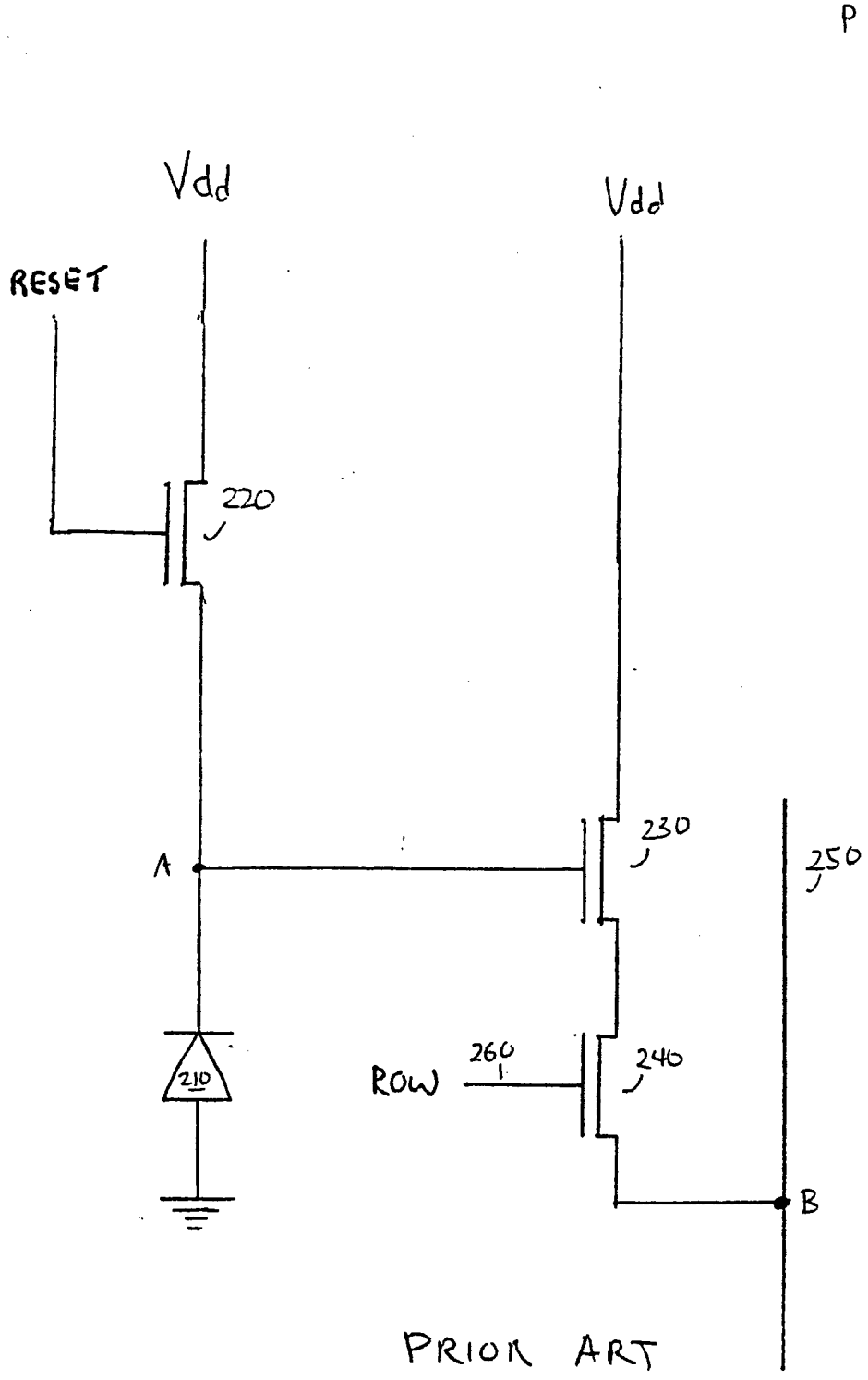


FIG 2

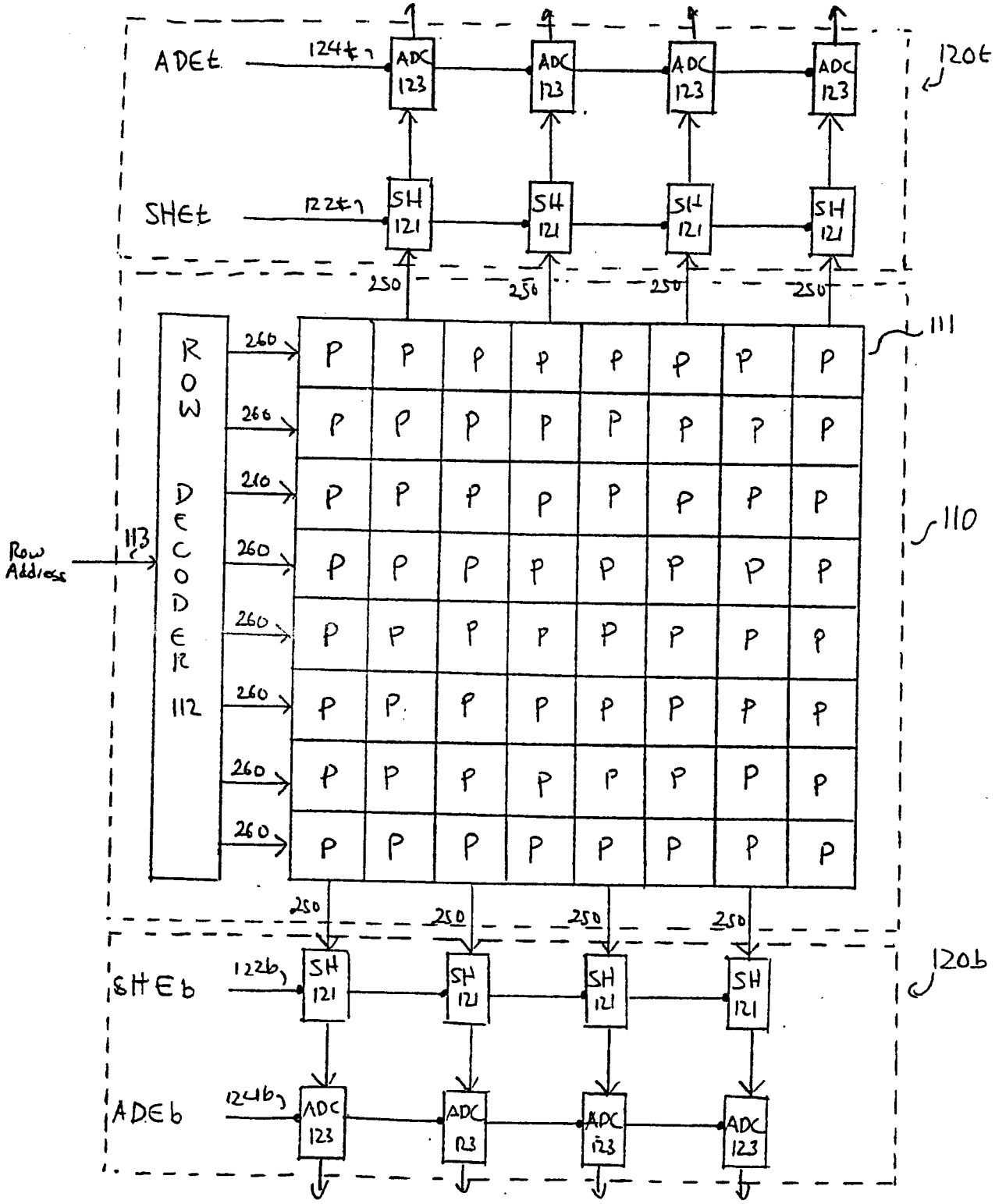


FIG 3 PRIOR ART

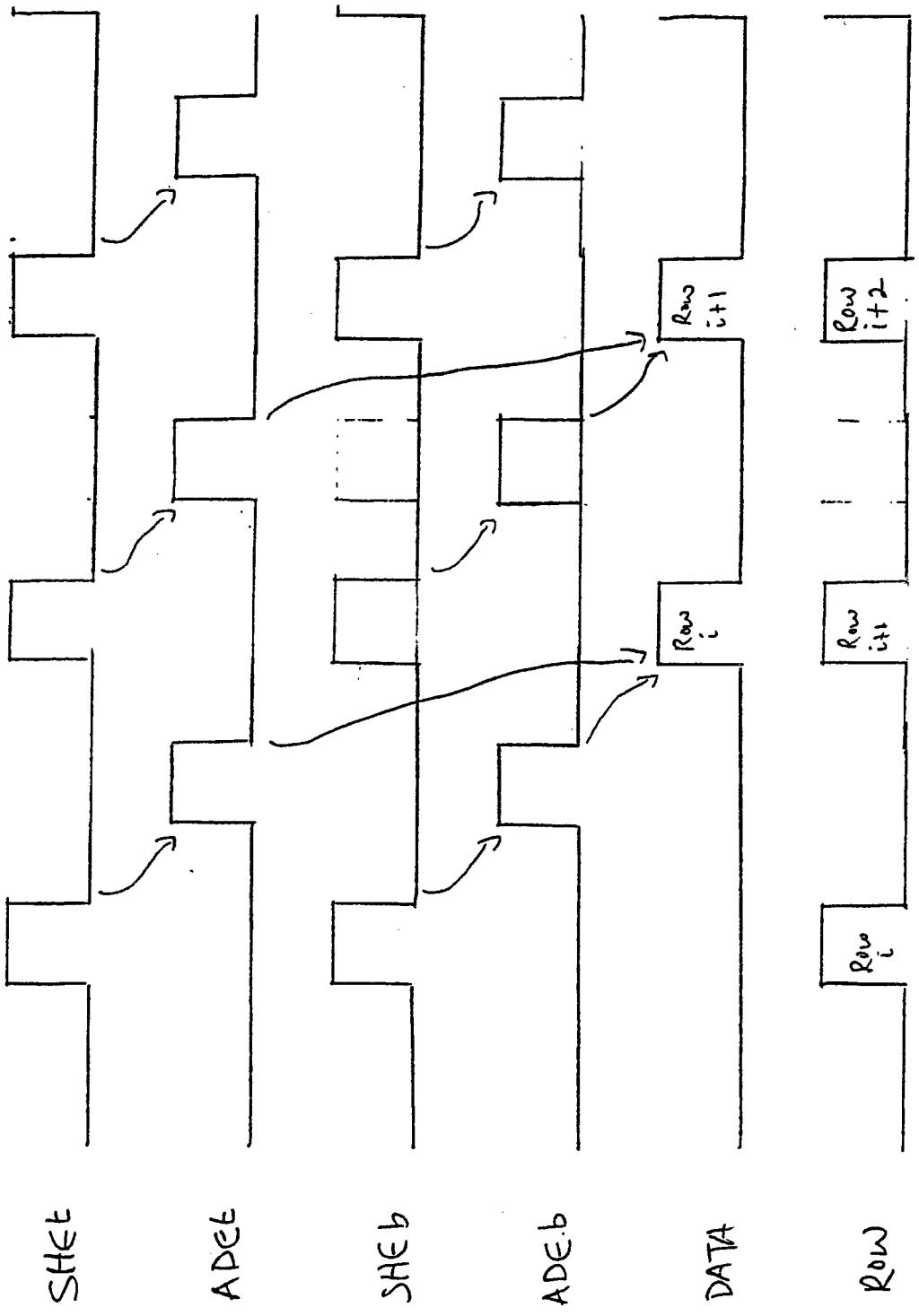


FIG. 4 PRIOR ART

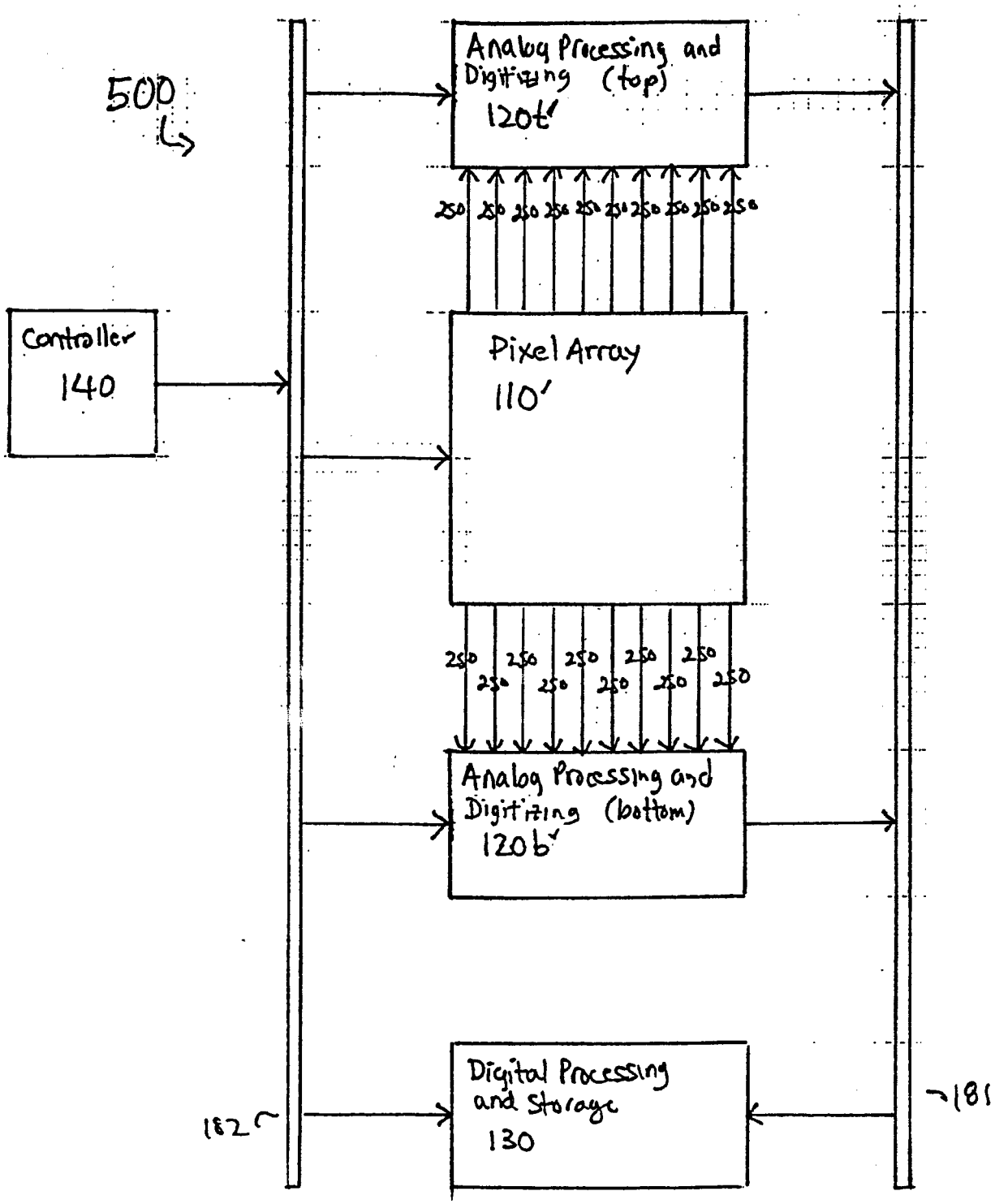

500
Analog Processing and Digitizing (top)
120t'
250
Controller
140
Pixel Array
110'
Analog Processing and Digitizing (bottom)
120b'
Digital Processing and Storage
130
182
181

FIG. 5

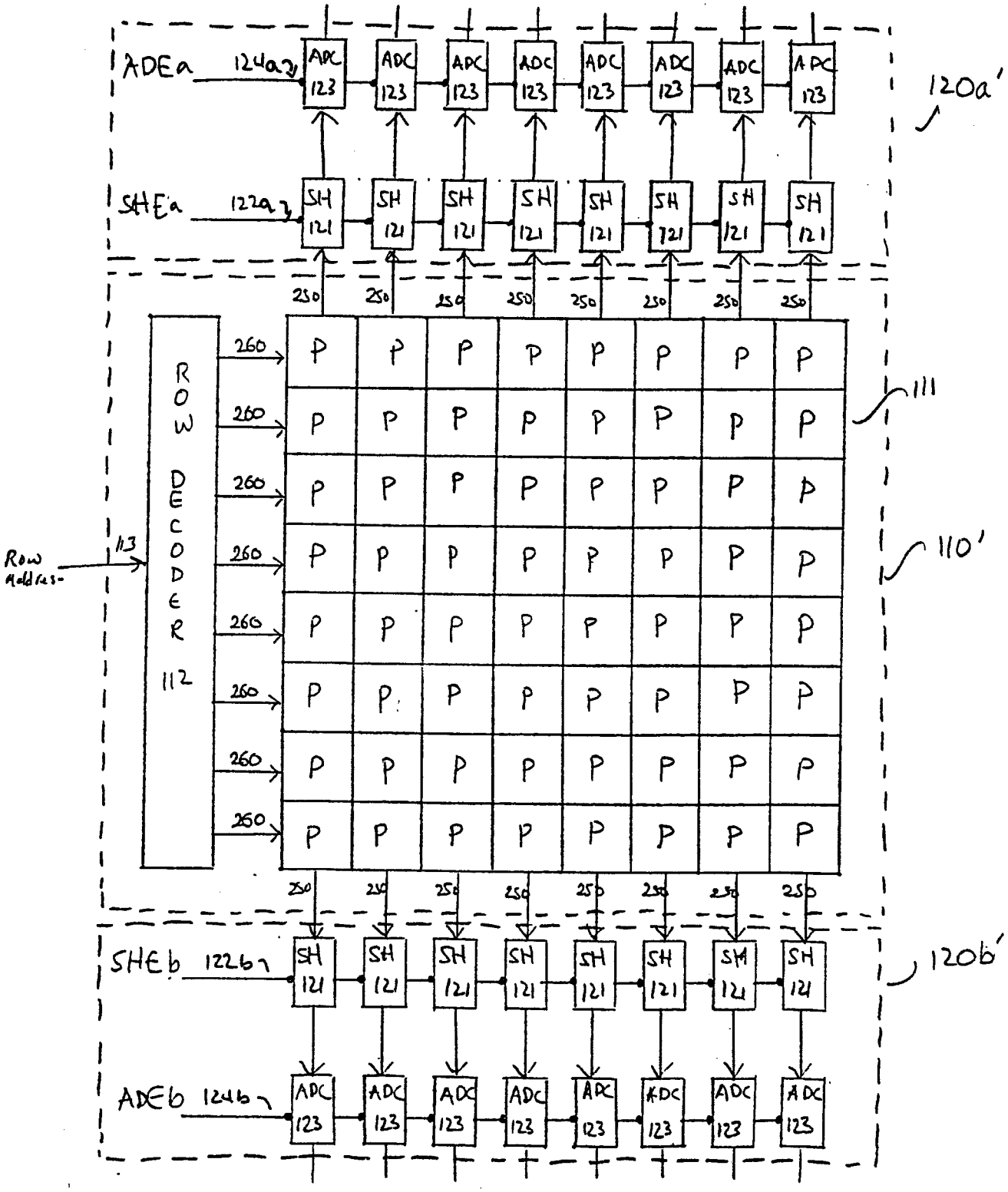

ADEa
124a
ADC 123
SHEa
122a
SH 121
250
260
ROW DECODER 112
113
Row address
P
111
110'
120a'
SHEb
122b
ADEb
124b
120b'

FIG. 6

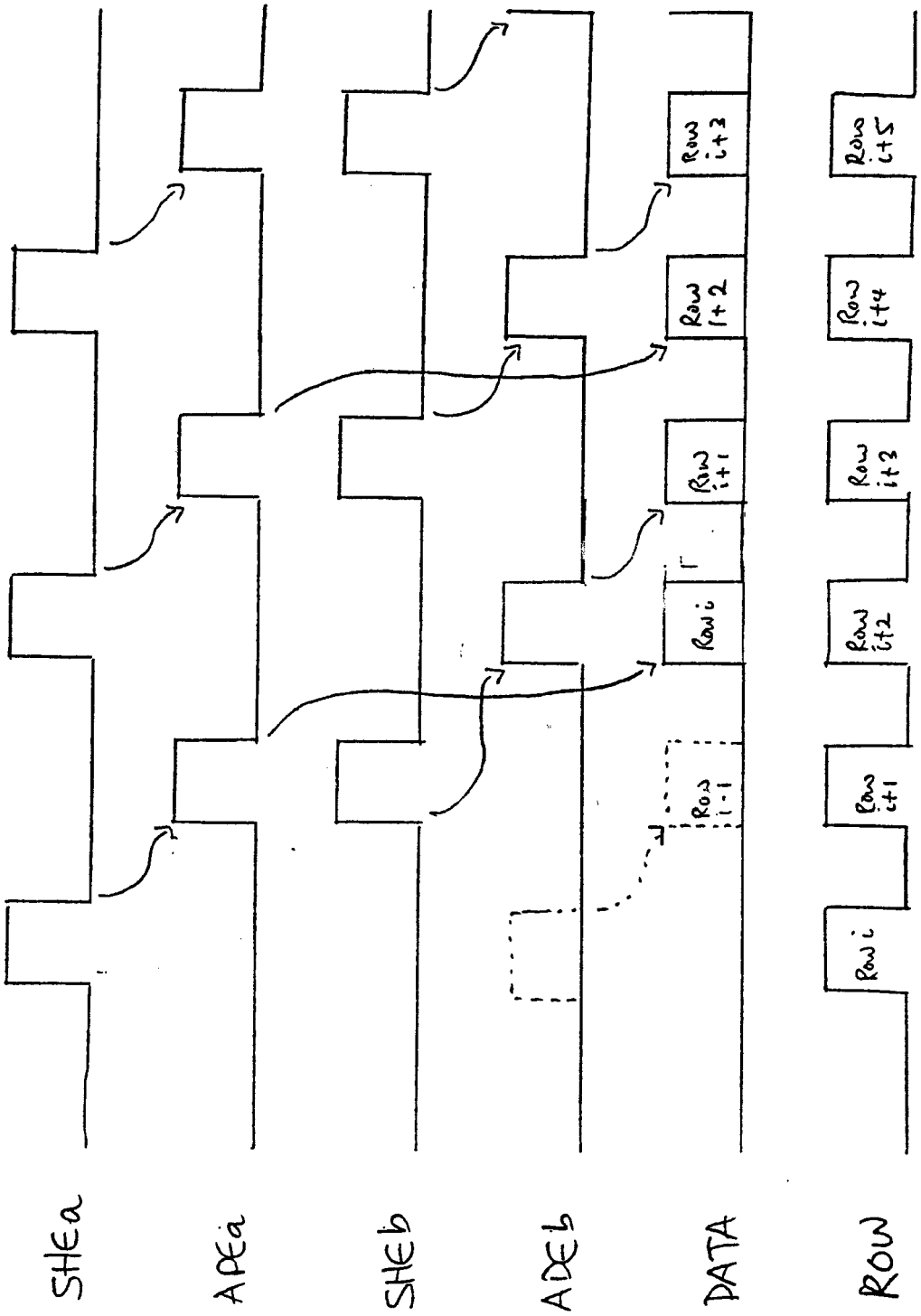

SHEa
APEa
SHEb
APEb
DATA
ROW
Row i-1
Row i
Row i+1
Row i+2
Row i+3
Row i+1
Row i+2
Row i+3
Row i+4
Row i+5

FIG. 7

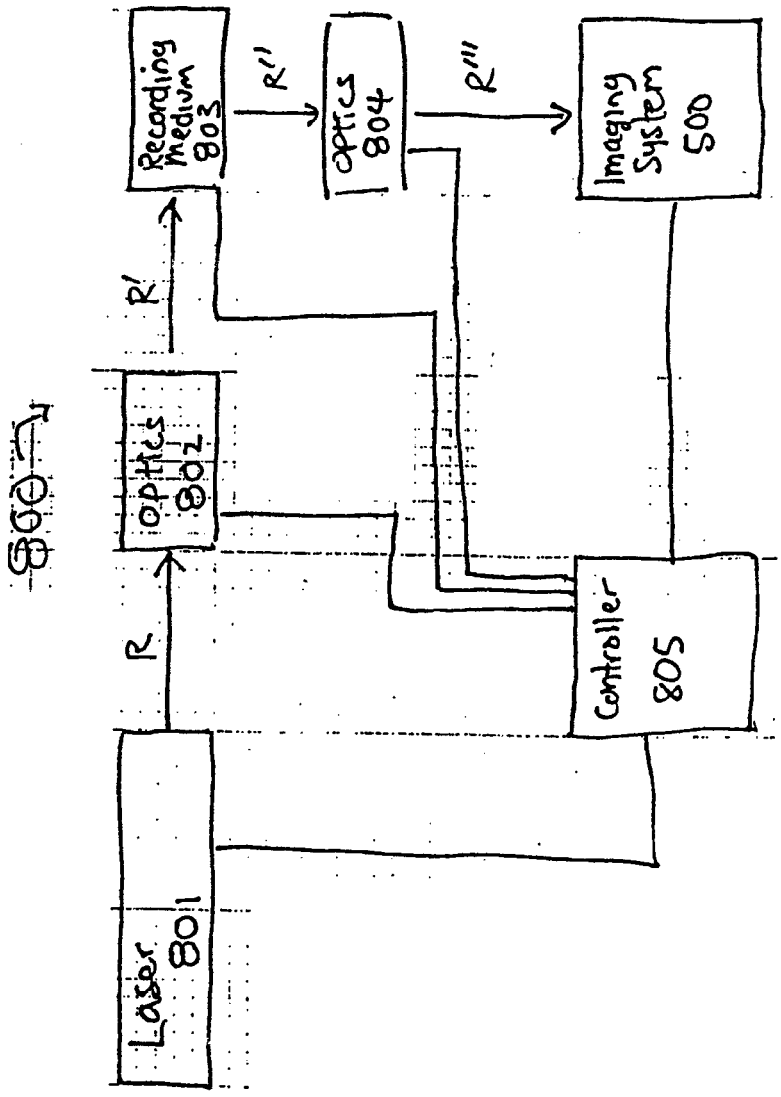

800
Laser 801
R
optics 802
R'
Recording Medium 803
R''
optics 804
R'''
Imaging System 500
Controller 805

FIG. 8

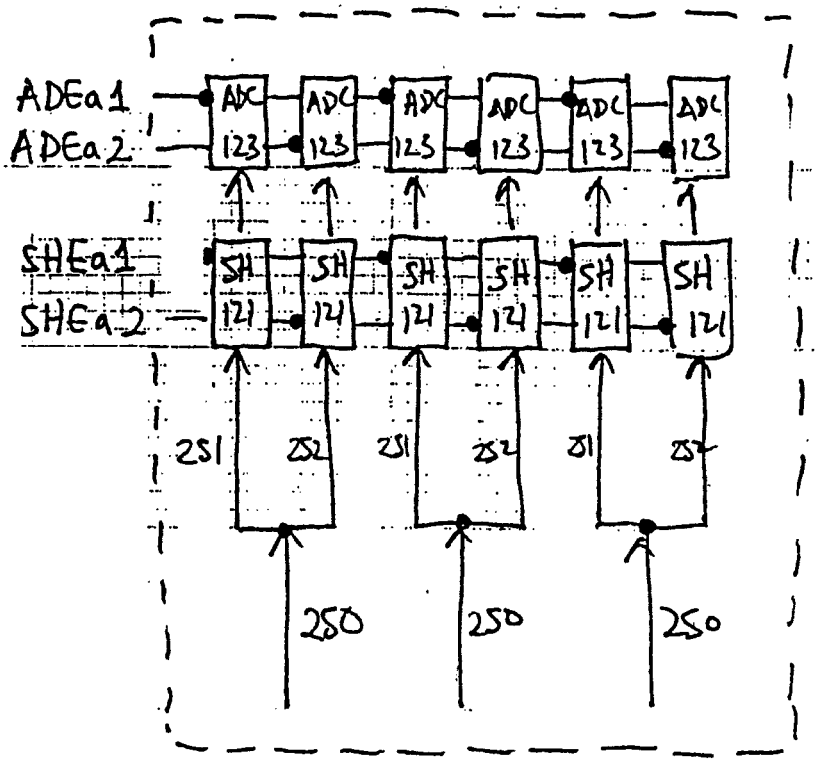


FIG. 9