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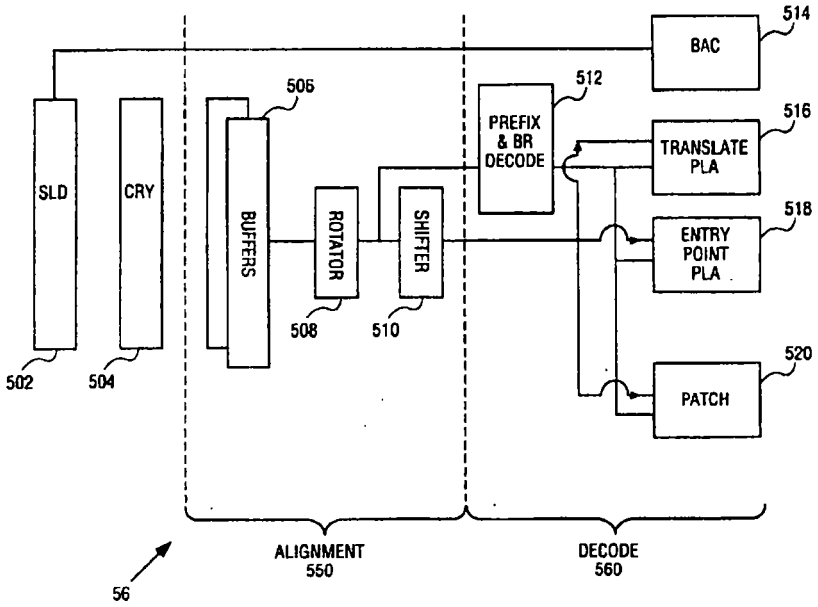
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(54) Title: LENGTH DECODER TO DETECT ONE-BYTE PREFIXES AND BRANCH INSTRUCTIONS



(57) Abstract: A system and method for pre-decoding one-byte instruction prefixes and branch instruction indicators is described. A static line detect generates a number of instruction indicators. Further, a prefix and branch decode unit combines at least two of the number of instruction indicators, and a pre-decode unit decodes the combined instruction indicators.

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