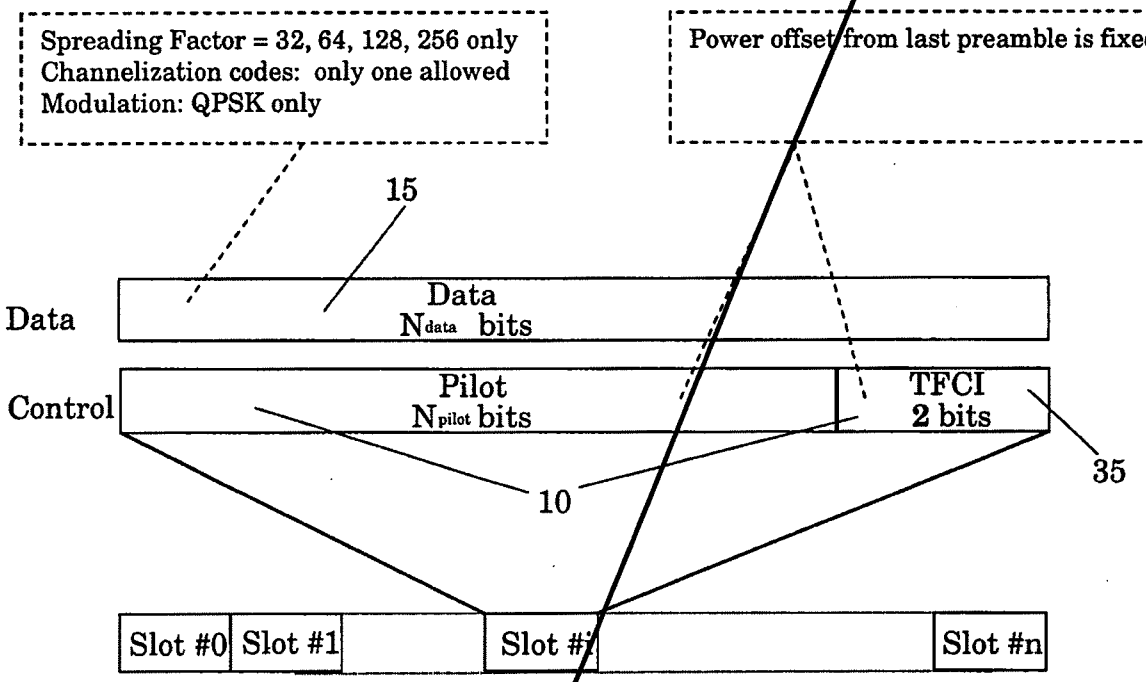

Fig. 1A Prior Art

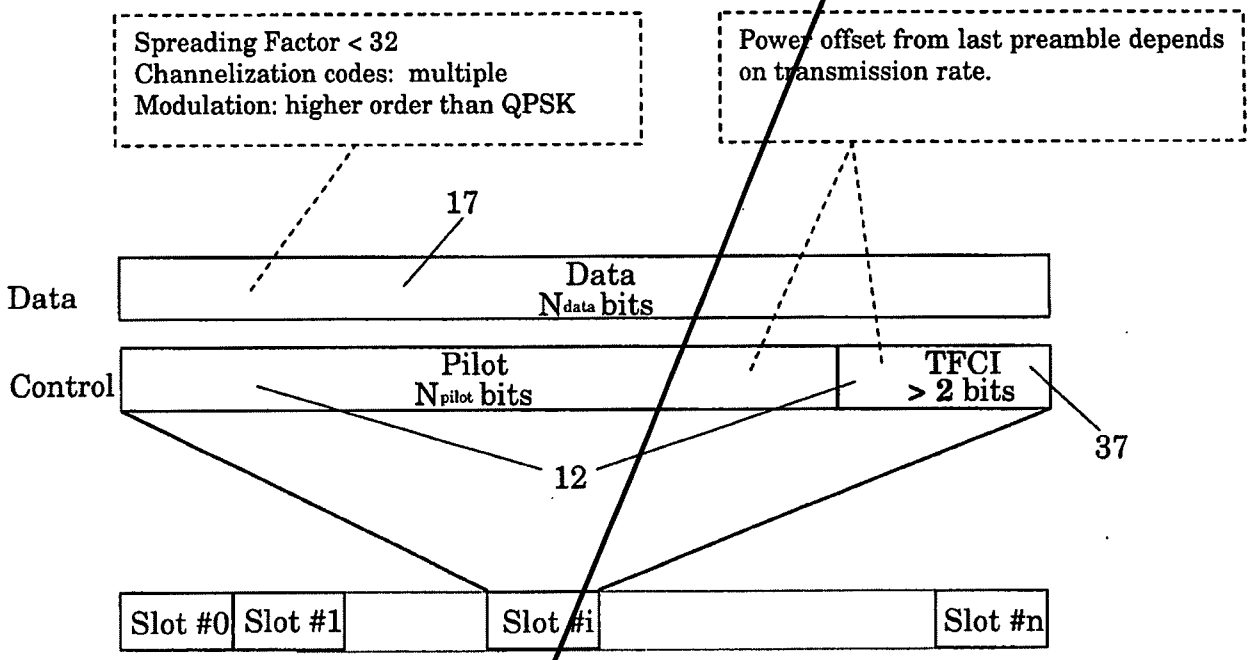
Spreading Factor < 32
Channelization codes: multiple
Modulation: higher order than QPSK
Power offset from last preamble depends on transmission rate.
17
Data
Data
N_{data} bits
Control
Pilot
N_{pilot} bits
TFCI
> 2 bits
12
37
Slot #0
Slot #1
Slot #i
Slot #n

Fig. 1B

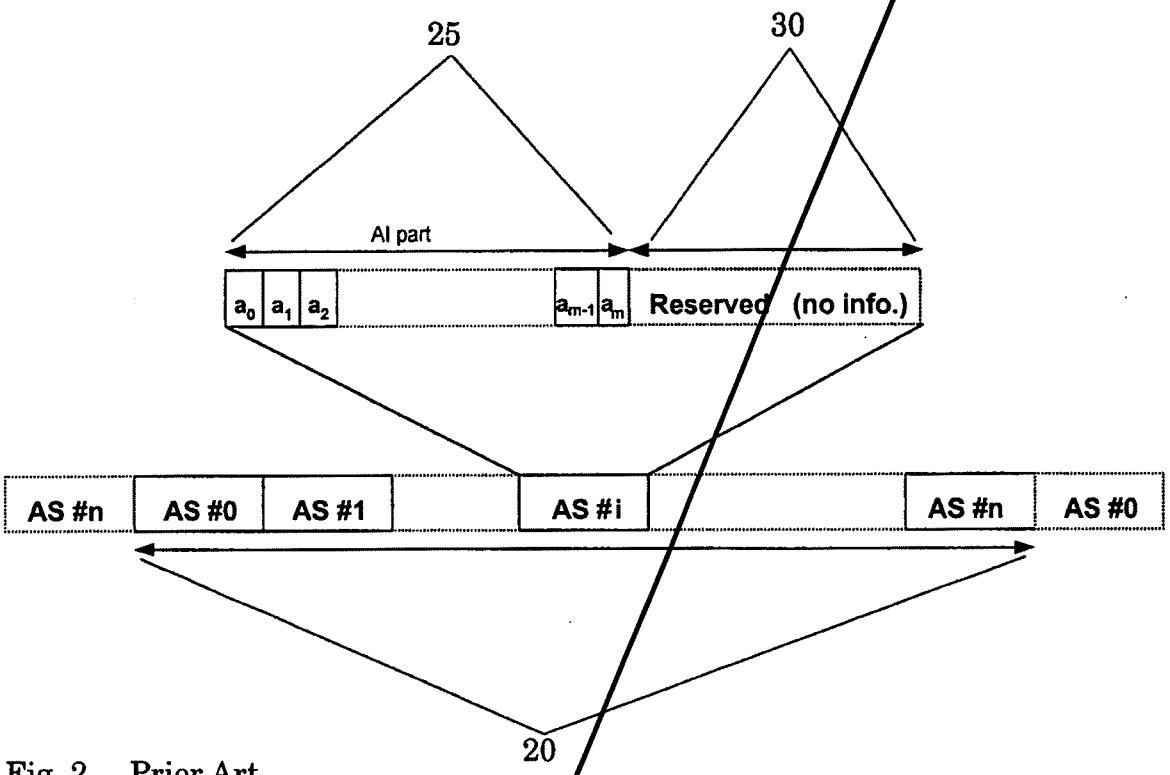

Fig. 2 Prior Art

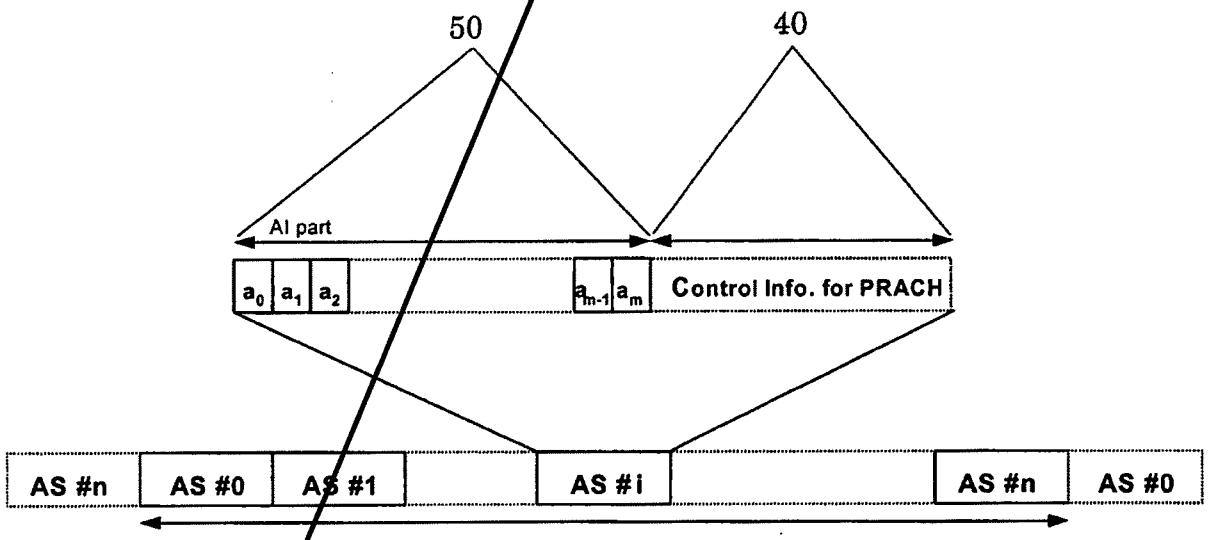

Fig. 3

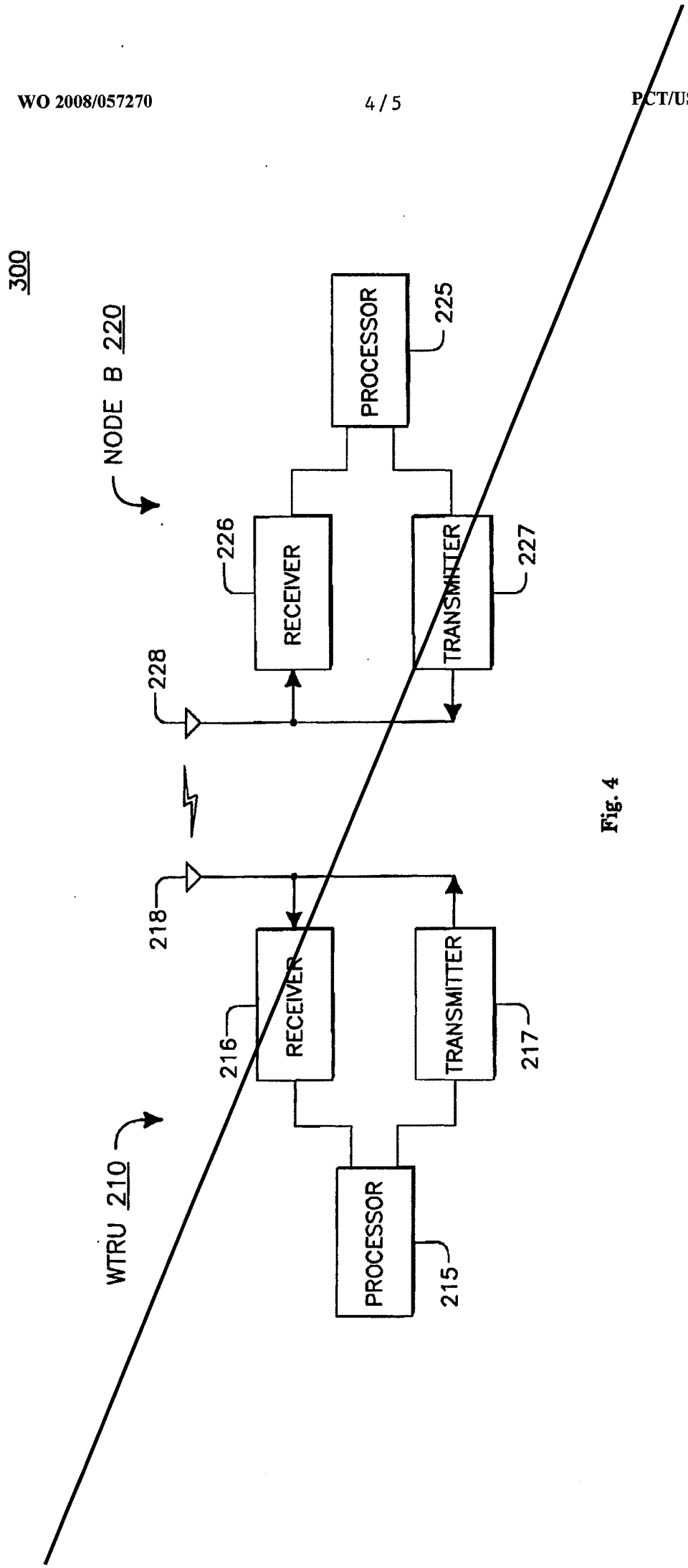
300
NODE B 220
PROCESSOR
225
226
RECEIVER
TRANSMITTER
227
228
218
RECEIVER
216
TRANSMITTER
217
WTRU 210
PROCESSOR
215

Fig. 4

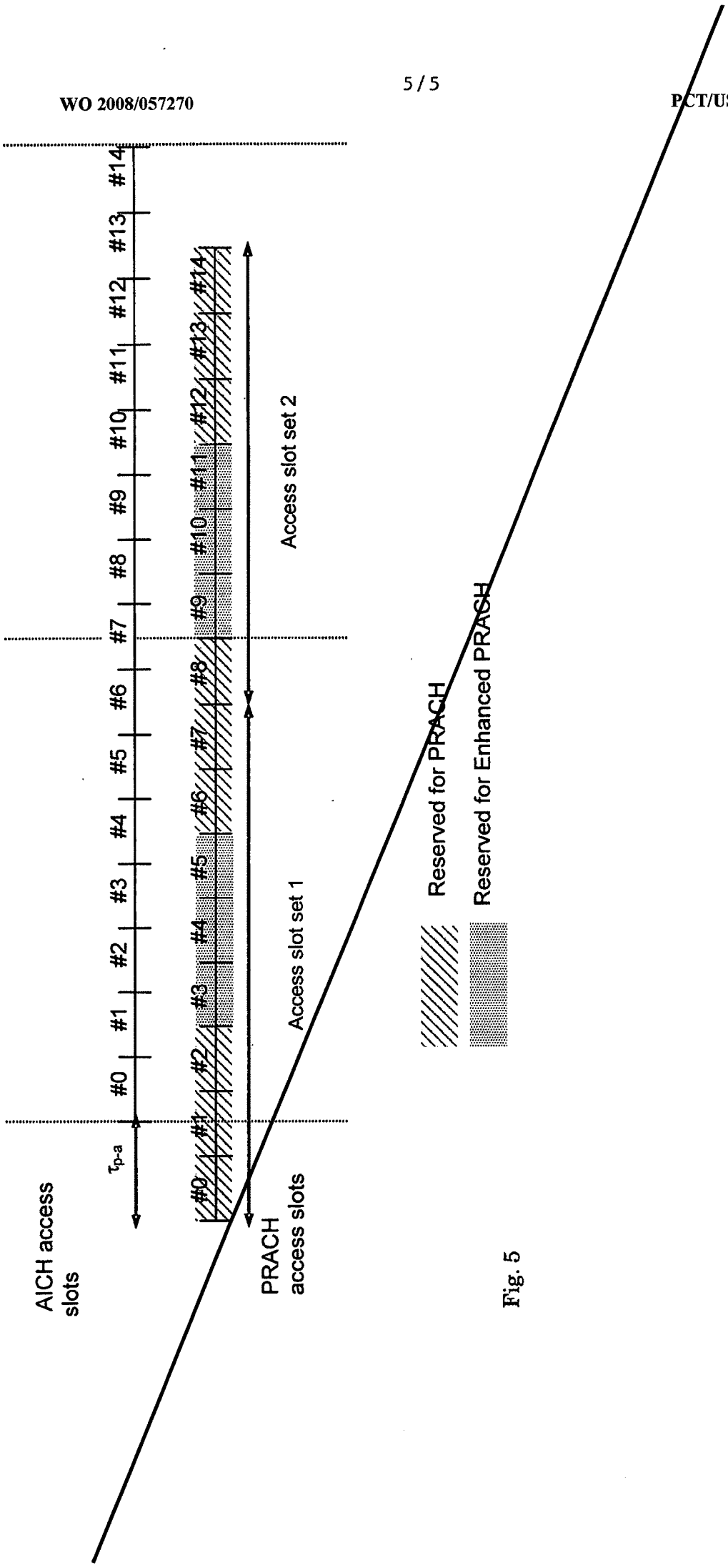
AICH access slots
PRACH access slots
τ_{p-a}
#0
#1
#2
#3
#4
#5
#6
#7
#8
#9
#10
#11
#12
#13
#14
Access slot set 1
Access slot set 2
Reserved for PRACH
Reserved for Enhanced PRACH

Fig. 5

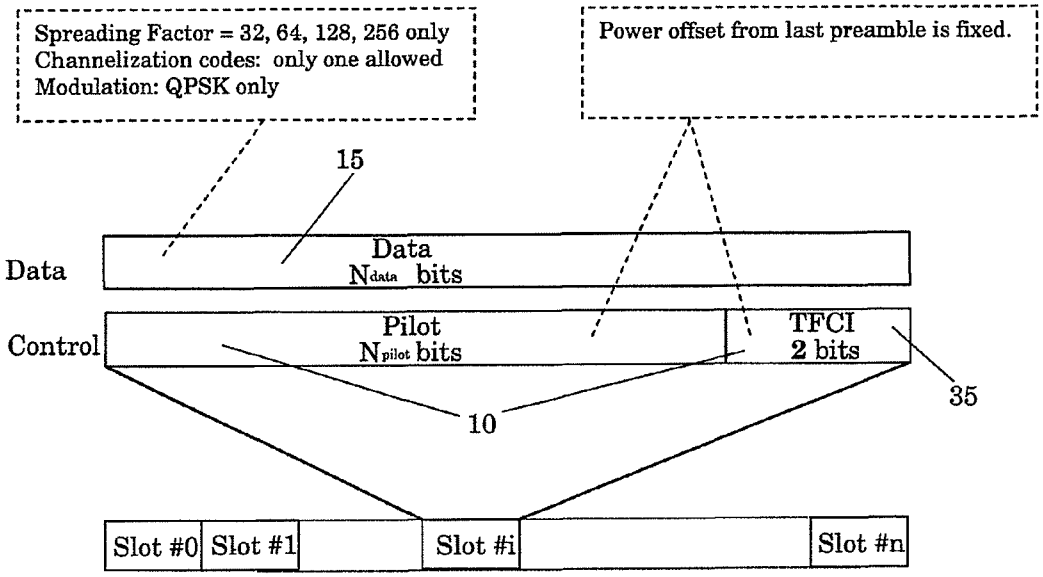

Fig. 1A Prior Art

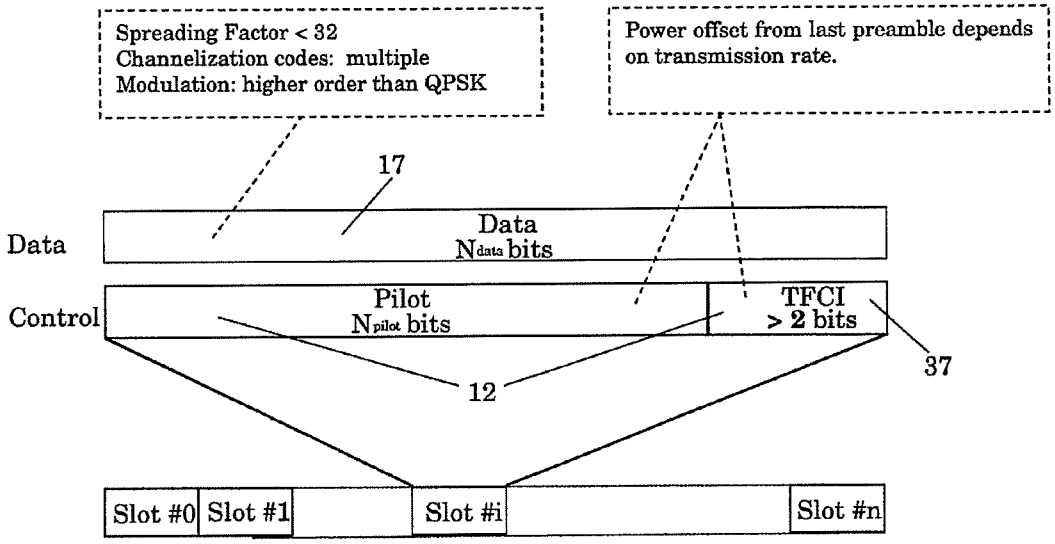
Spreading Factor < 32
Channelization codes: multiple
Modulation: higher order than QPSK
Power offset from last preamble depends on transmission rate.
17
Data
Data
N_{data} bits
Control
Pilot
N_{pilot} bits
TFCI
> 2 bits
12
37
Slot #0
Slot #1
Slot #i
Slot #n

Fig. 1B

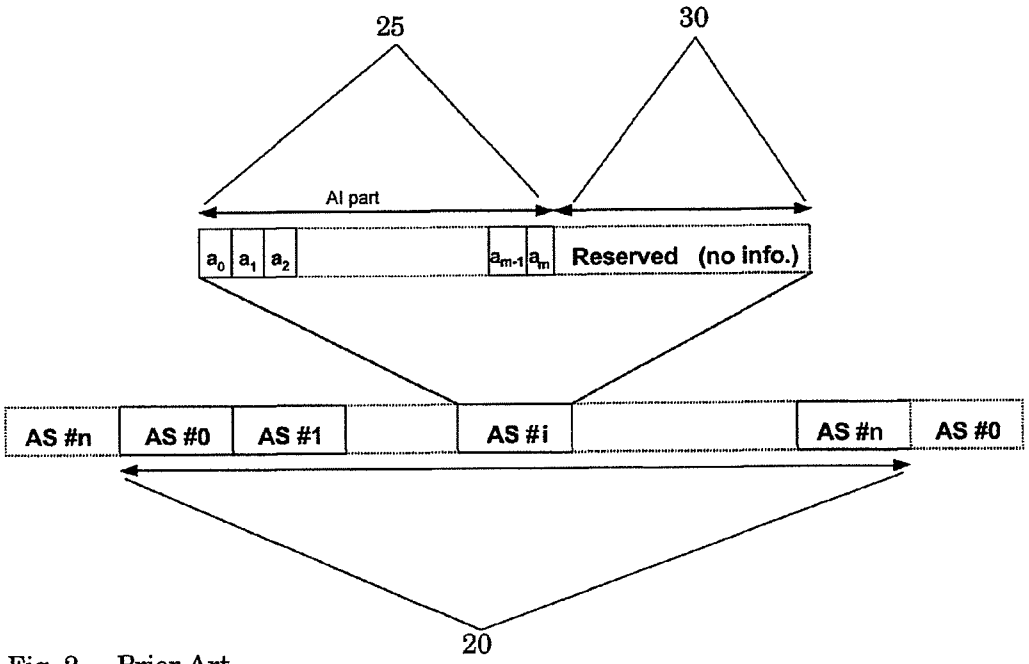

Fig. 2 Prior Art

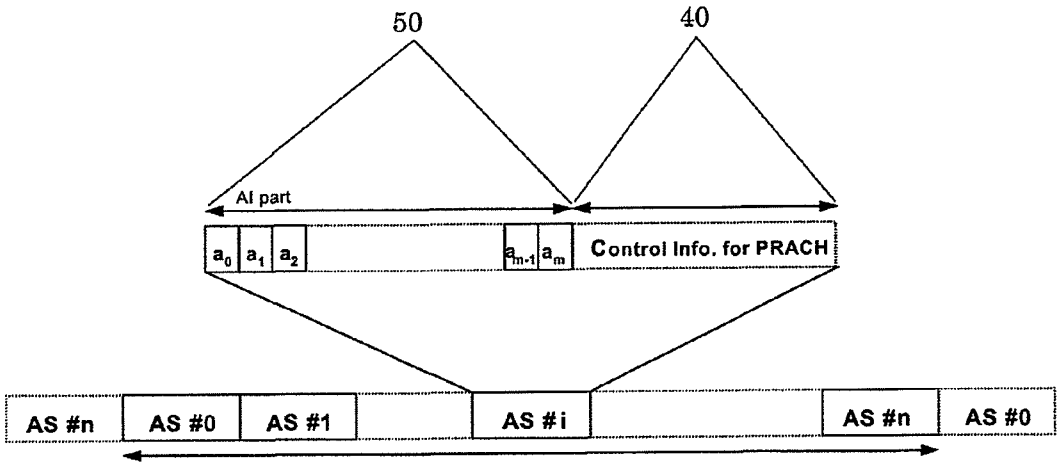

Fig. 3

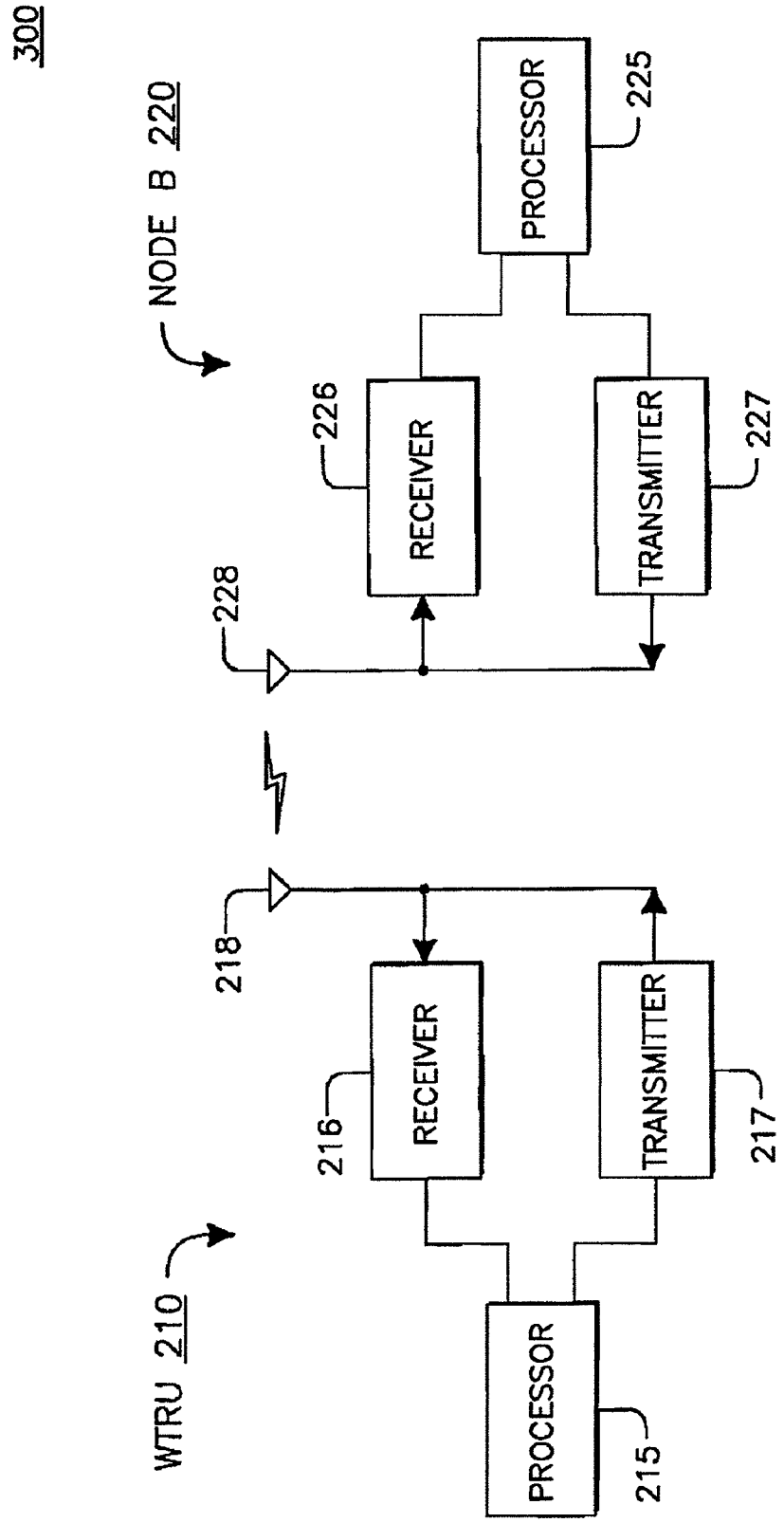
300
NODE B 220
PROCESSOR
225
226
RECEIVER
TRANSMITTER
227
228
218
RECEIVER
216
TRANSMITTER
217
WTRU 210
PROCESSOR
215

Fig. 4

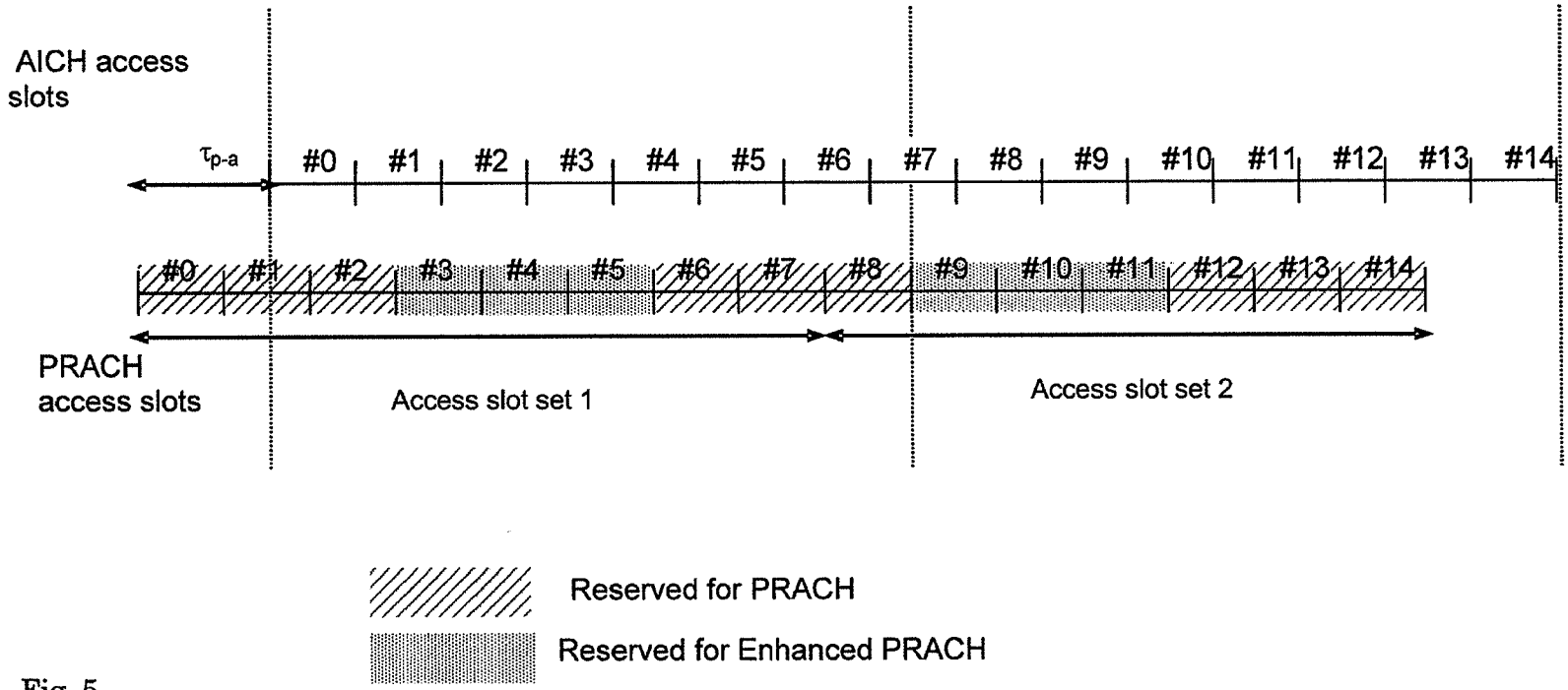
AICH access slots
τ_{p-a}
#0
#1
#2
#3
#4
#5
#6
#7
#8
#9
#10
#11
#12
#13
#14
PRACH access slots
Access slot set 1
Access slot set 2
Reserved for PRACH
Reserved for Enhanced PRACH

Fig. 5