

ABSTRACT OF THE DISCLOSURE (FIG. 7)

"ELECTRONIC DEVICE WITH SERIAL ATA INTERFACE
AND POWER SAVING METHOD FOR SERIAL ATA BUSES"

5

In an electronic device with a serial ATA
interface, upon detection of the issue or reception
of a preset command, a confirmation device, such as
a CPU, confirms the completion of execution of the
10 command. Upon confirming the completion of execution
of the command, a controller, which may also be the
CPU, controls shifting of the serial ATA interface to
a power saving mode.



162162



G00002