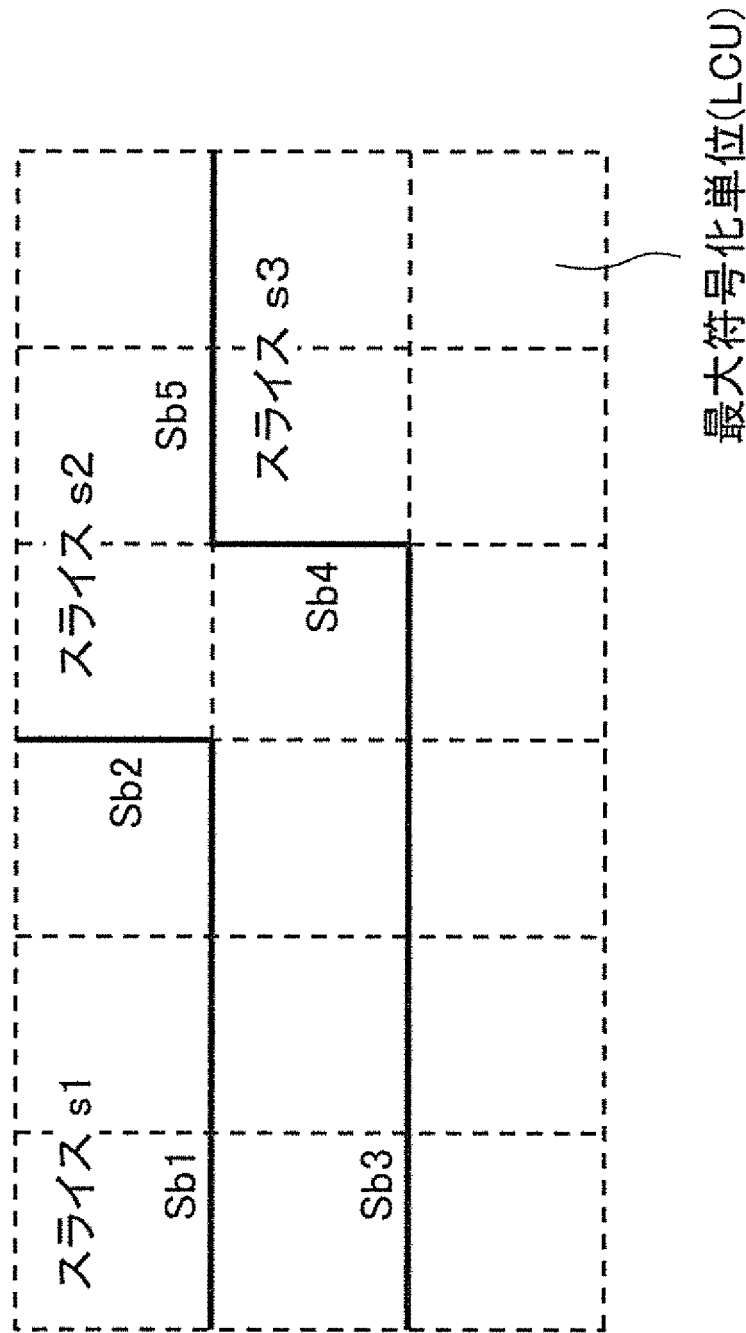
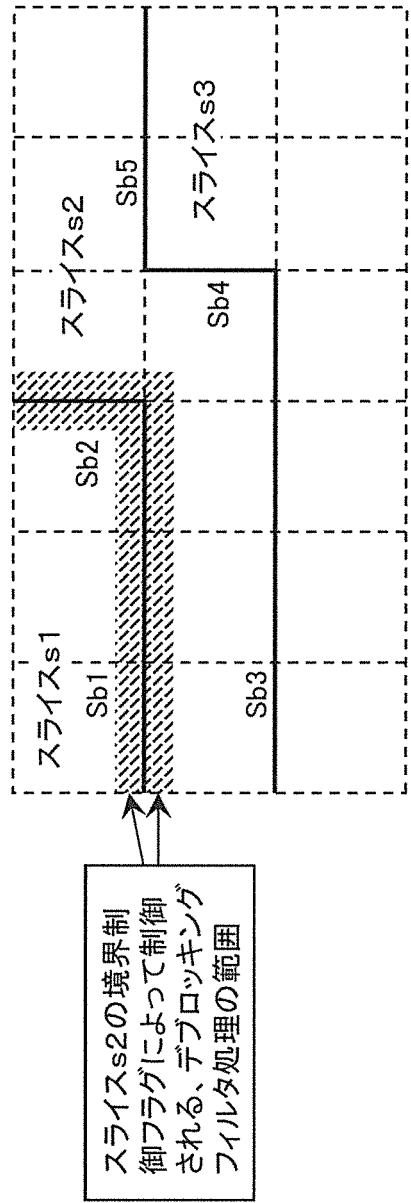


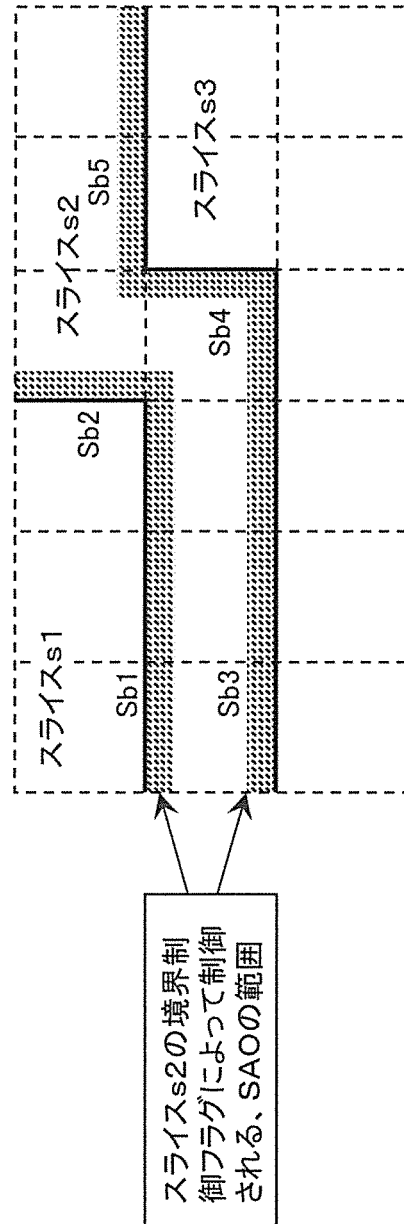
[図1A]



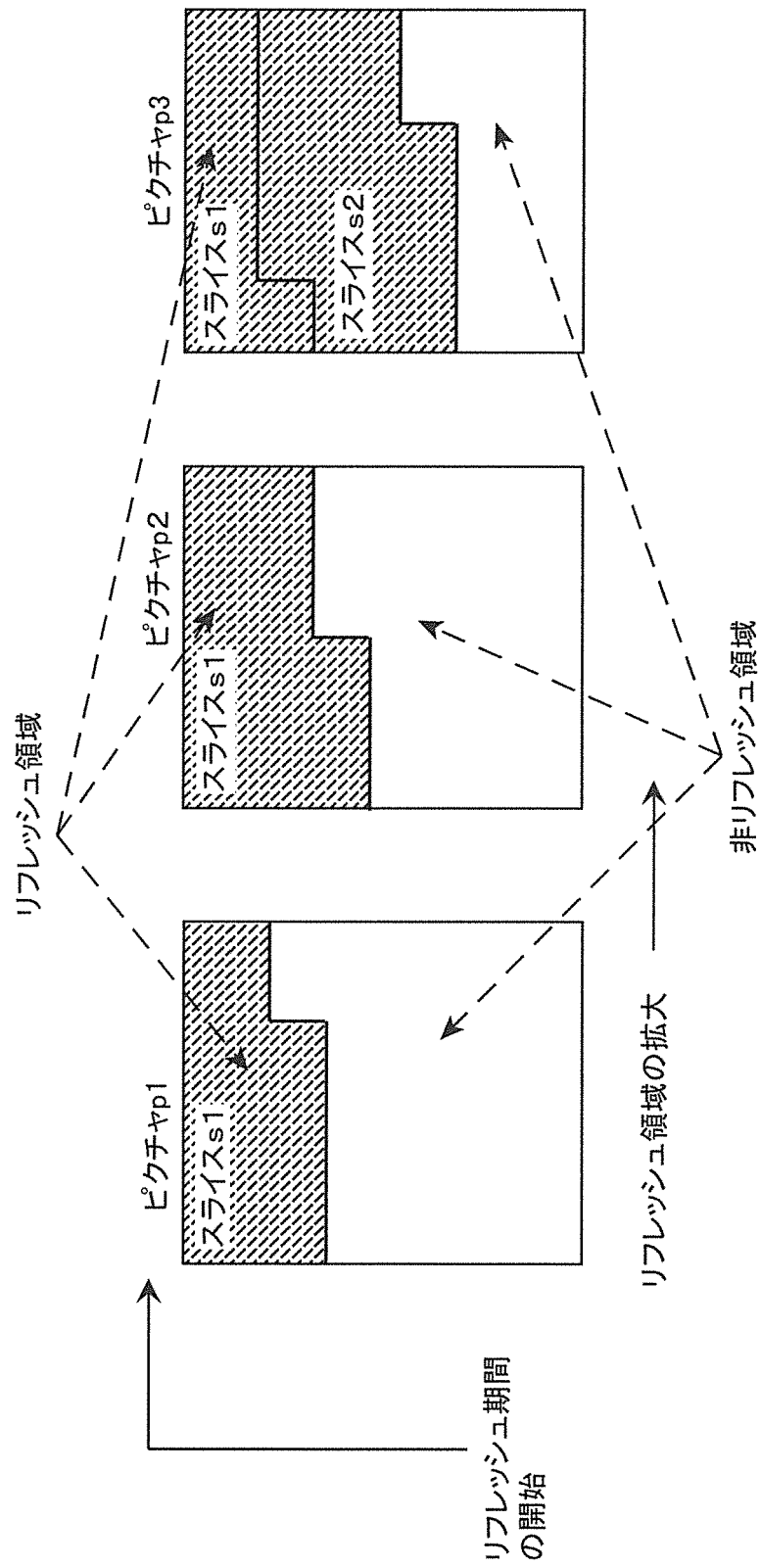
[図1B]



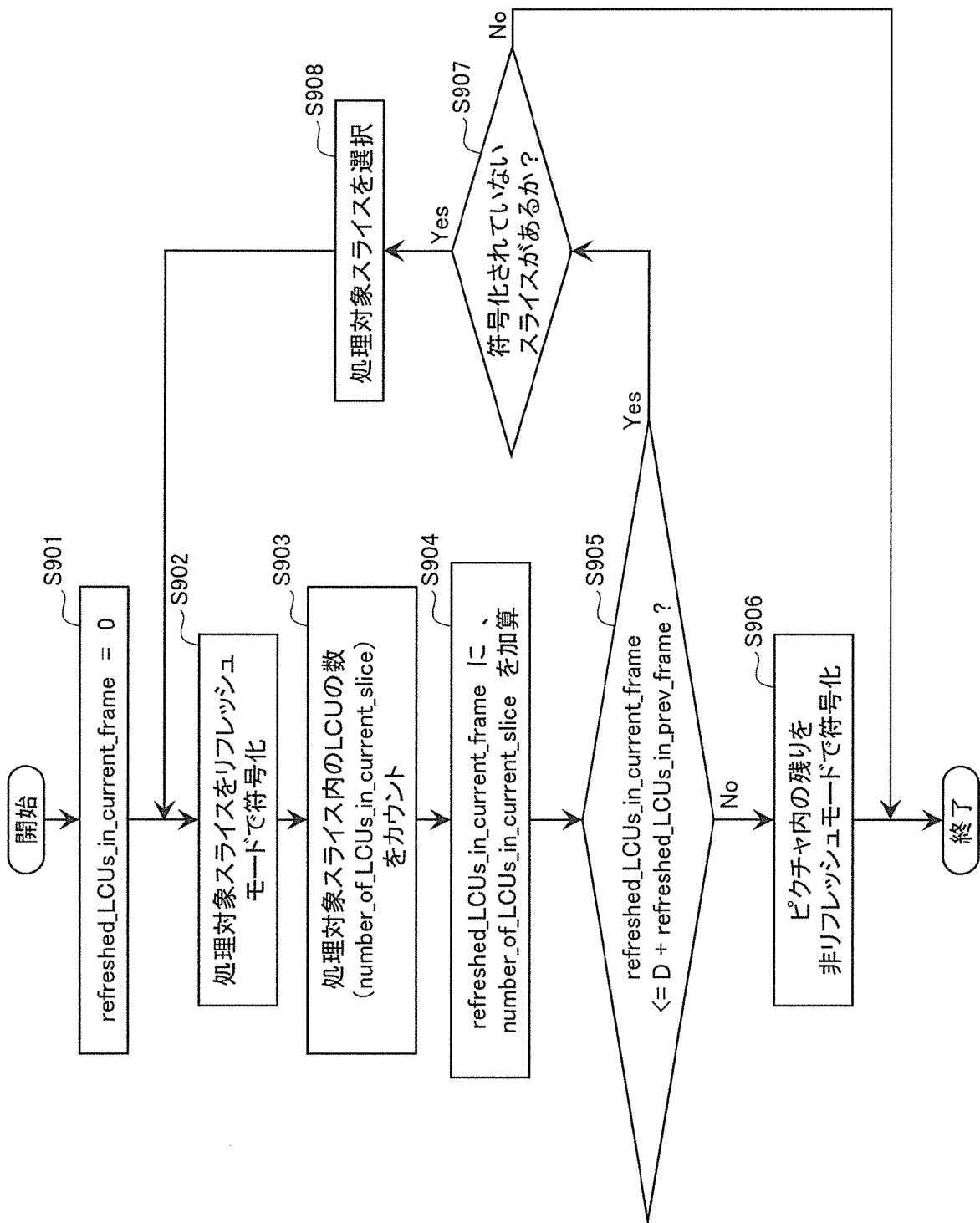
[図1C]



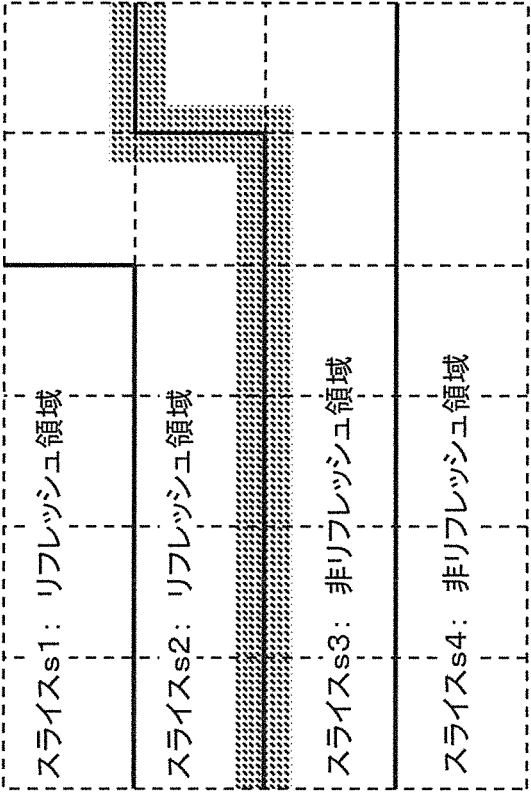
[図2]



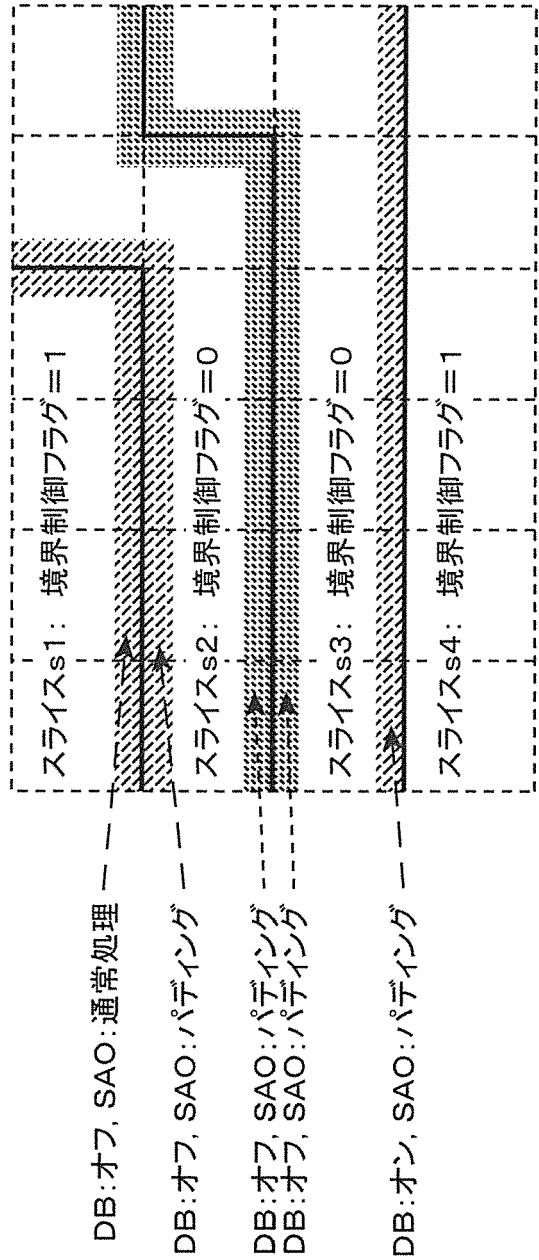
[図3]



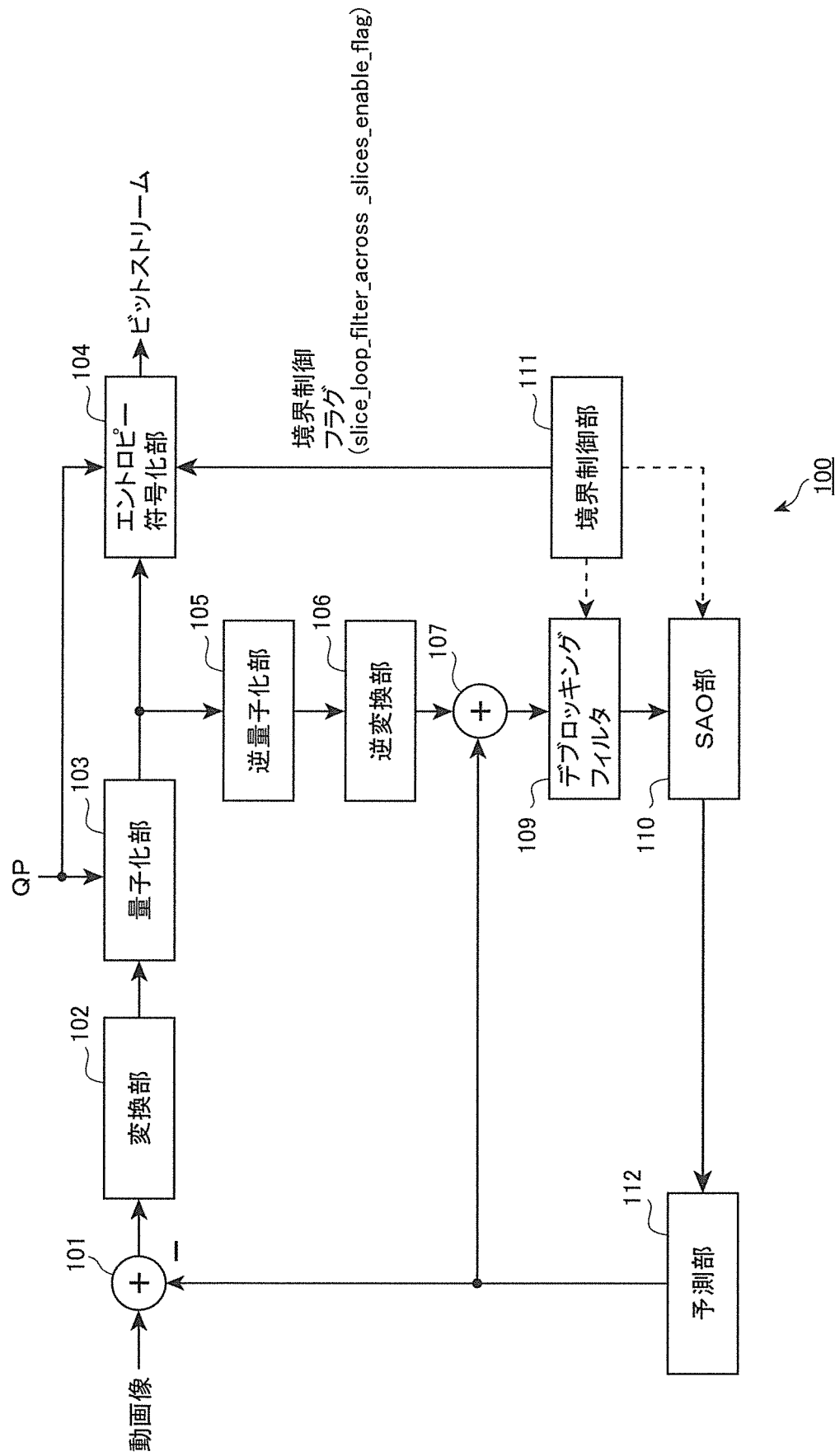
[図4]



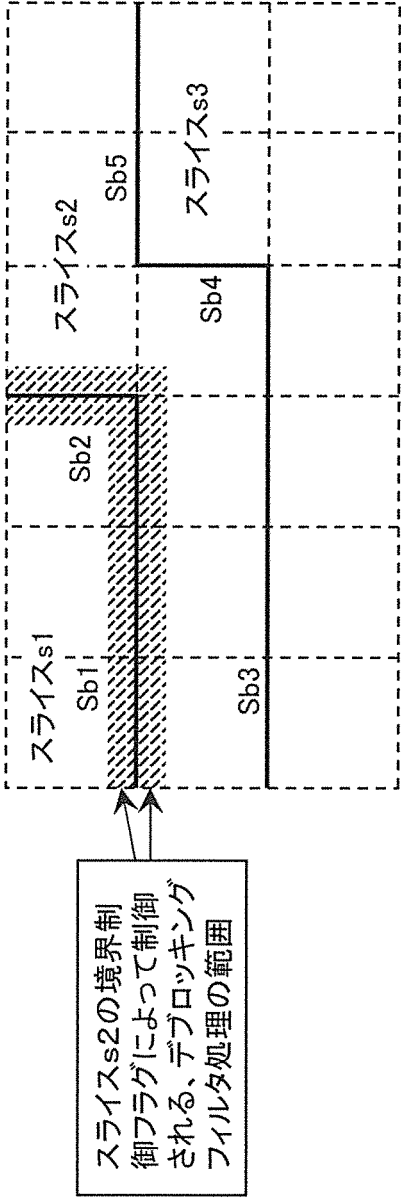
[図5]



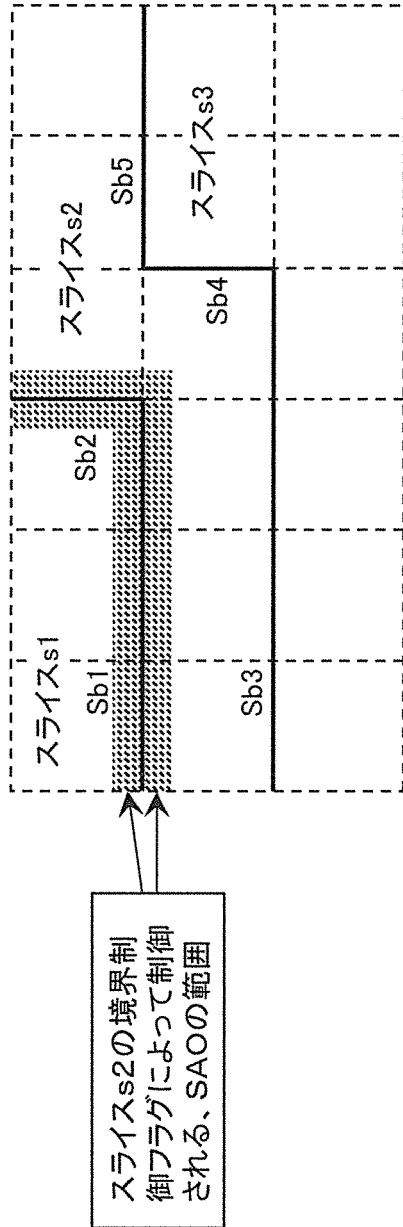
[図6]



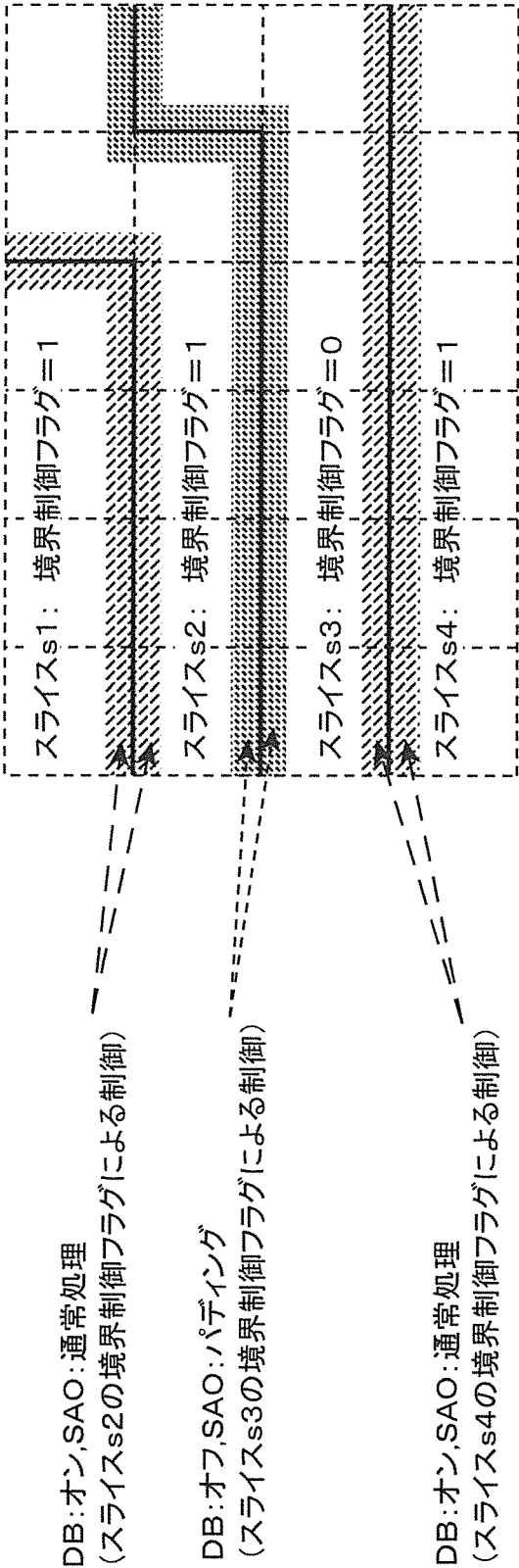
[図7A]



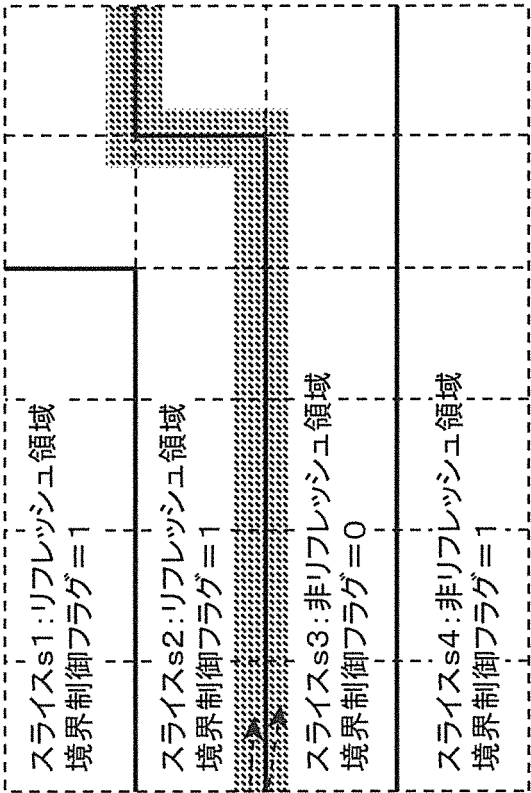
[図7B]



[図7C]

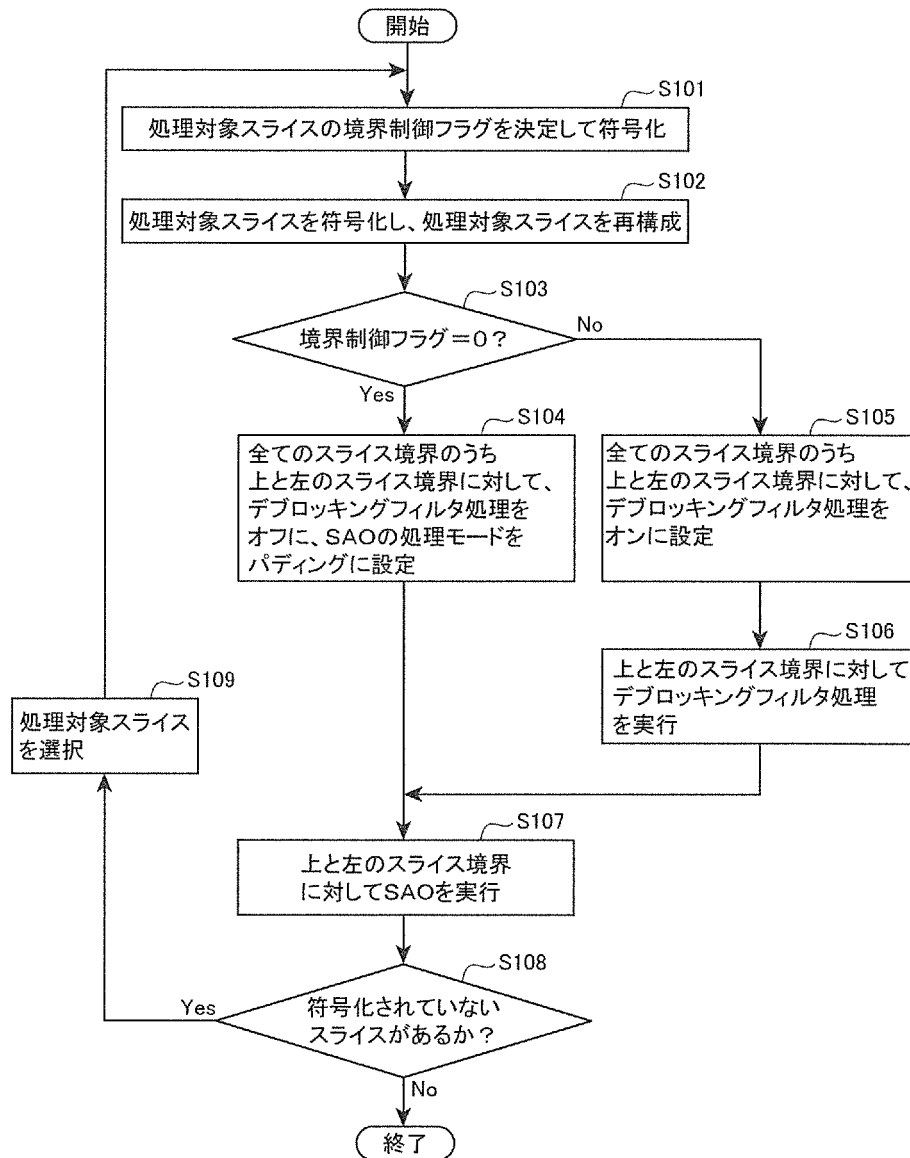


[図8]

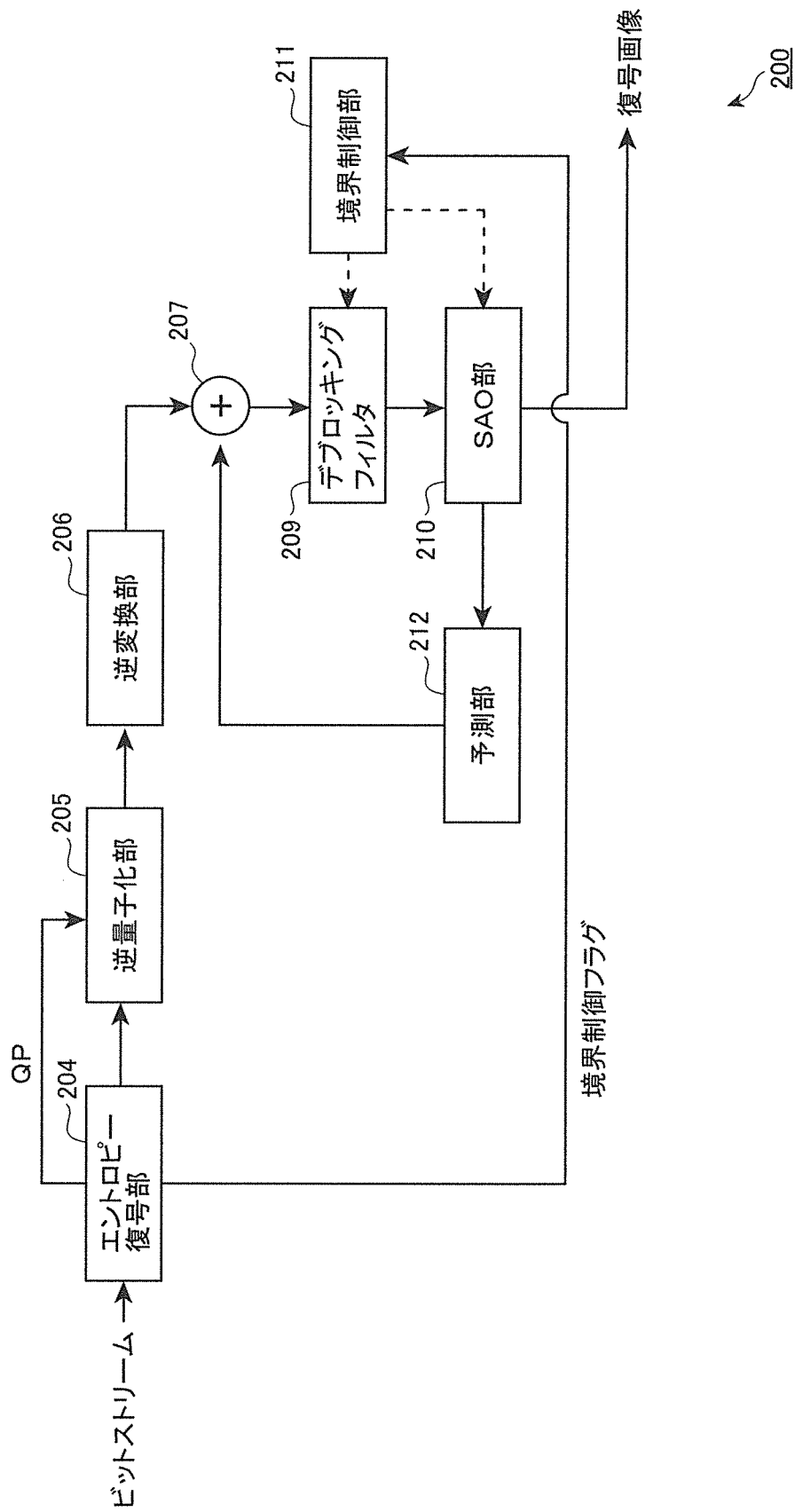


DB:オフ, SAO:パディング
(スライスs3の境界制御フラグによる制御)

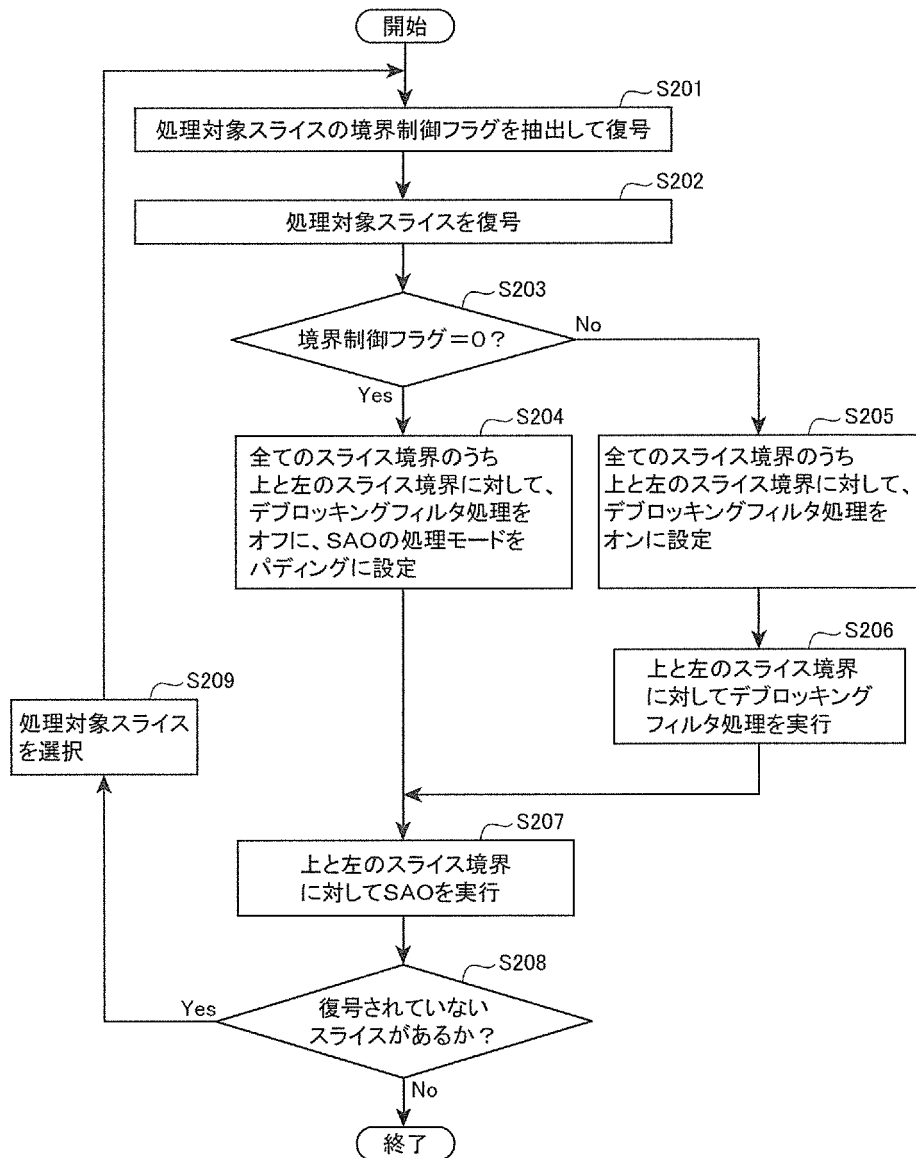
[図9]



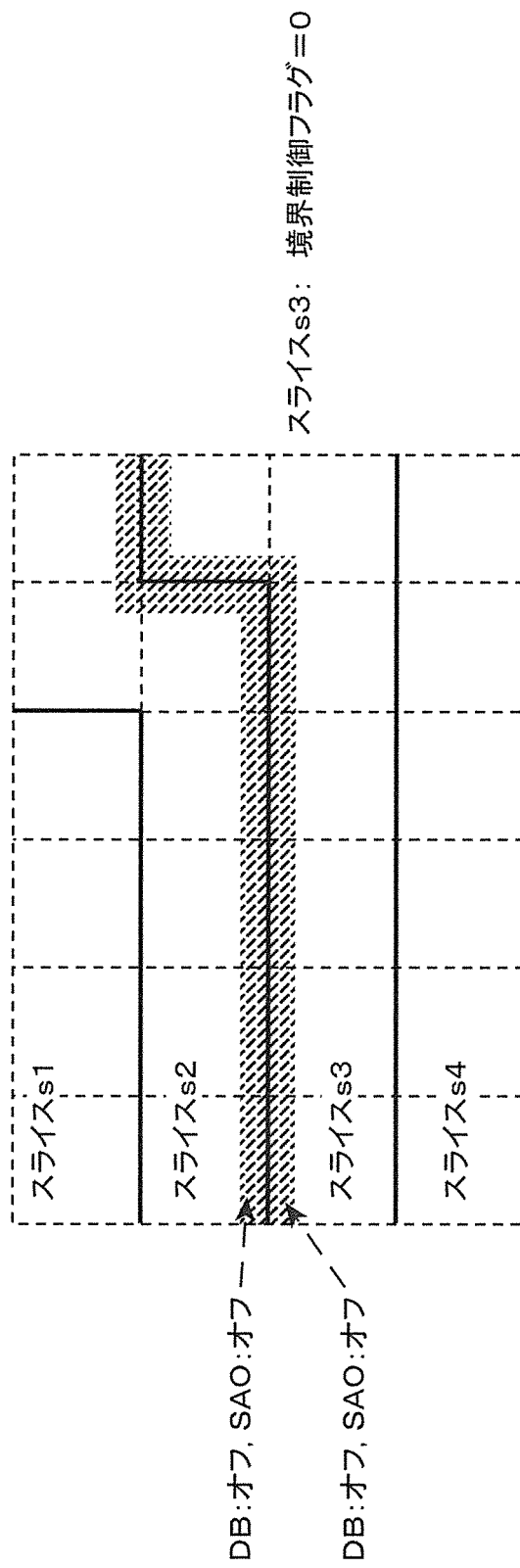
[図10]



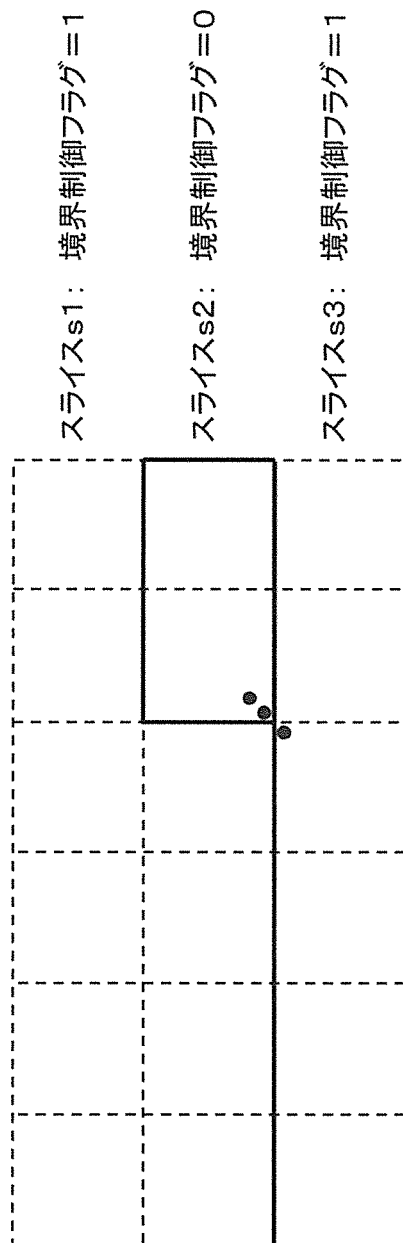
[図11]



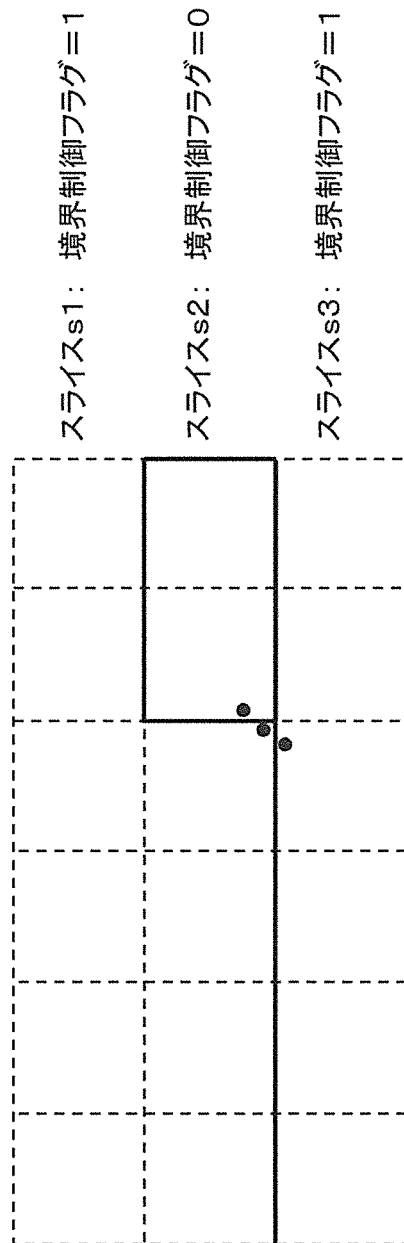
[図12]



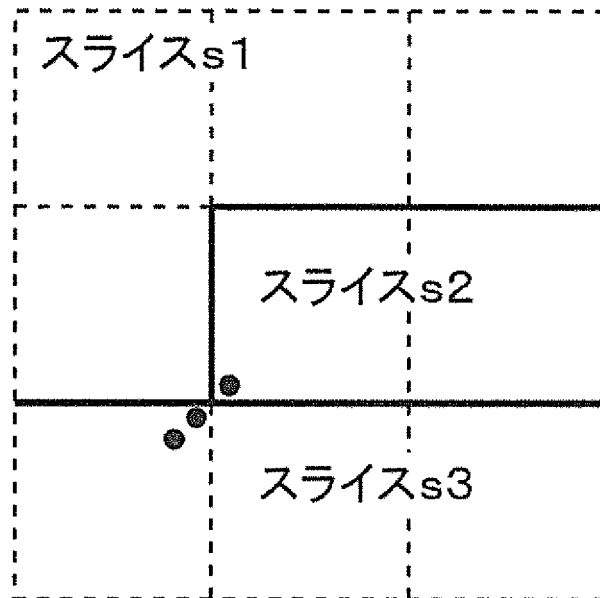
[図13]



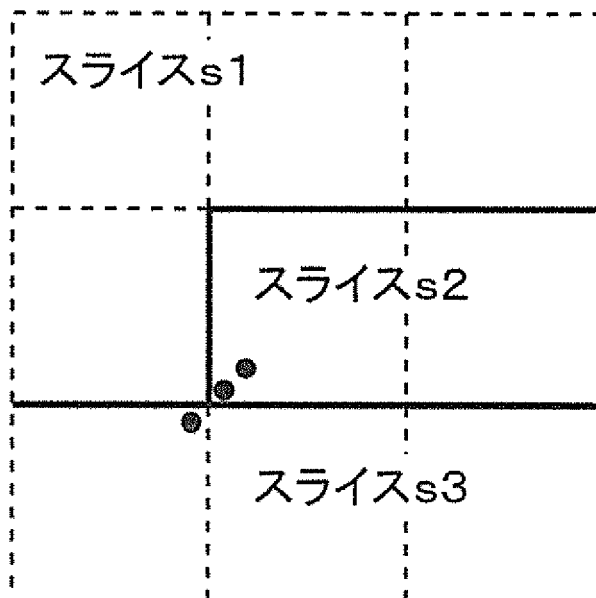
[図14]



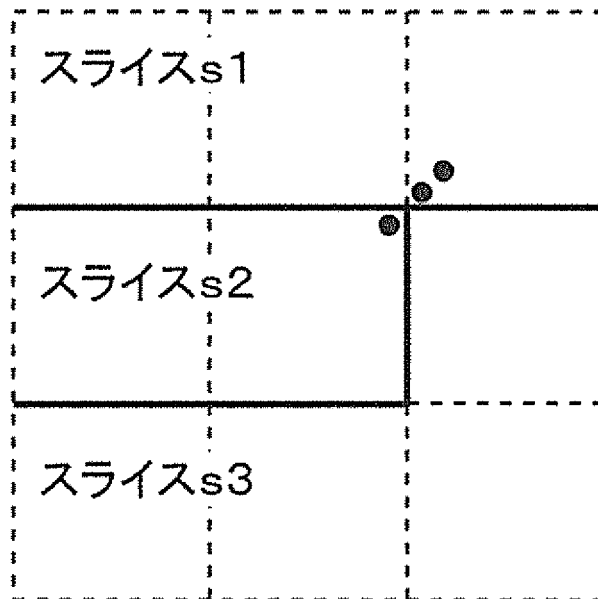
[図15A]



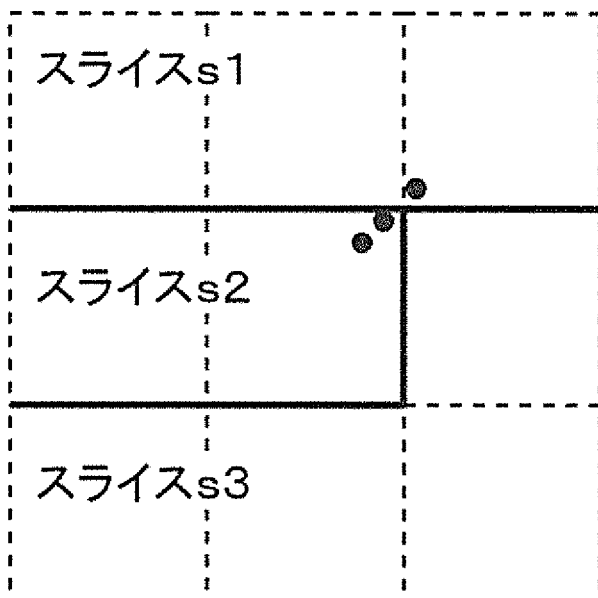
[図15B]



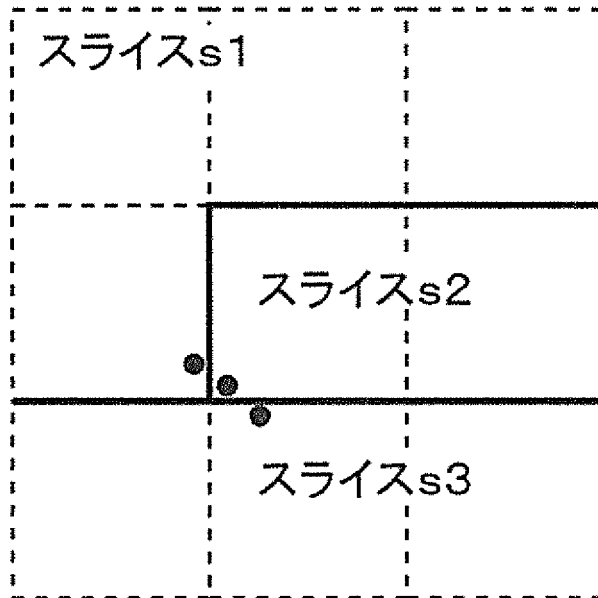
[図15C]



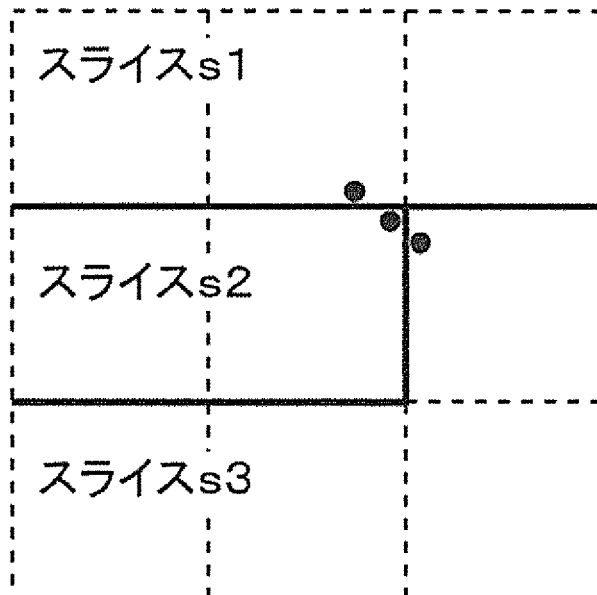
[図15D]



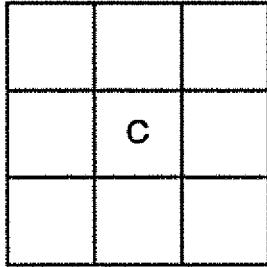
[図15E]



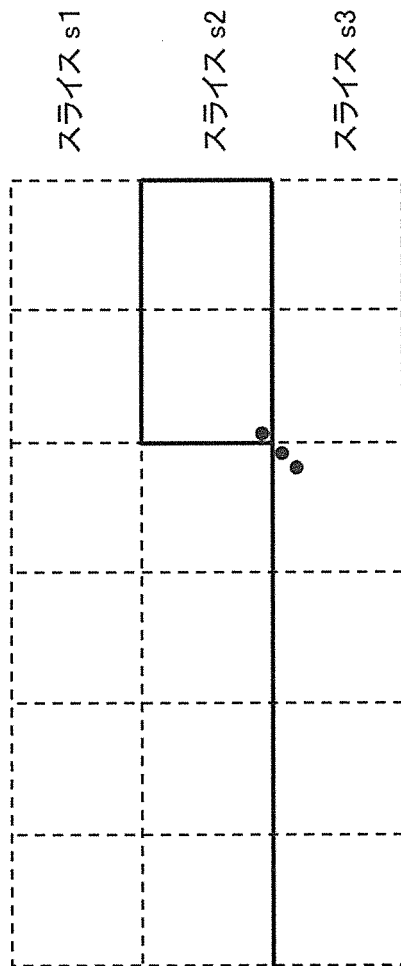
[図15F]



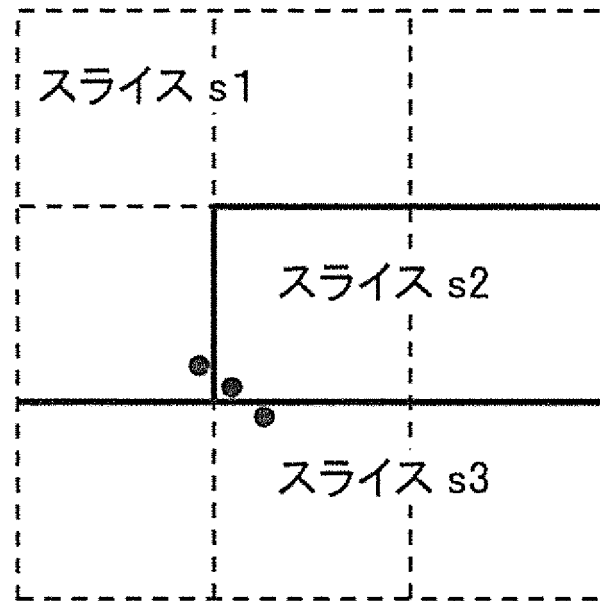
[図16]



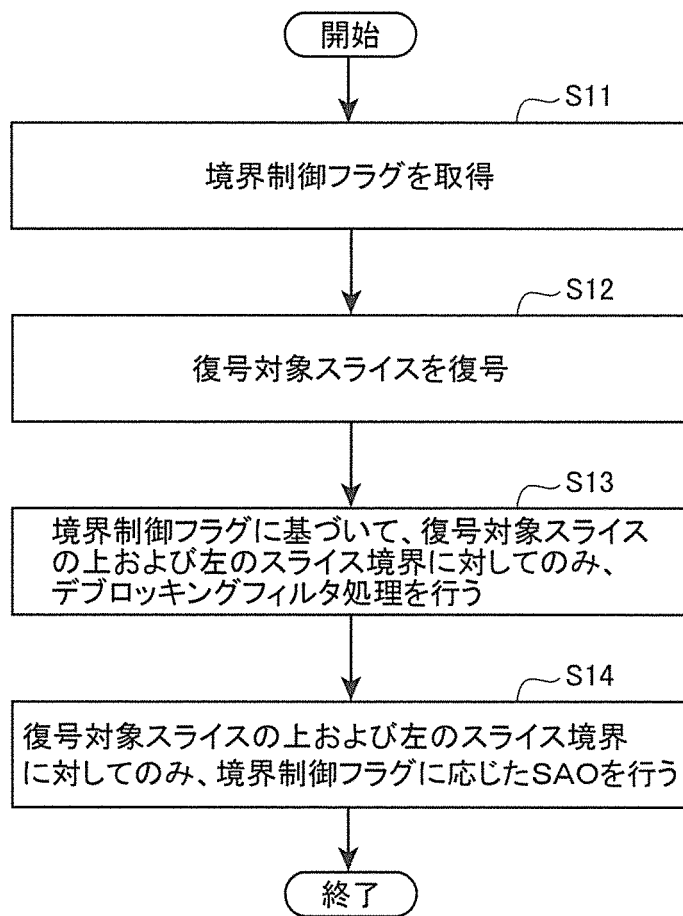
[図17]



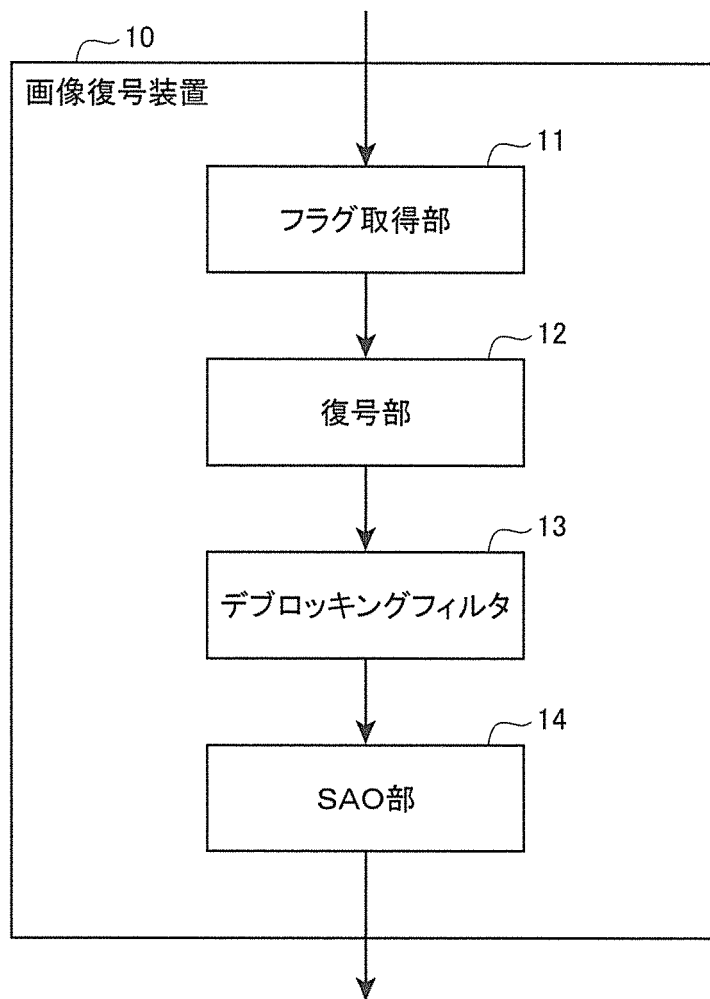
[図18]



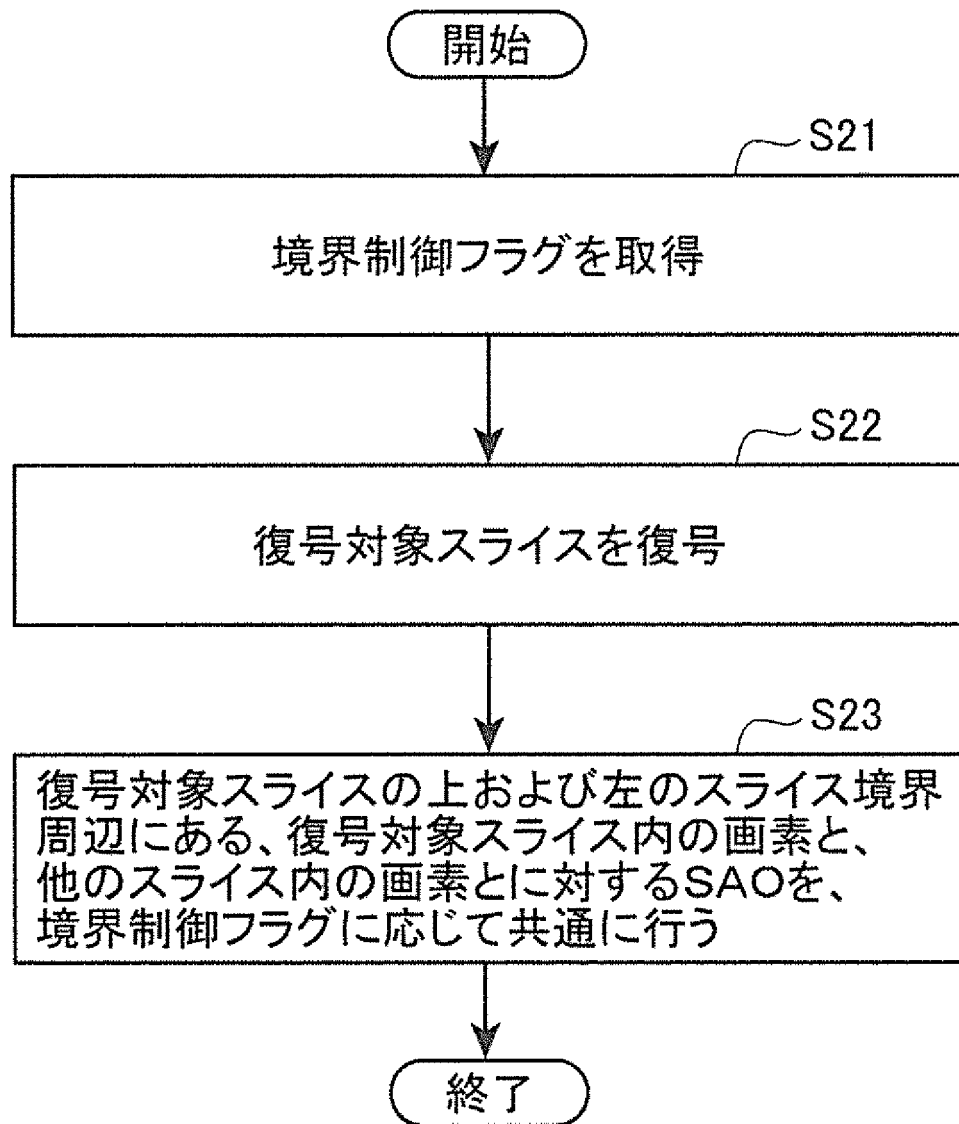
[図19A]



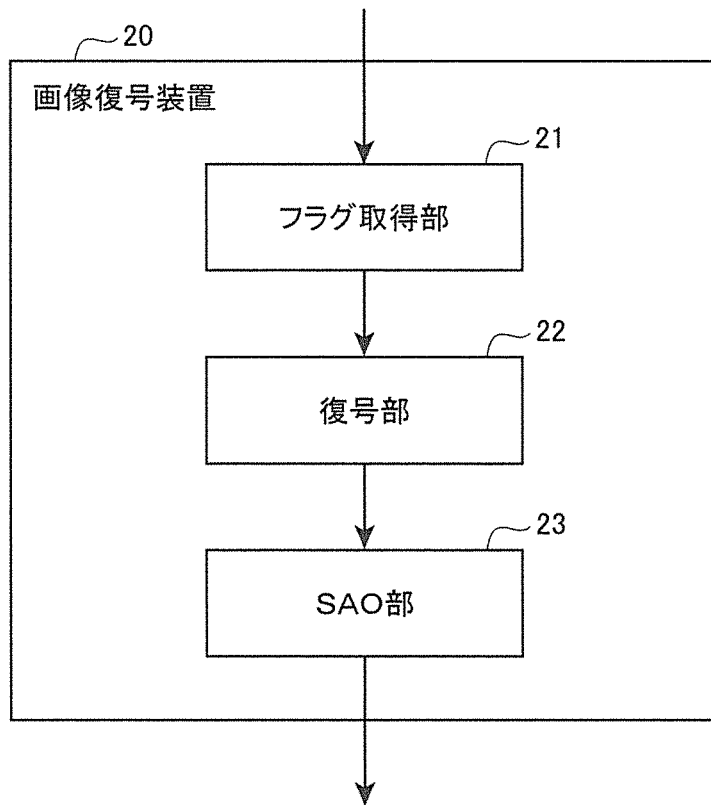
[図19B]



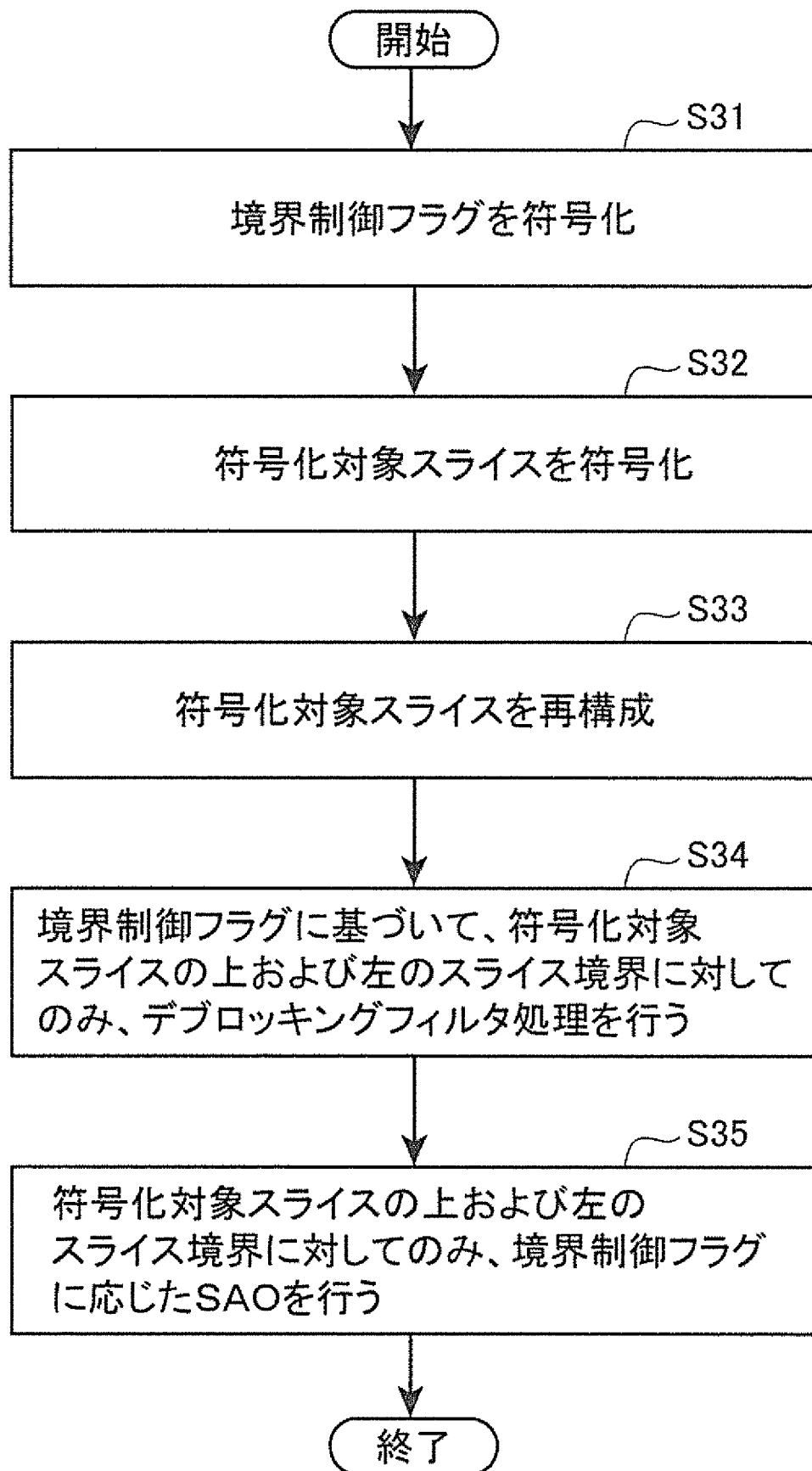
[図20A]



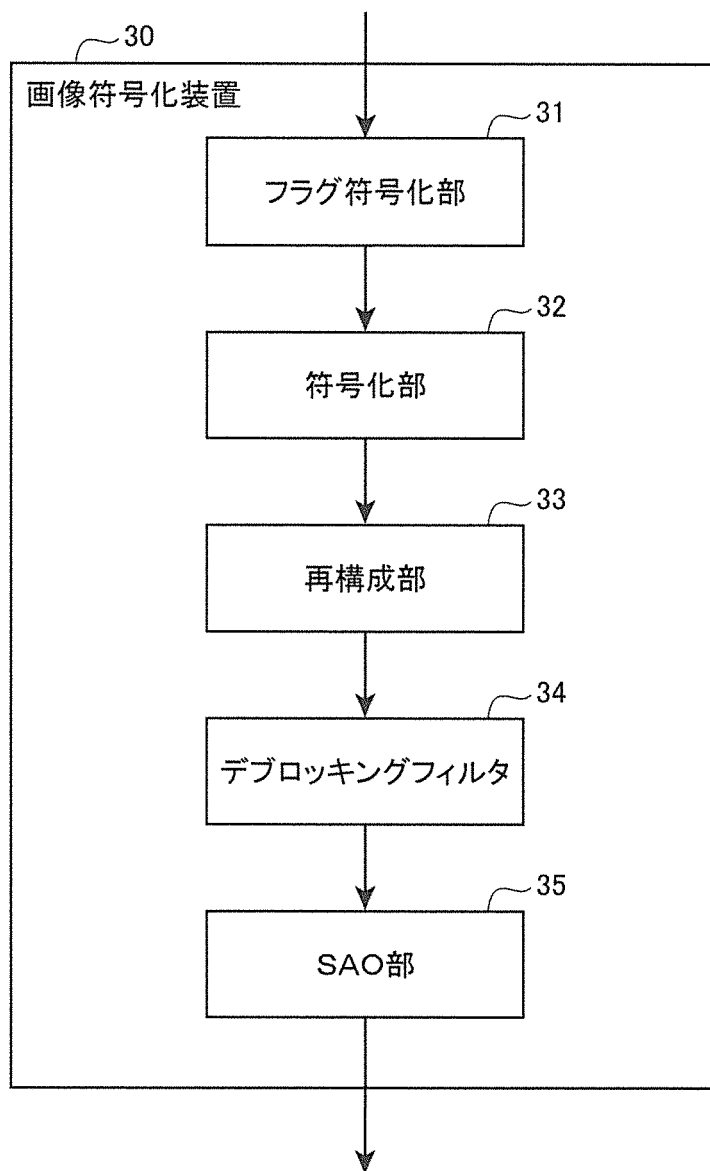
[図20B]



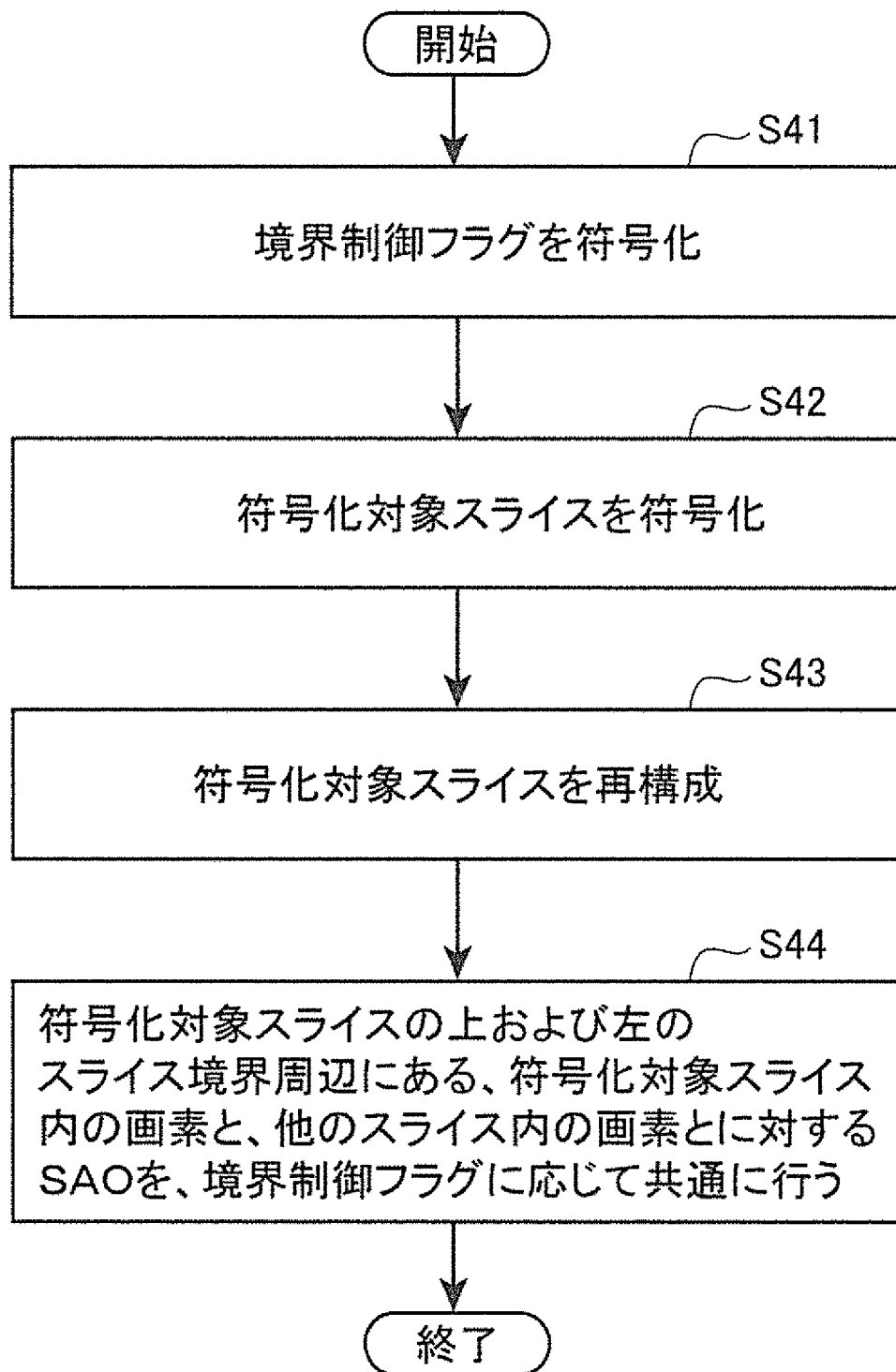
[図21A]



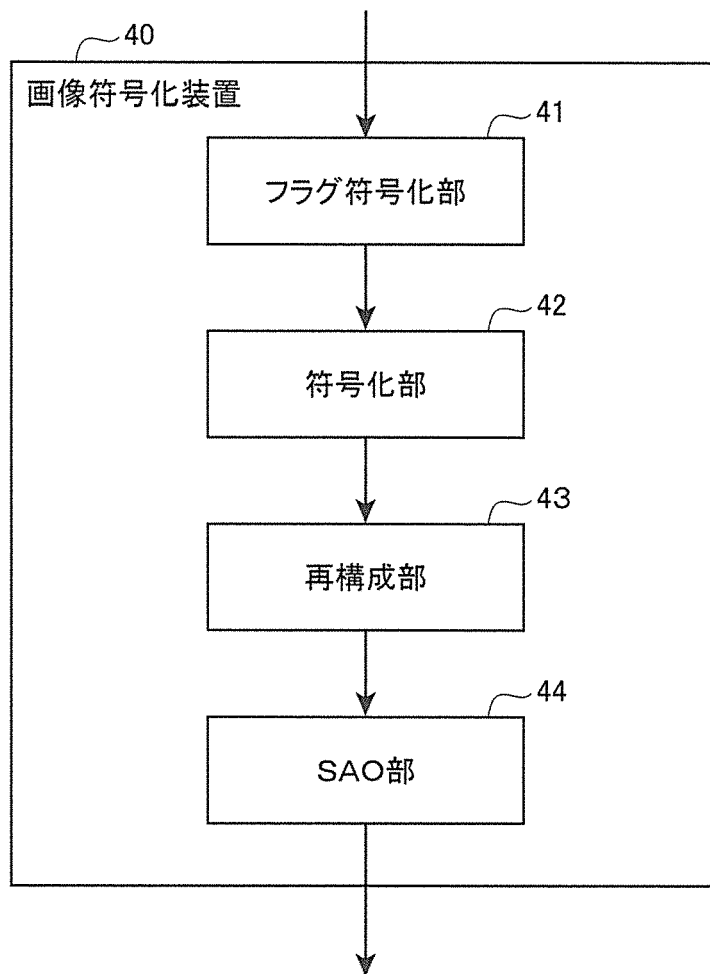
[図21B]



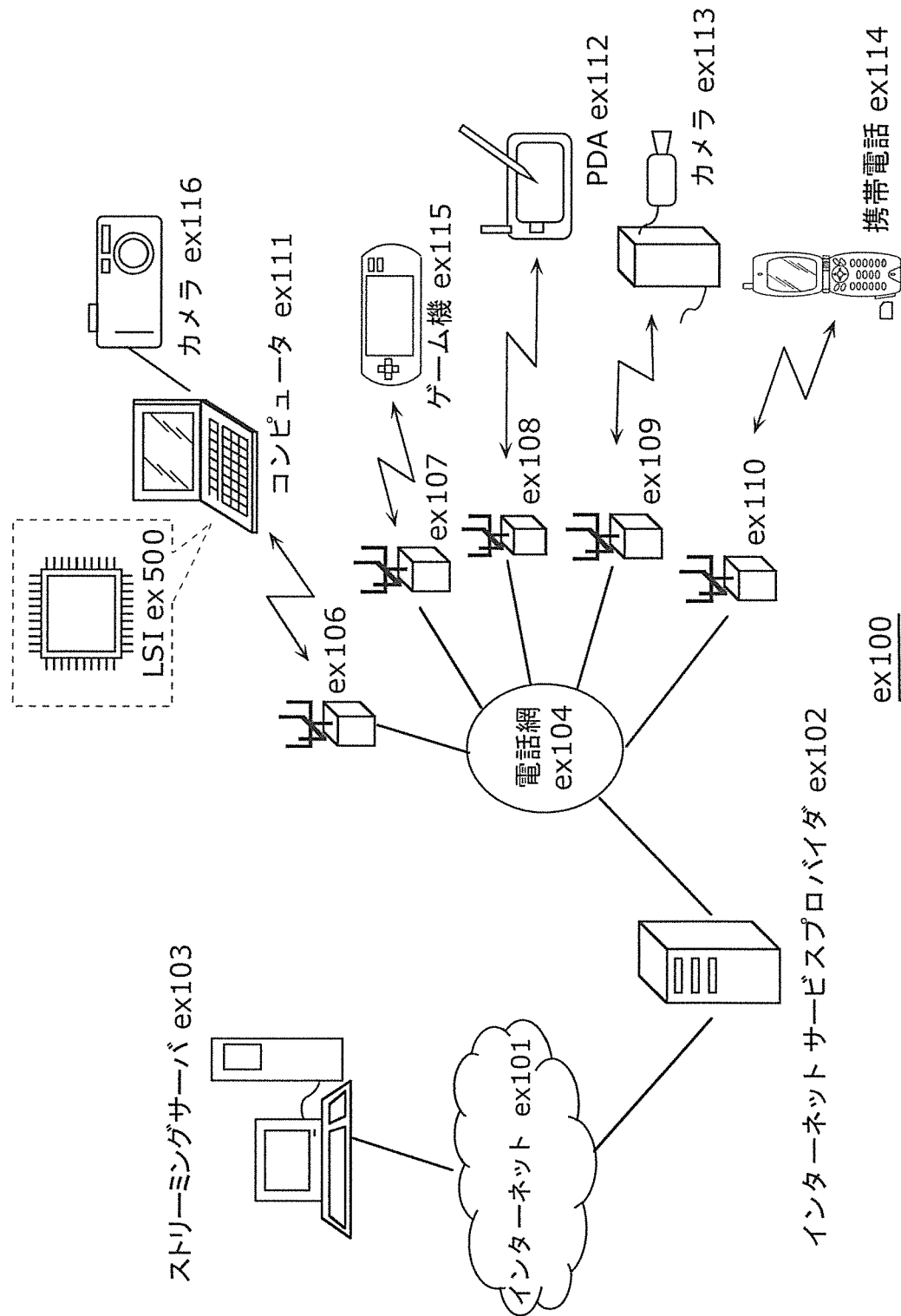
[図22A]



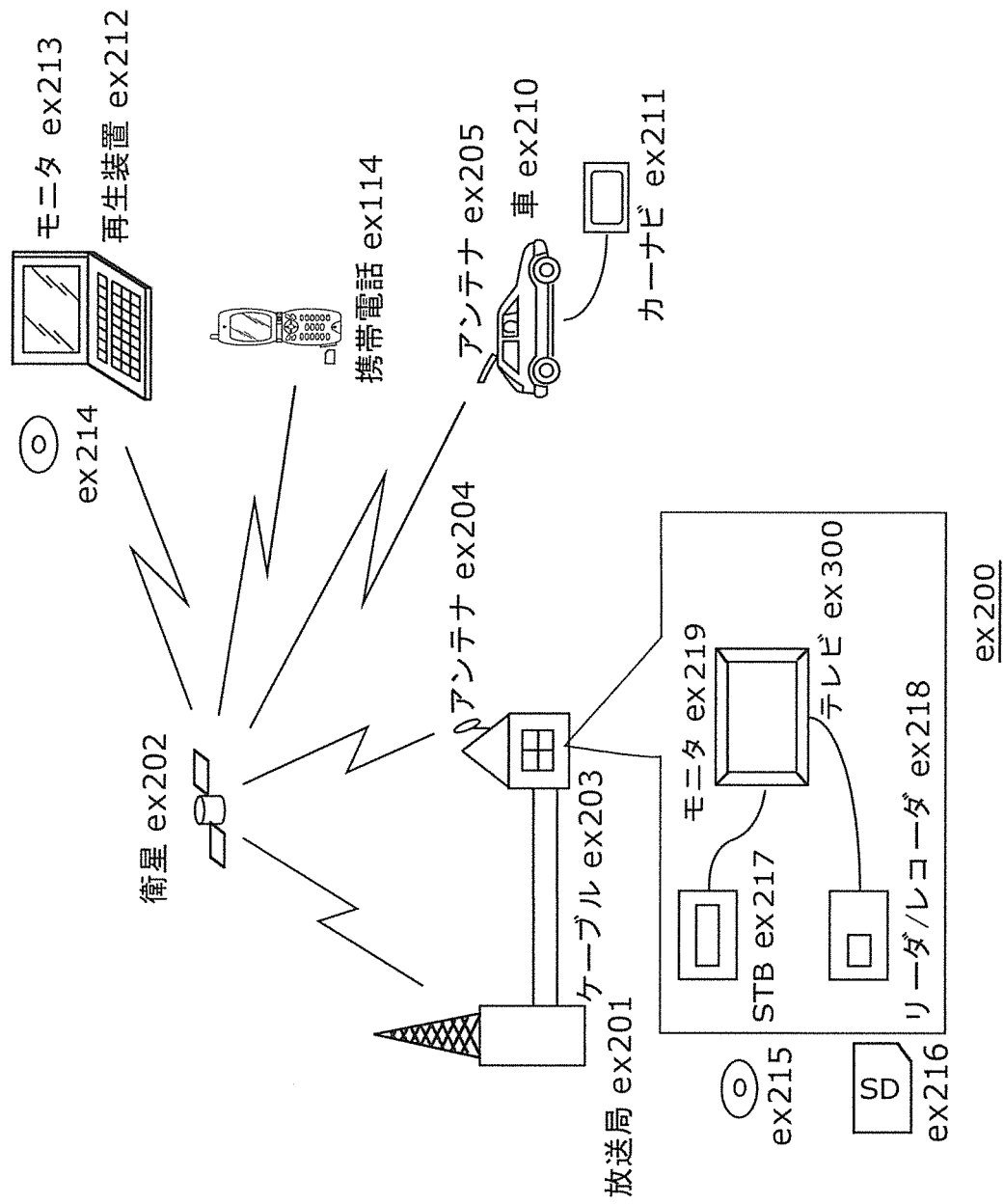
[図22B]



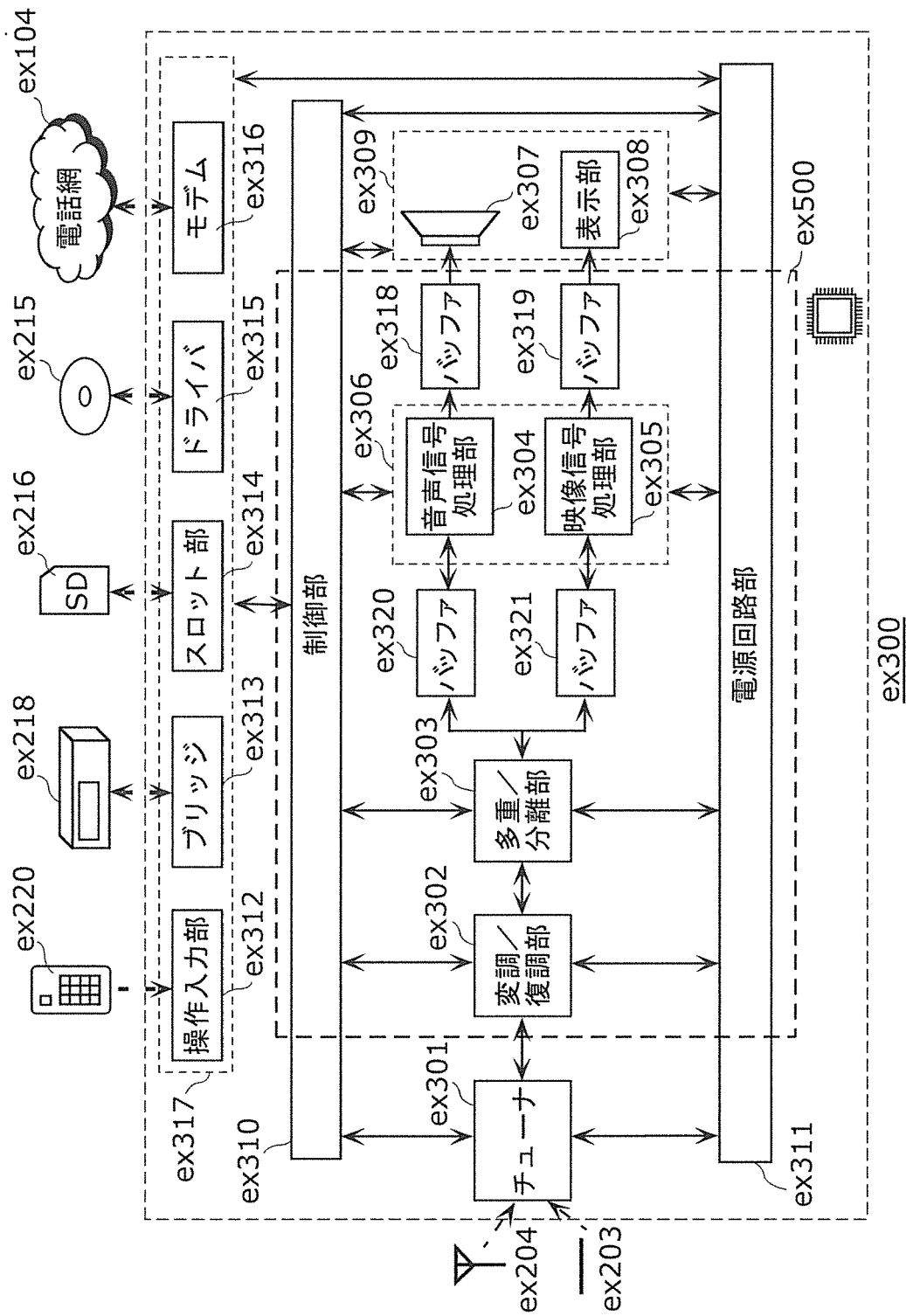
[図23]



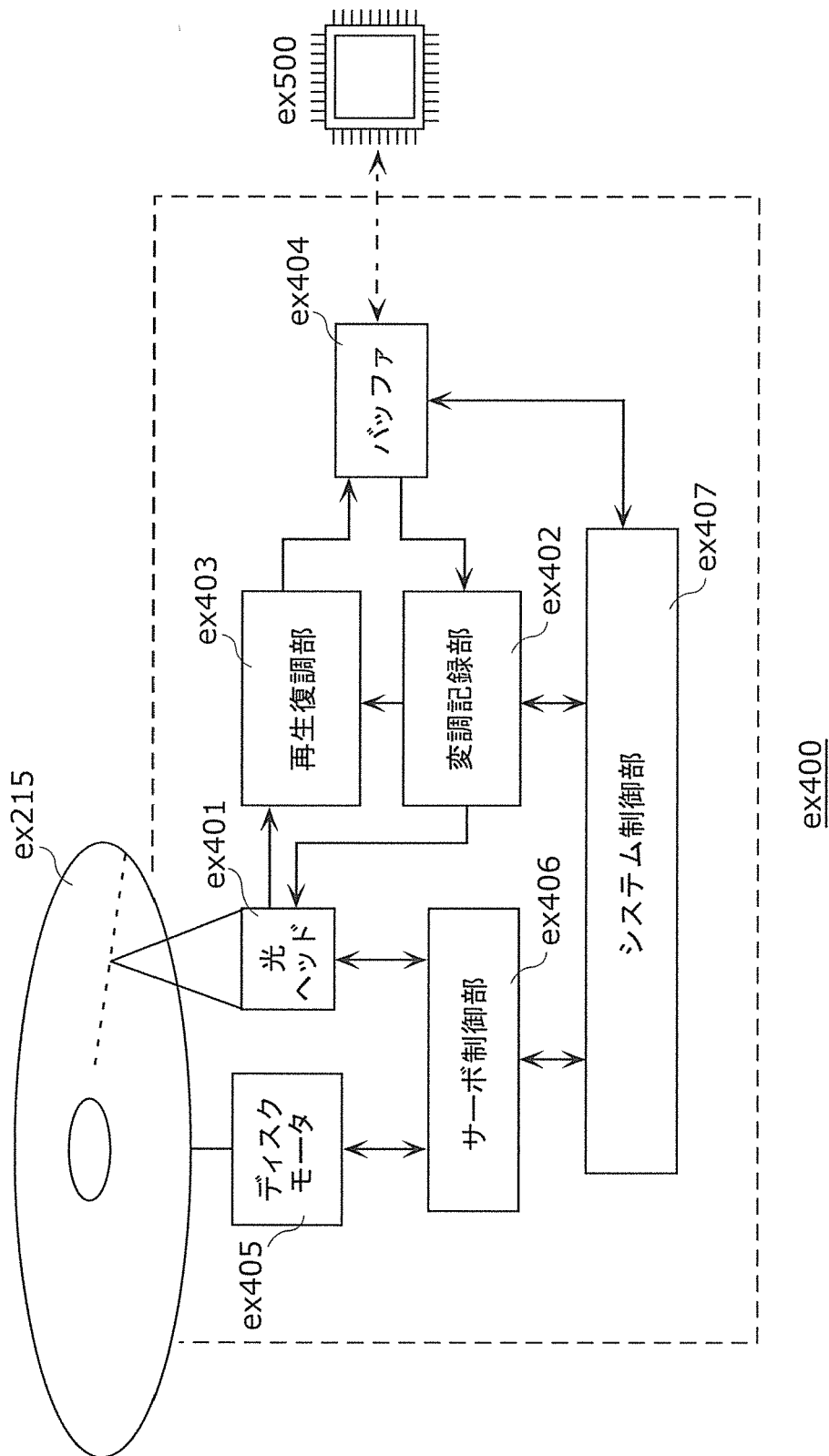
[図24]



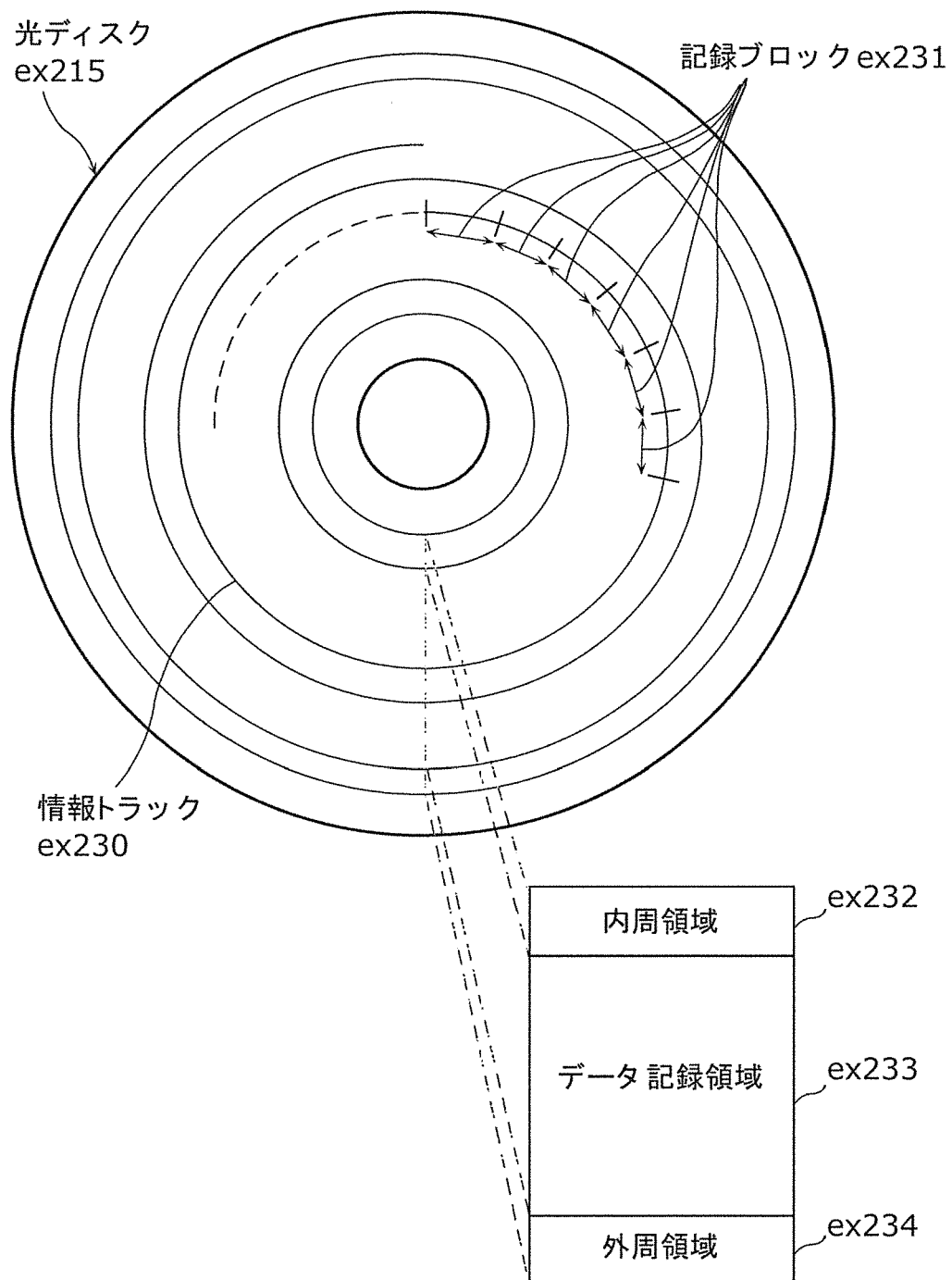
[図25]



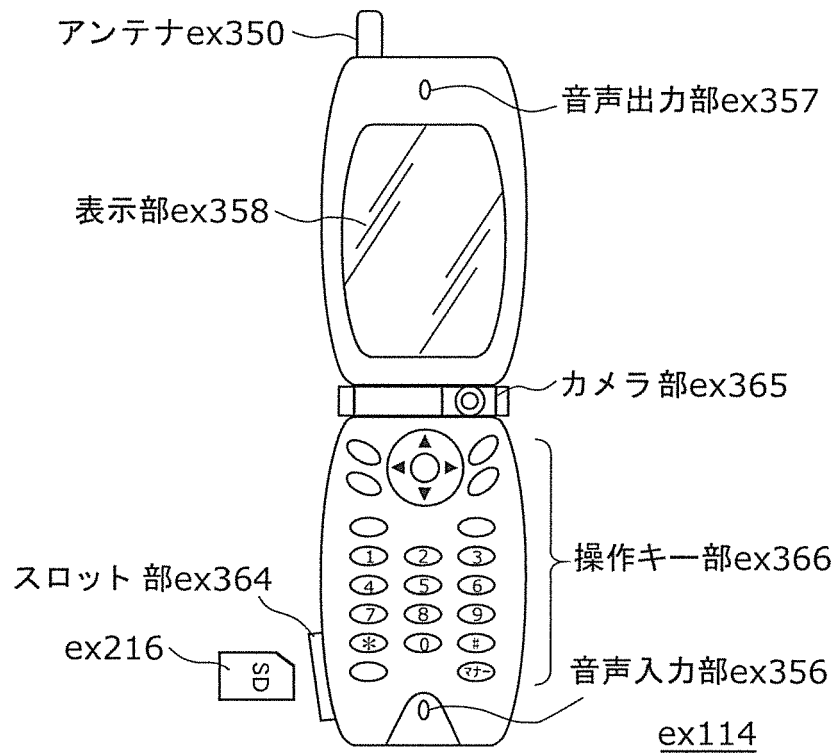
[図26]



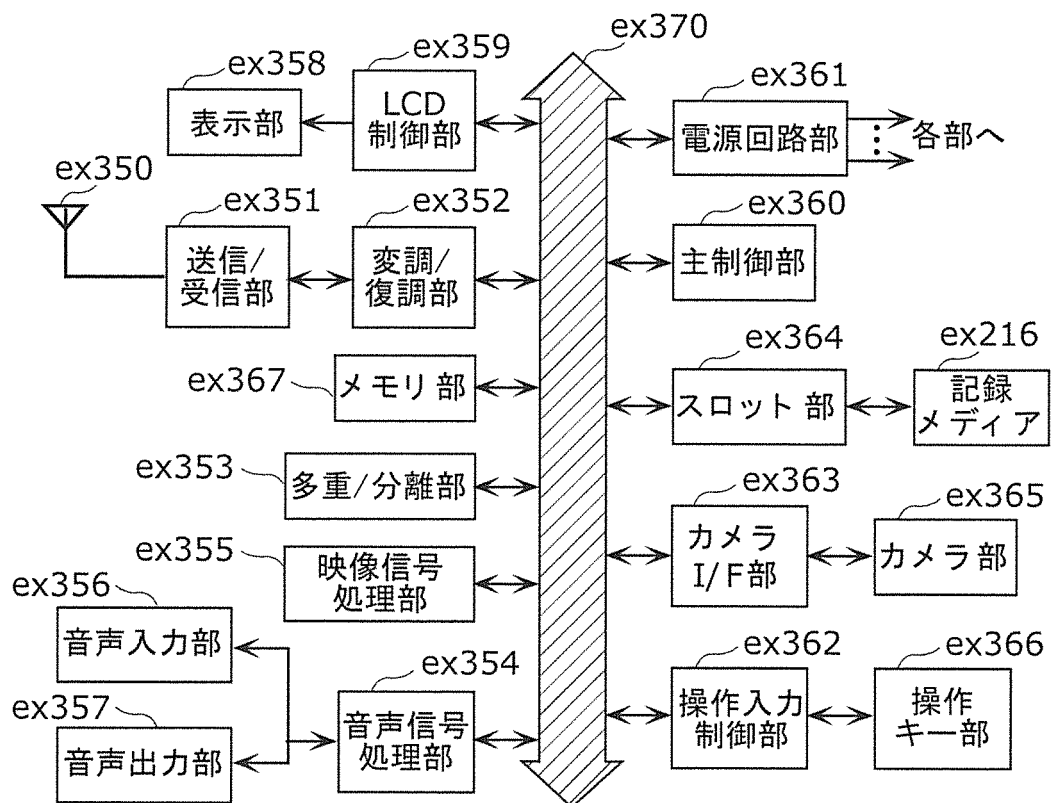
[図27]



[図28A]



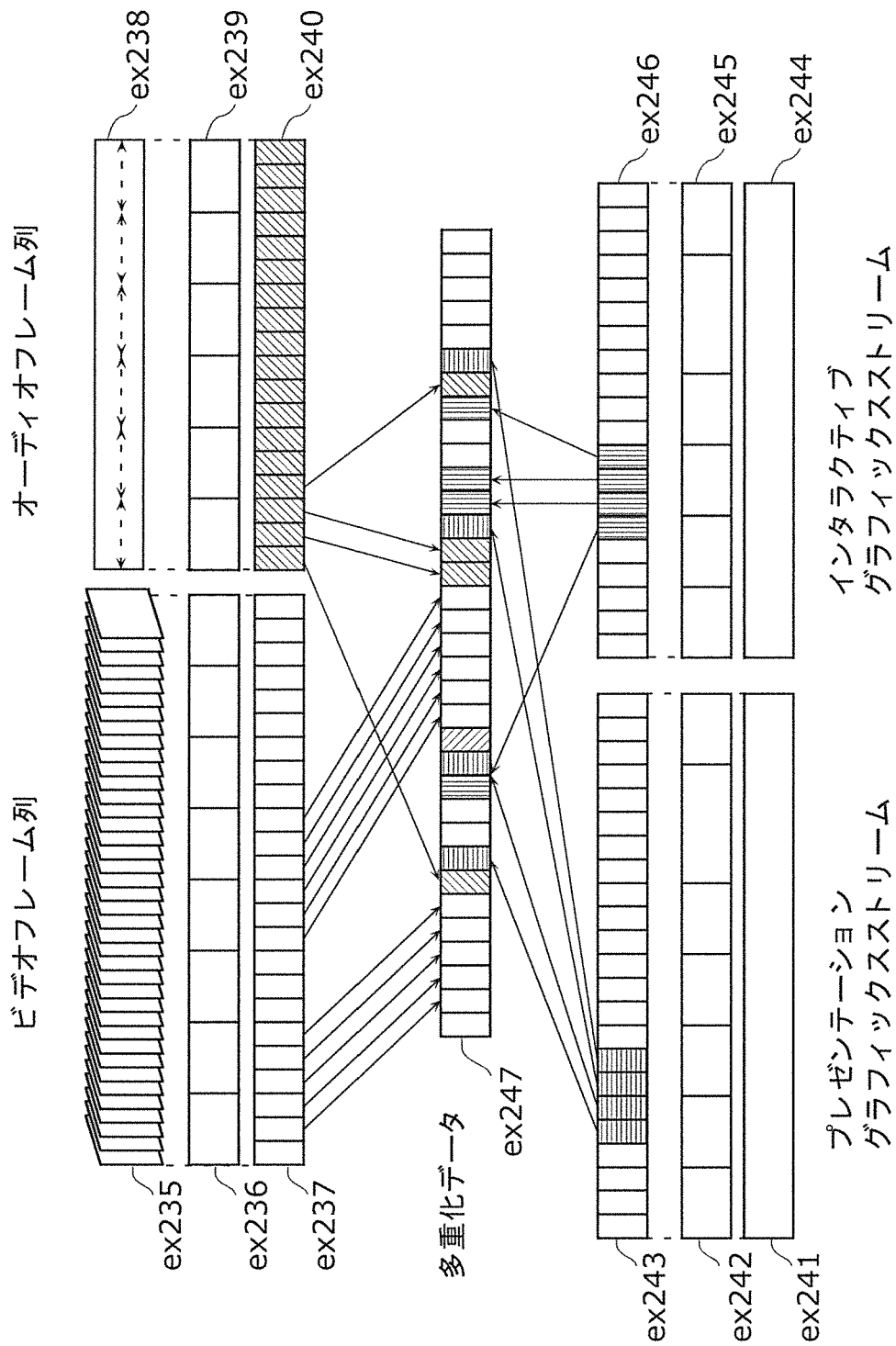
[図28B]



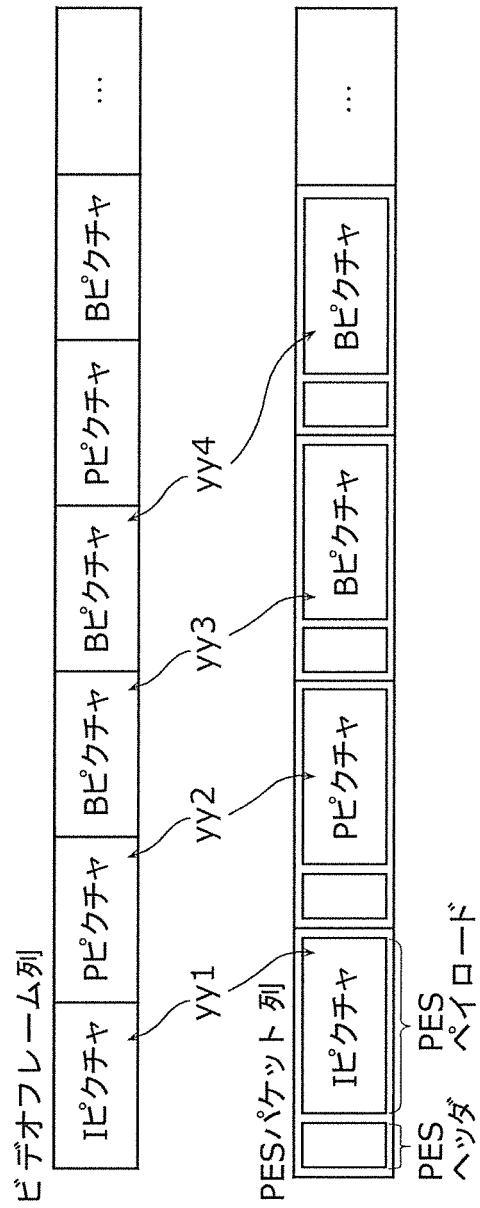
[図29]

ビデオストリーム(PID=0x1011 主映像)
オーディオストリーム(PID=0x1100)
オーディオストリーム(PID=0x1101)
プレゼンテーショングラフィックスストリーム(PID=0x1200)
プレゼンテーショングラフィックスストリーム(PID=0x1201)
インタラクティブグラフィックスストリーム(PID=0x1400)
ビデオストリーム(PID=0x1B00 副映像)
ビデオストリーム(PID=0x1B01 副映像)

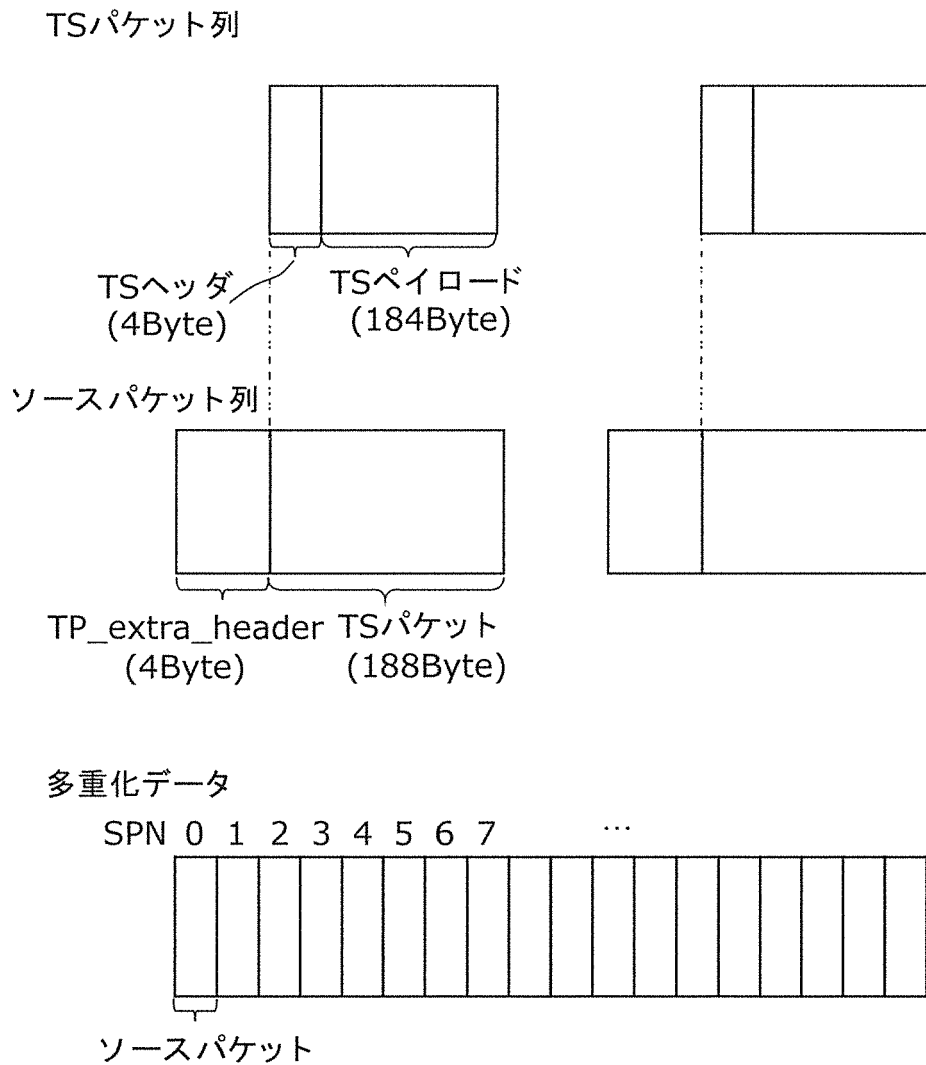
[図30]



[図31]

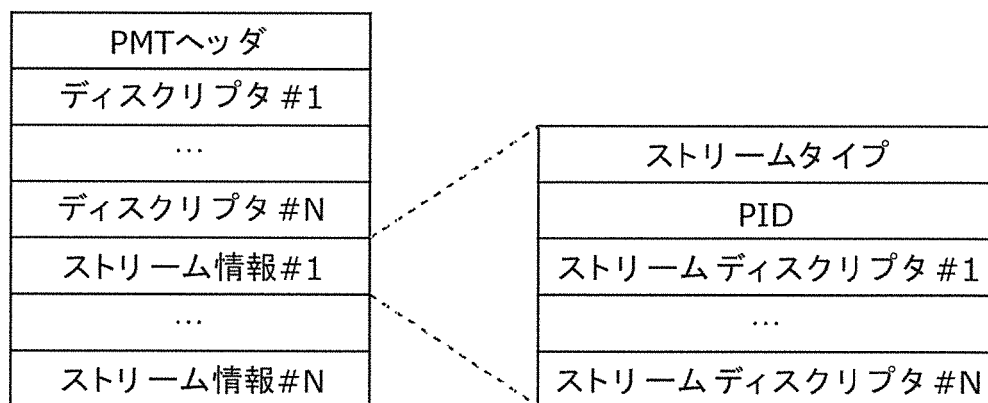


[図32]

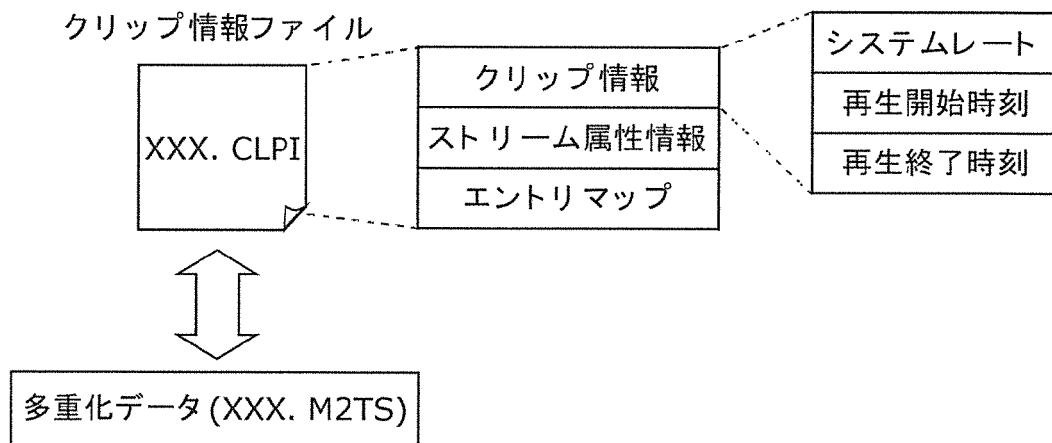


[図33]

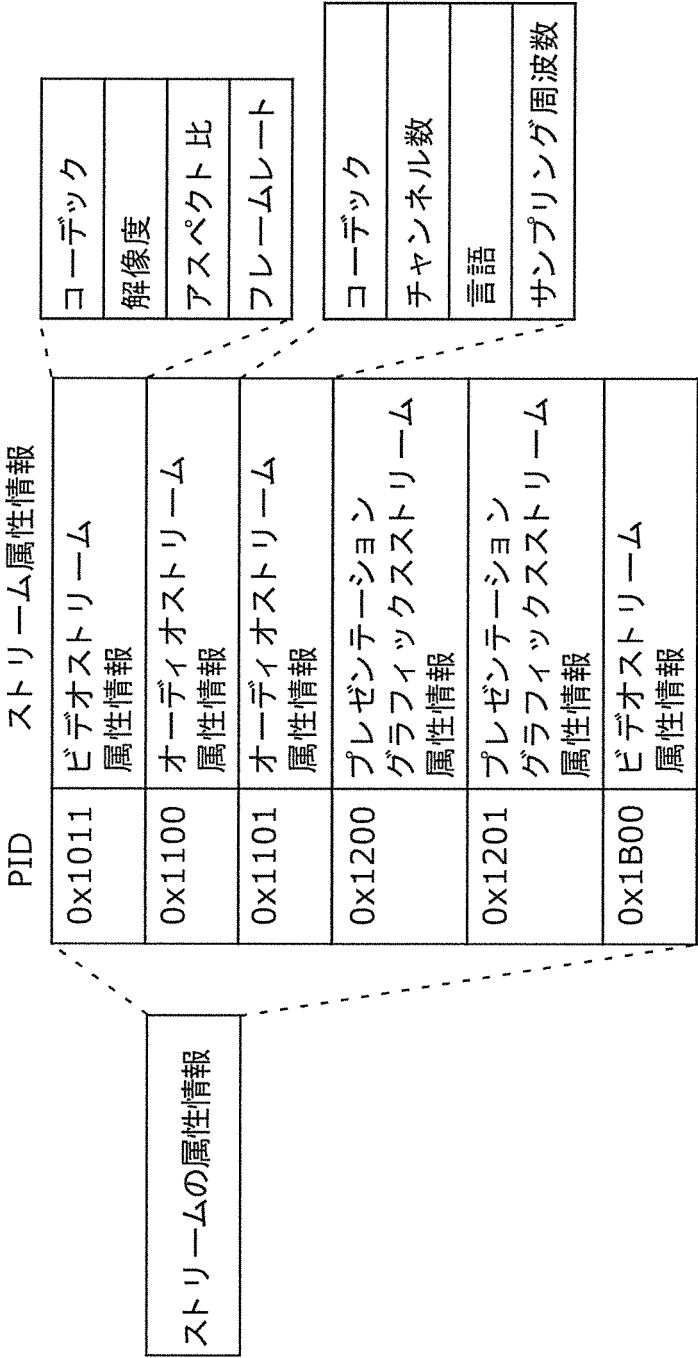
PMTのデータ構造



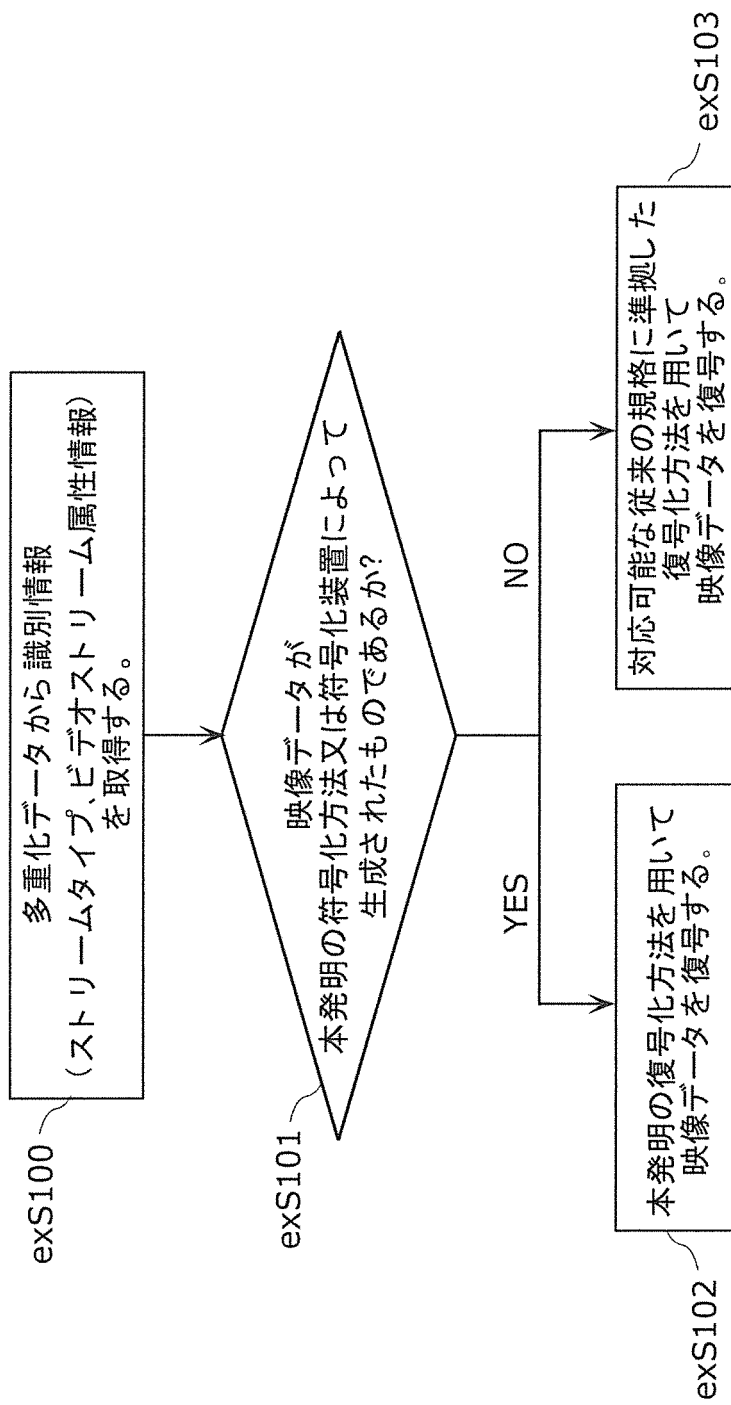
[図34]



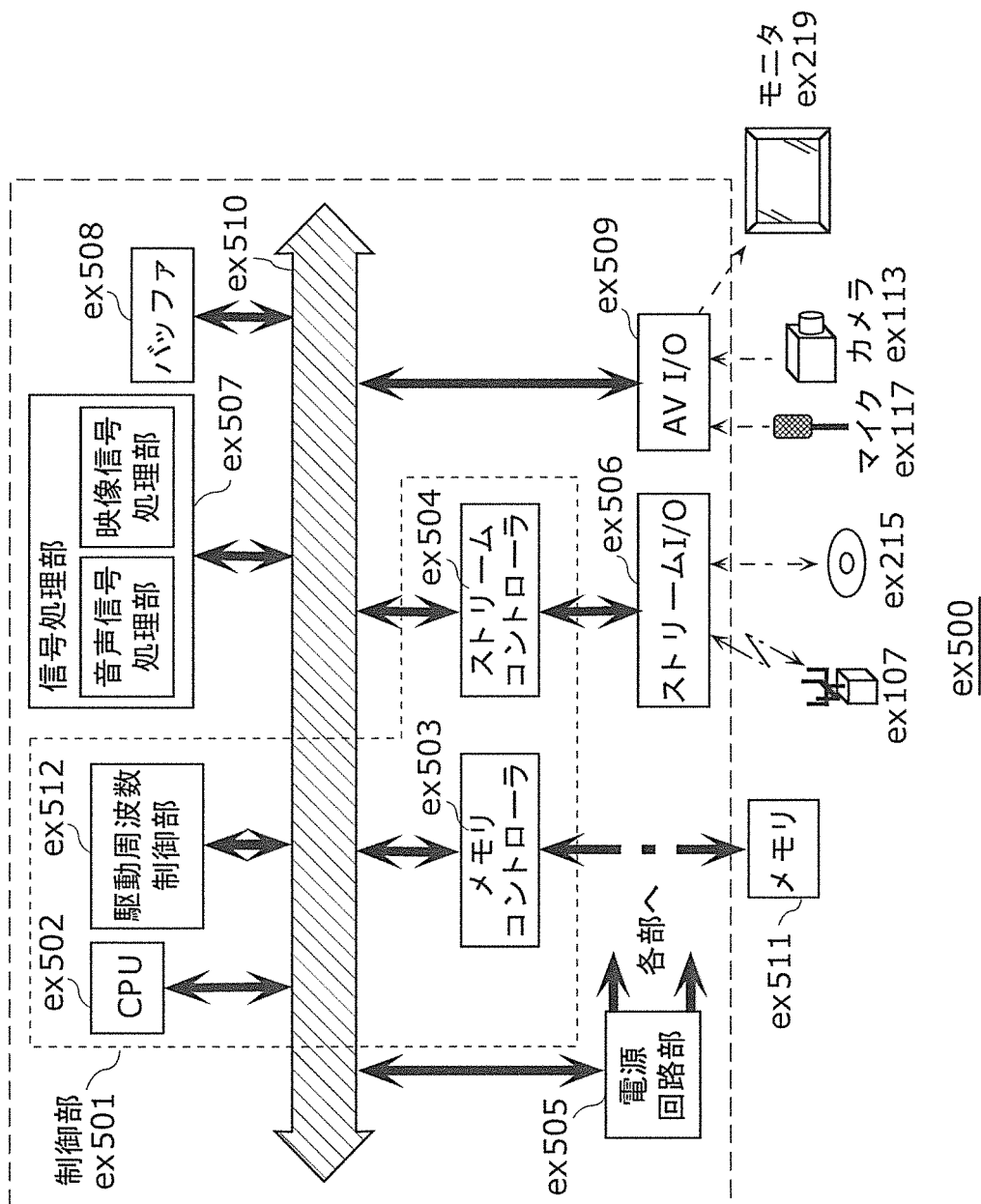
[図35]



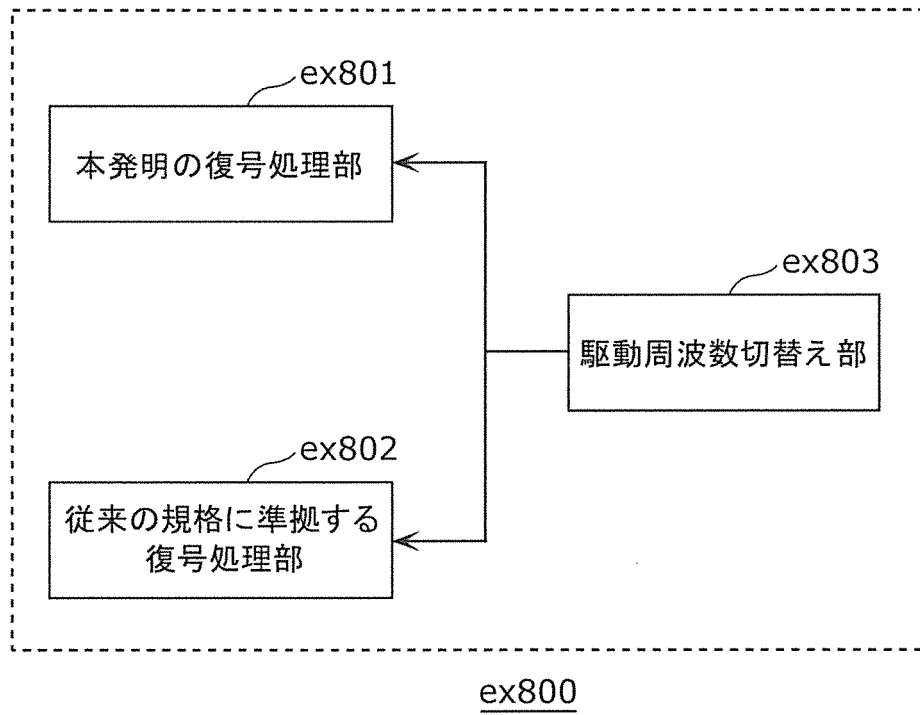
[図36]



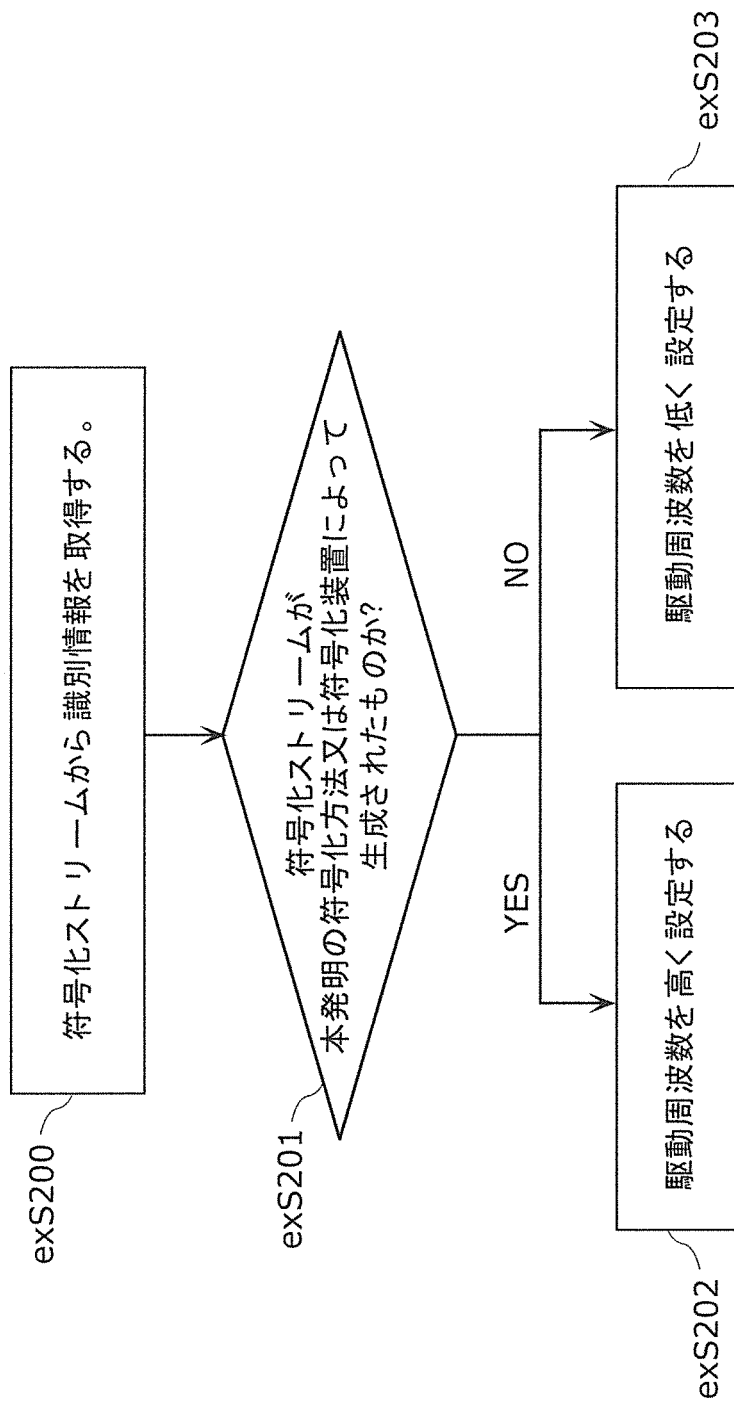
[図37]



[図38]



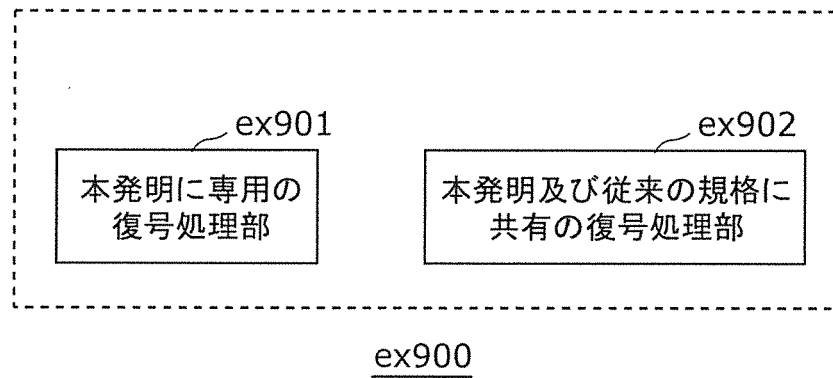
[図39]



[図40]

対応規格	駆動周波数
MPEG4.AVC	500MHz
MPEG2	350MHz
...	...

[図41A]



[図41B]

