

1/15

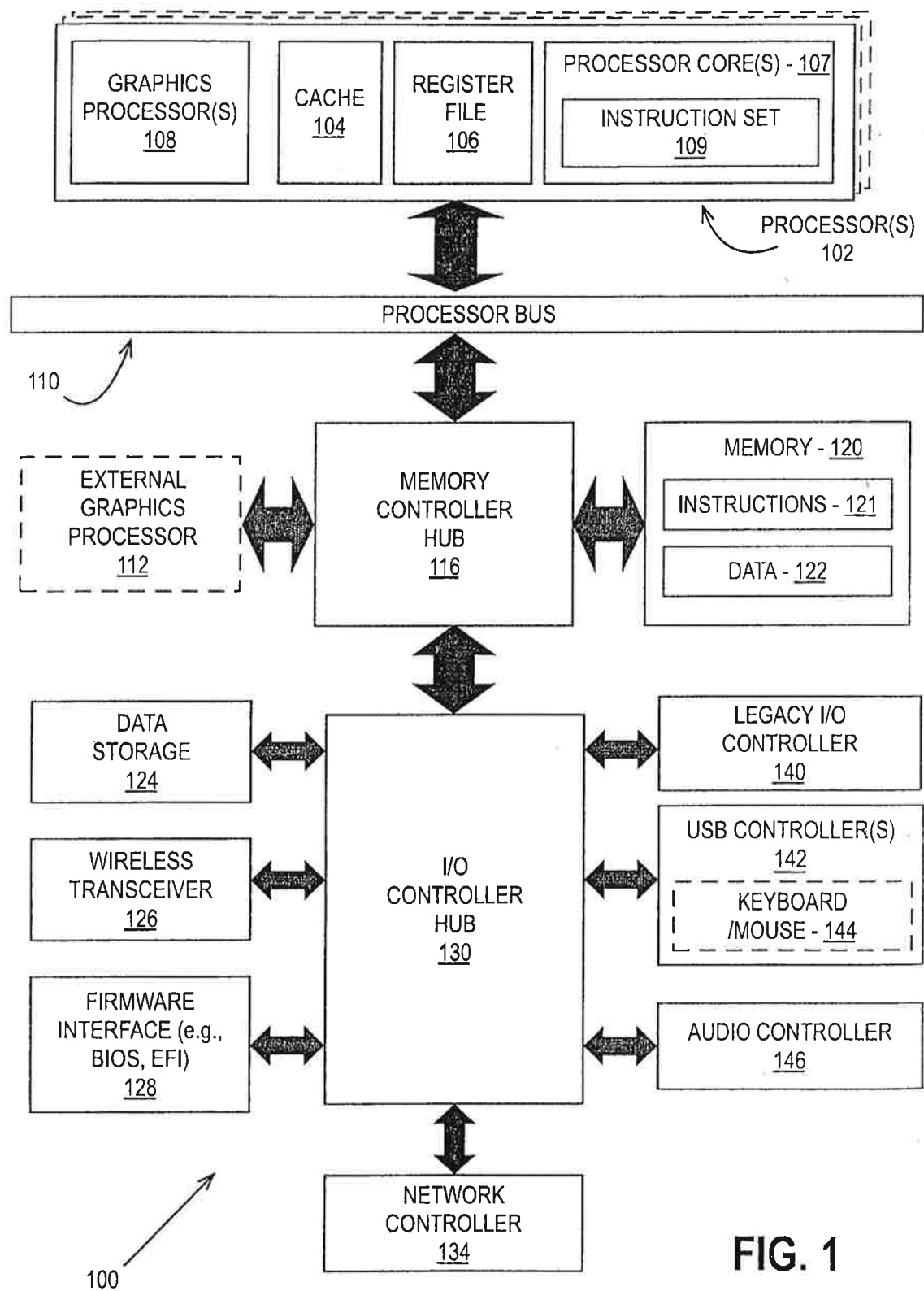


FIG. 1

PROCESSOR 200

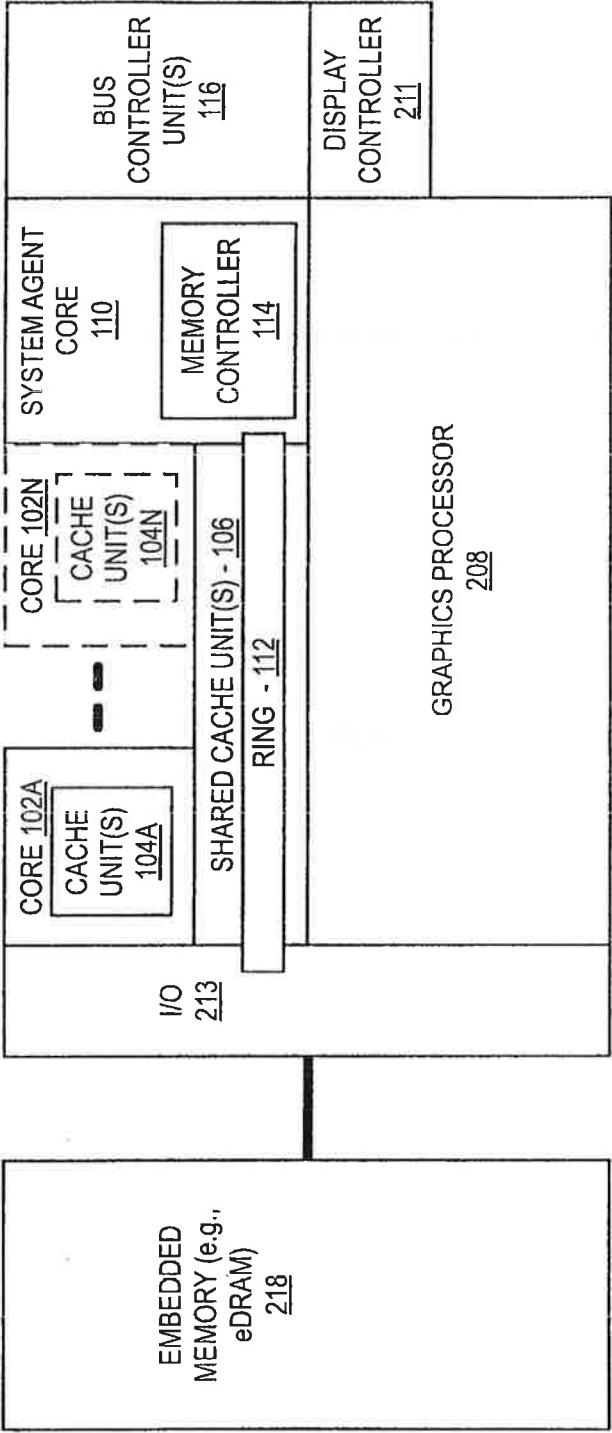


FIG. 2

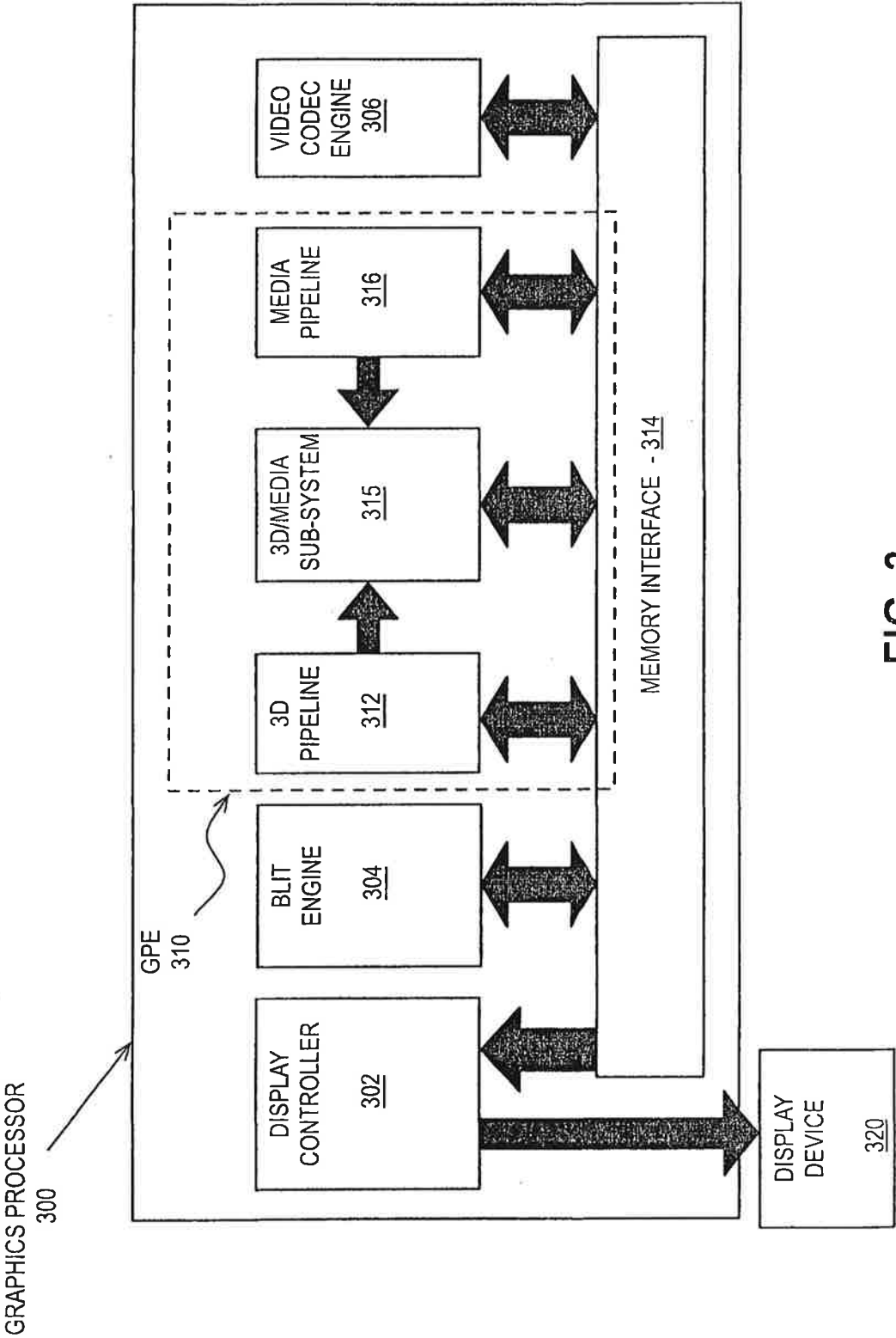


FIG. 3

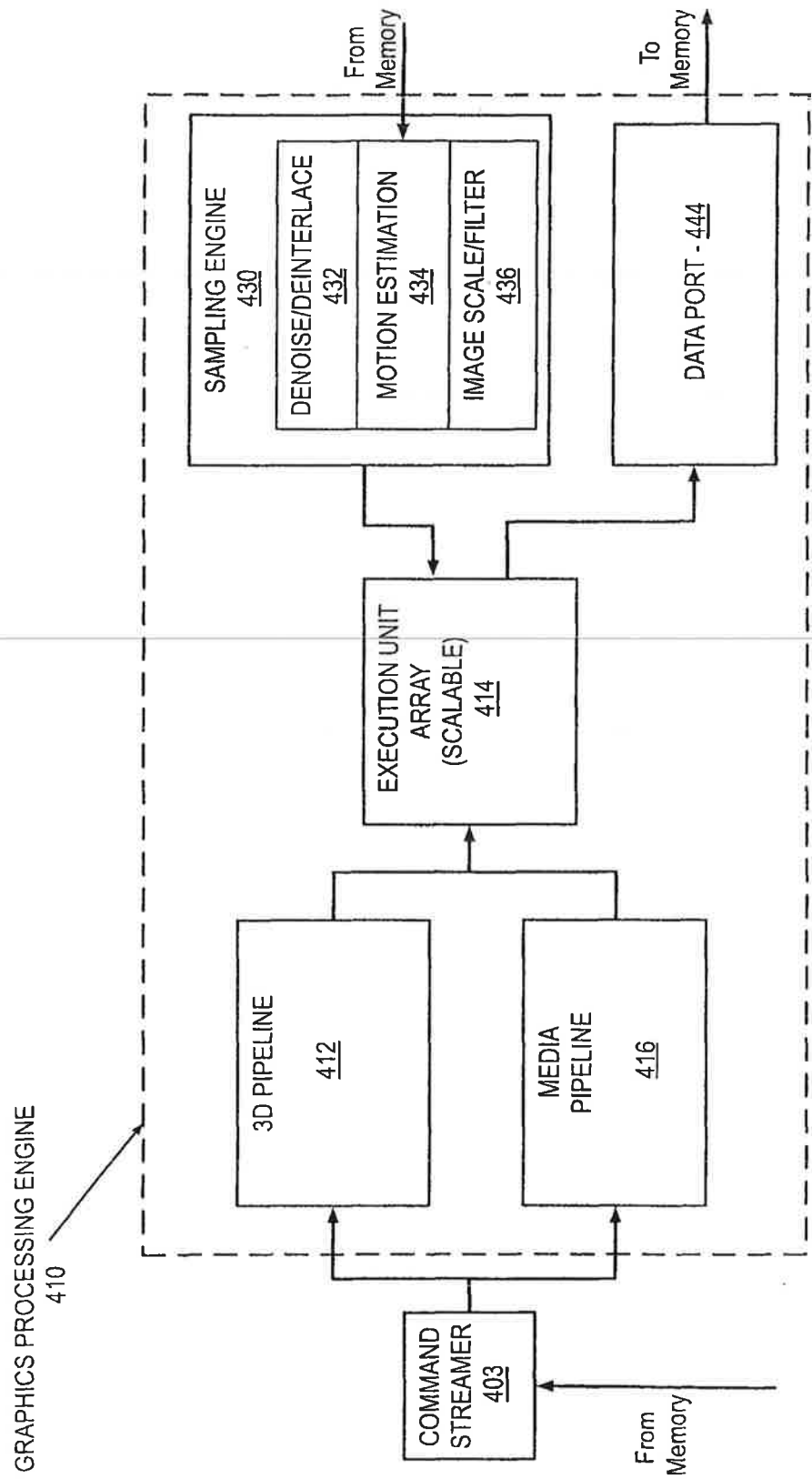


FIG. 4

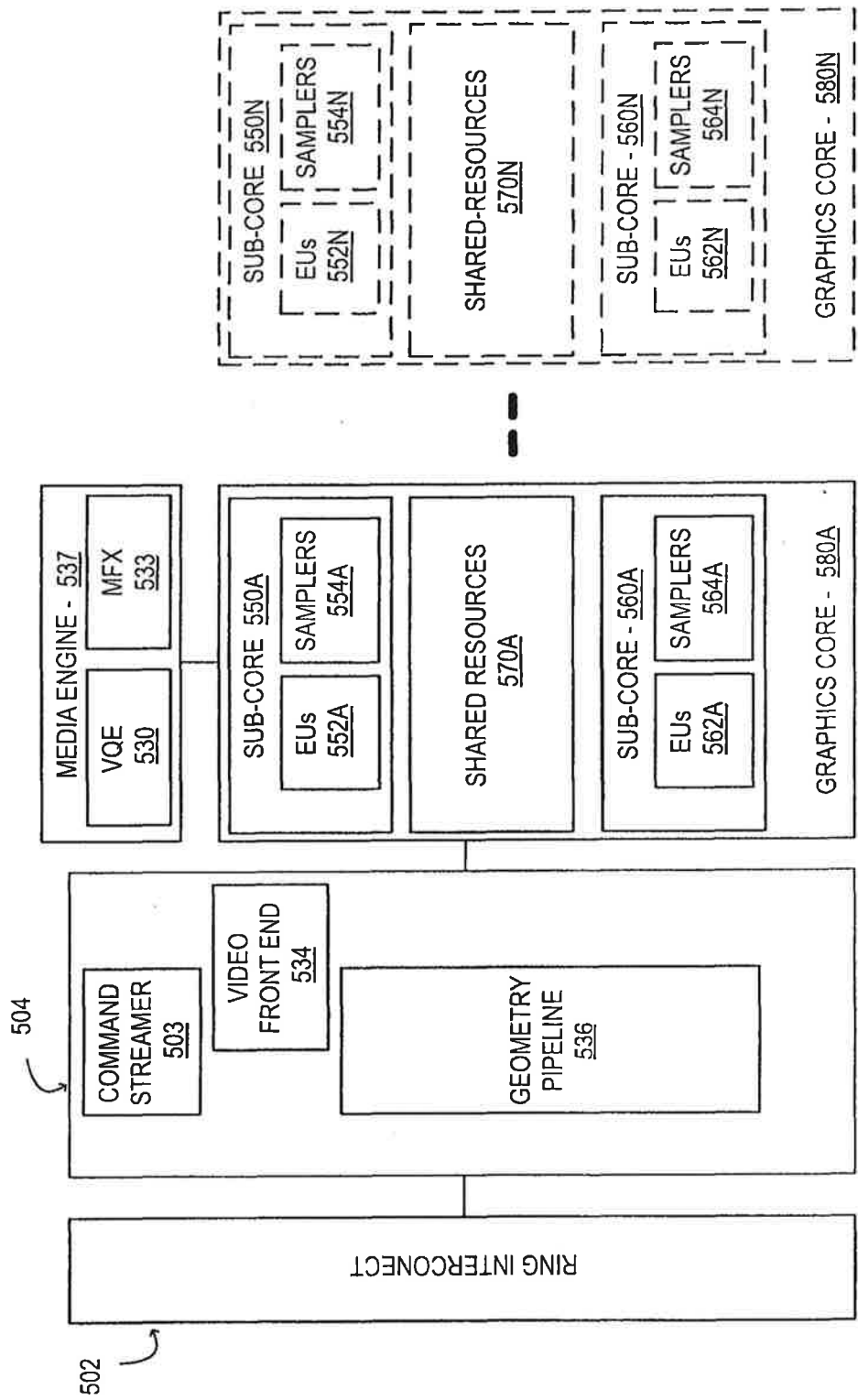


FIG. 5

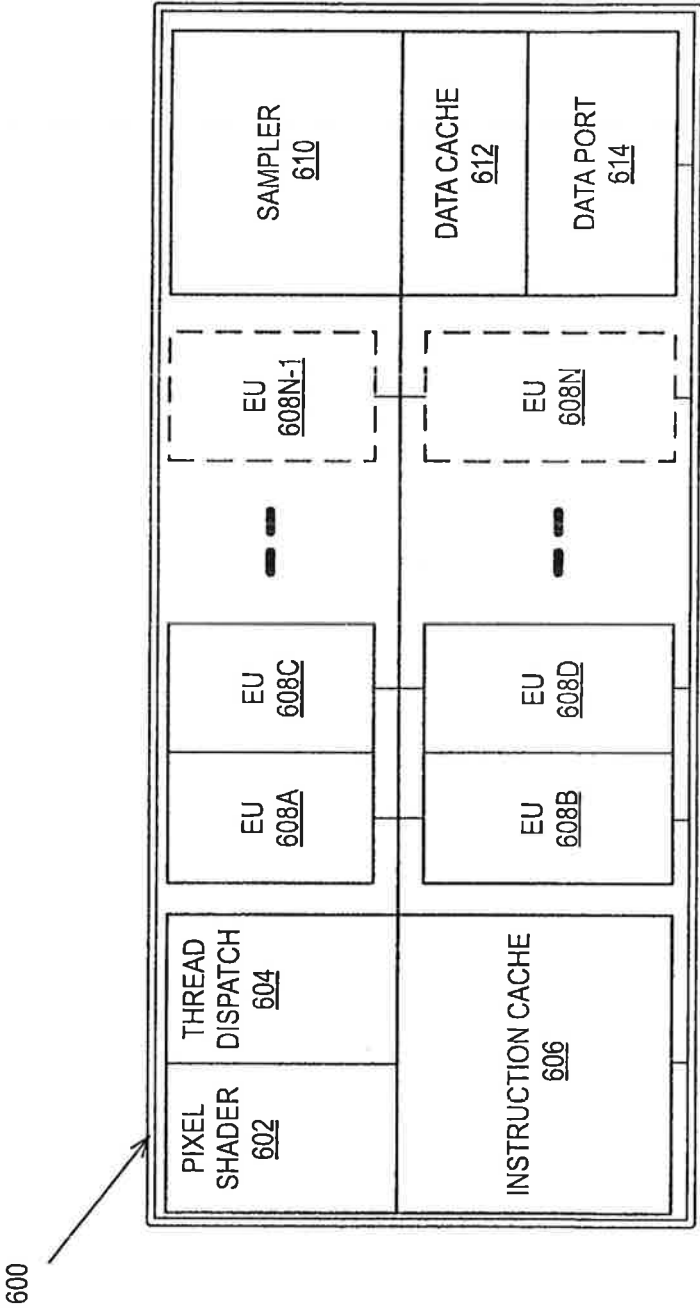


FIG. 6

GRAPHICS CORE INSTRUCTION FORMATS

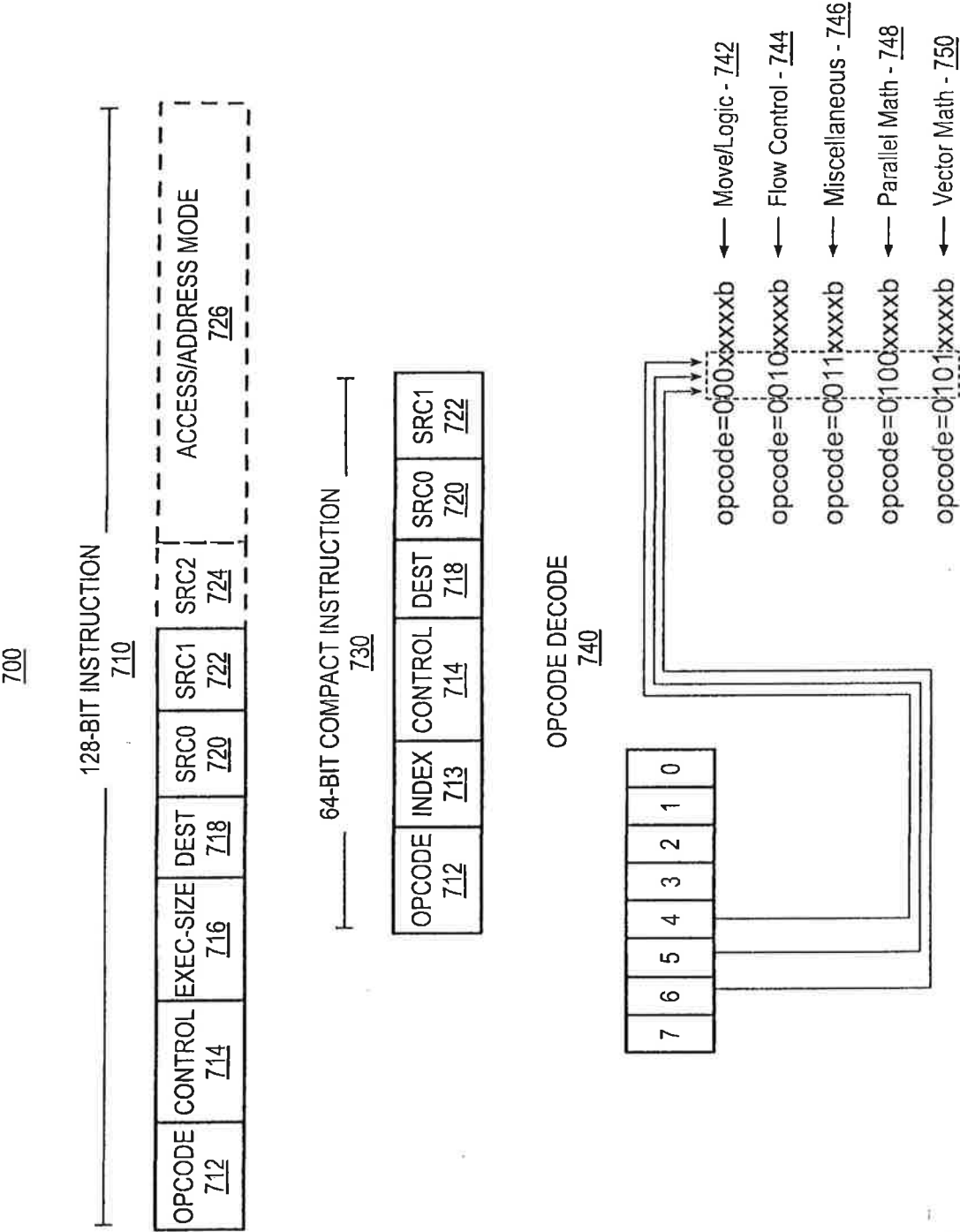


FIG. 7

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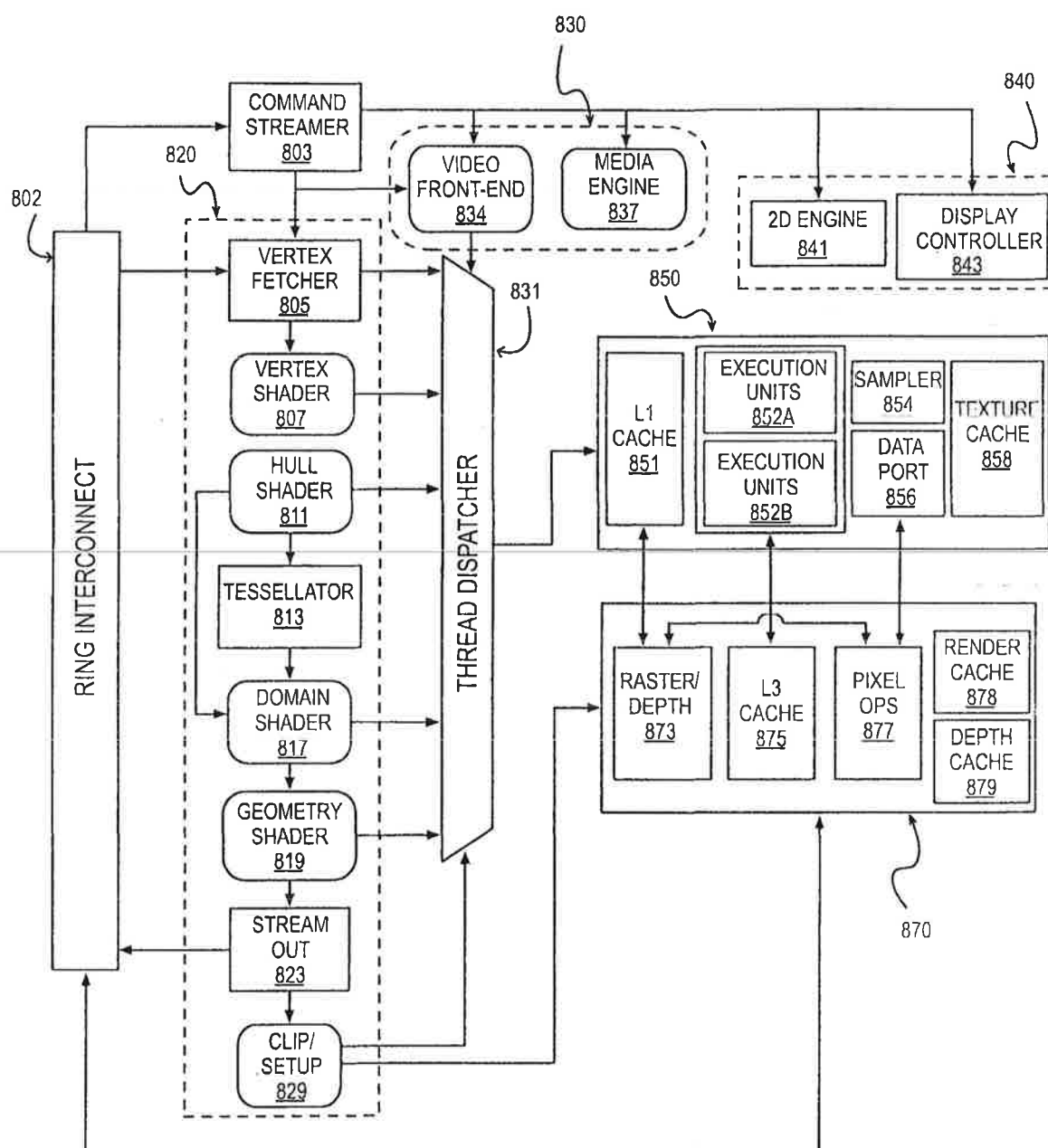


FIG. 8

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FIG. 9A

SAMPLE COMMAND FORMAT

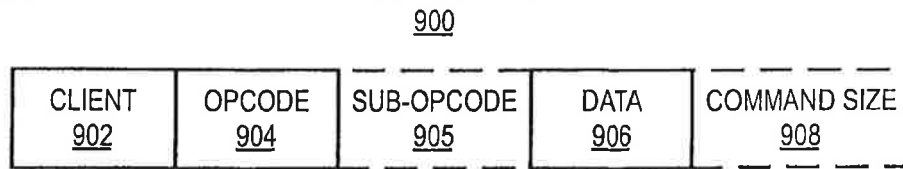
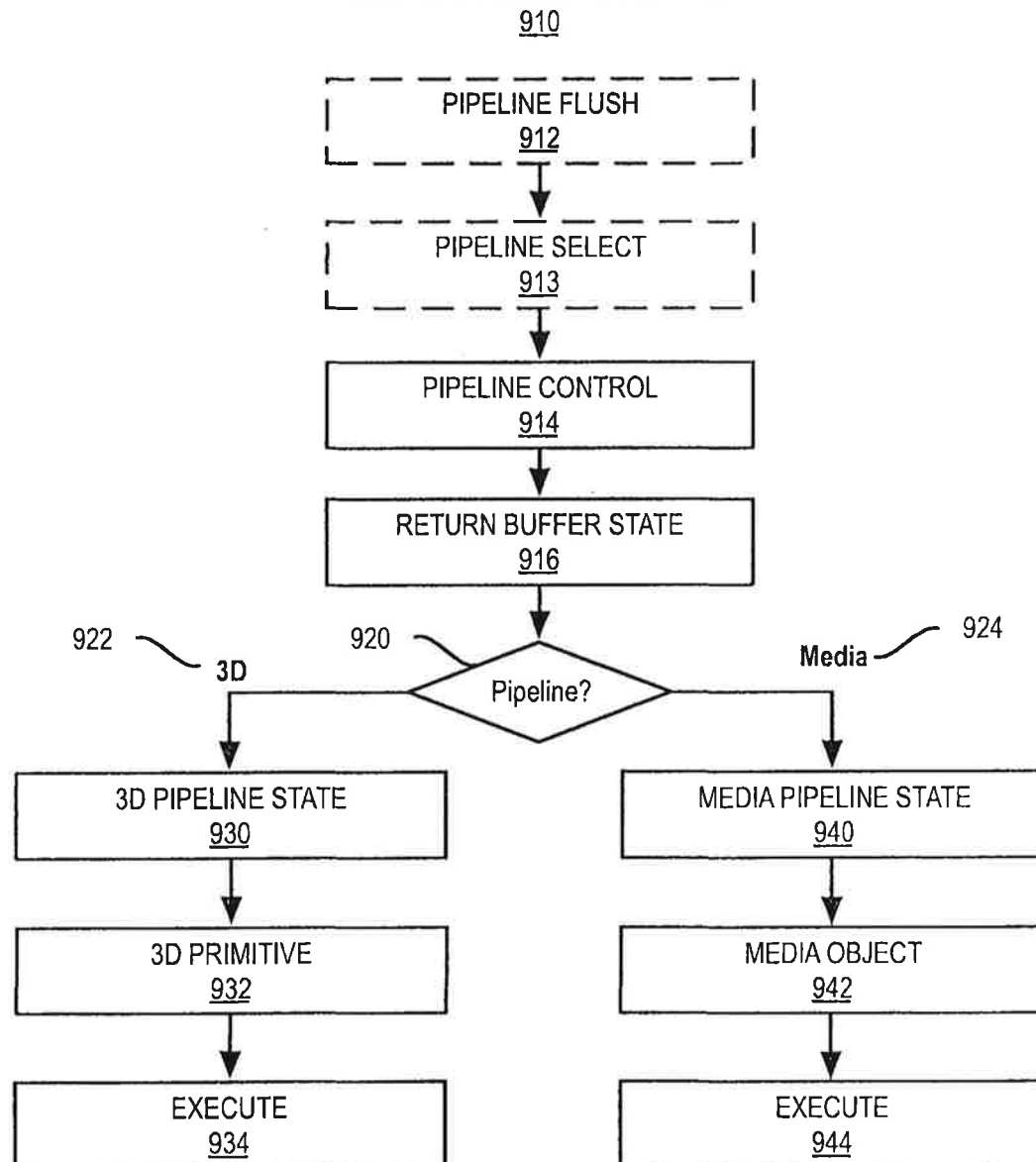


FIG. 9B

SAMPLE COMMAND SEQUENCE



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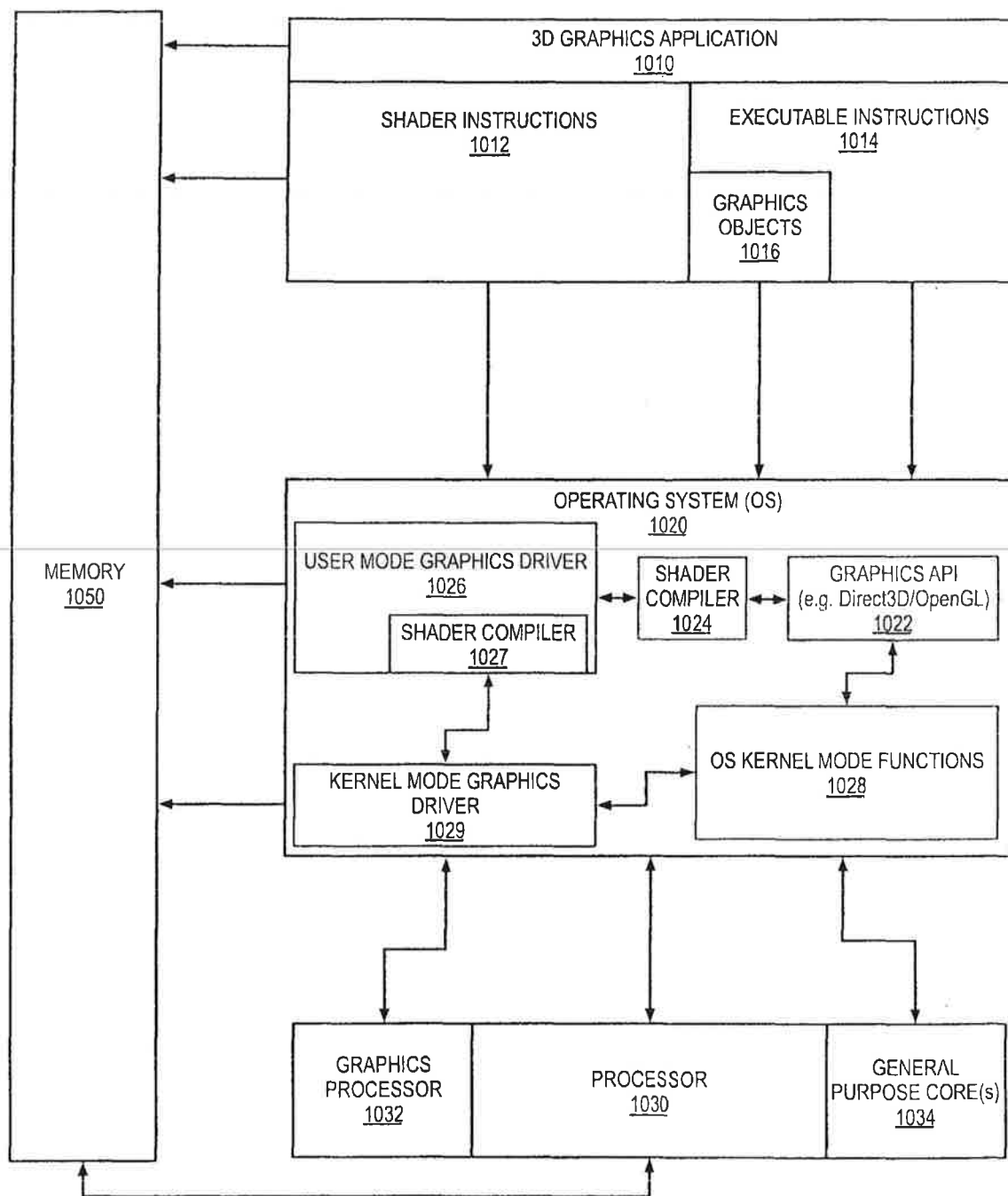


FIG. 10

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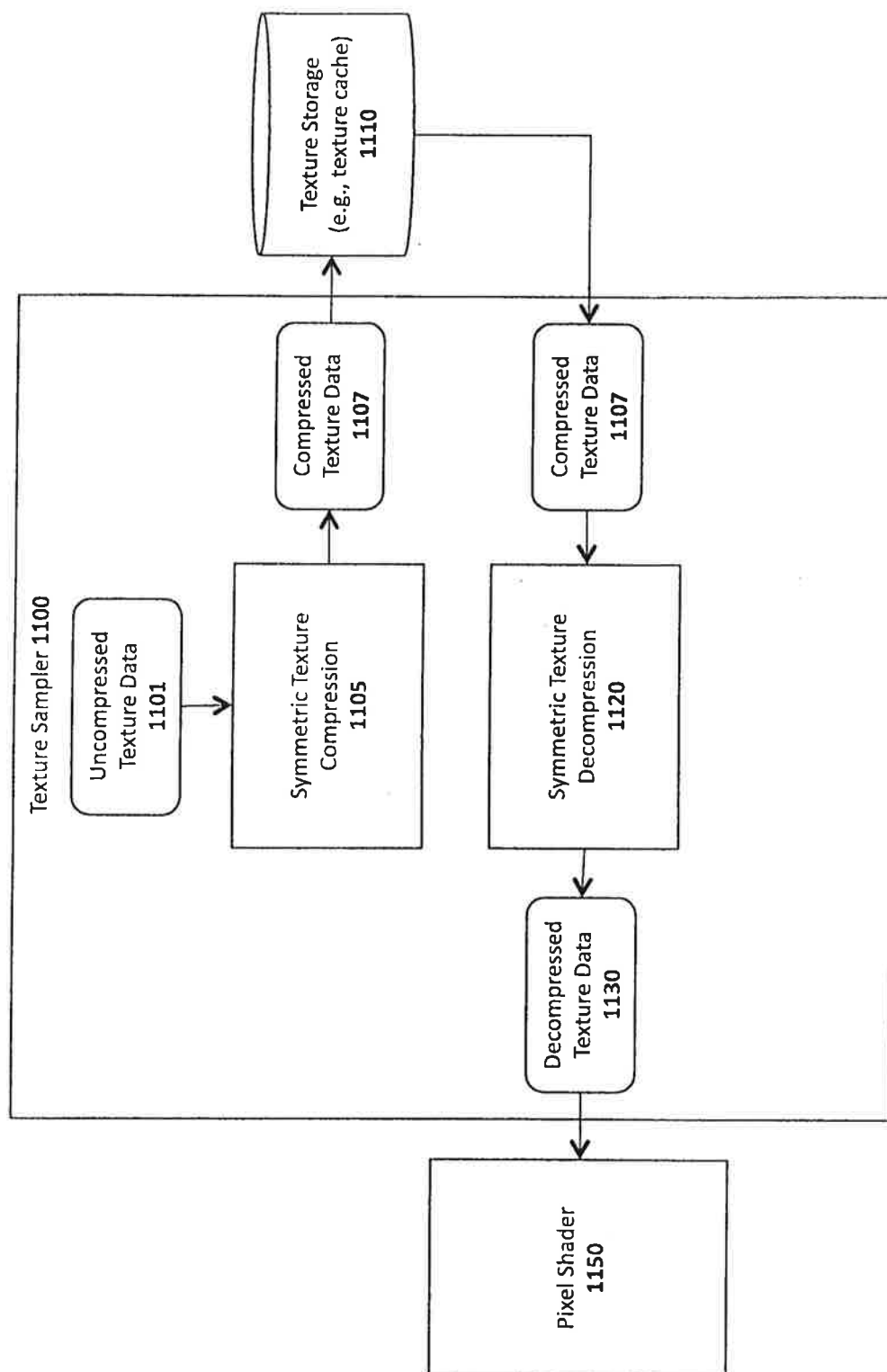
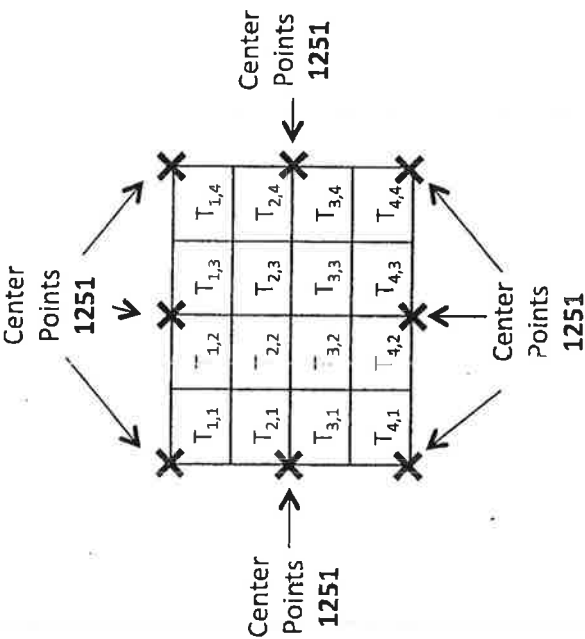
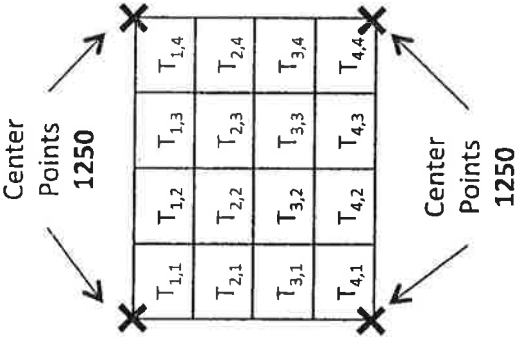


FIG. 11



Eight Center Points
Per Block

FIG. 12B



Four Center Points
Per Block

FIG. 12A

		Approximation Center Points per Block				
		4	8	12	16	20
Block Size	Texels in Block	Compression Rate				
4x4	16	0.25	0.50	0.75		
5x5	25	0.16	0.32	0.48	0.64	0.80
6x6	36	0.11	0.22	0.33	0.44	0.56
7x7	49	0.08	0.16	0.24	0.33	0.41
8x8	64	0.06	0.13	0.19	0.25	0.31
Multiply-add operations per texel		4	8	12	16	20

FIG. 12C

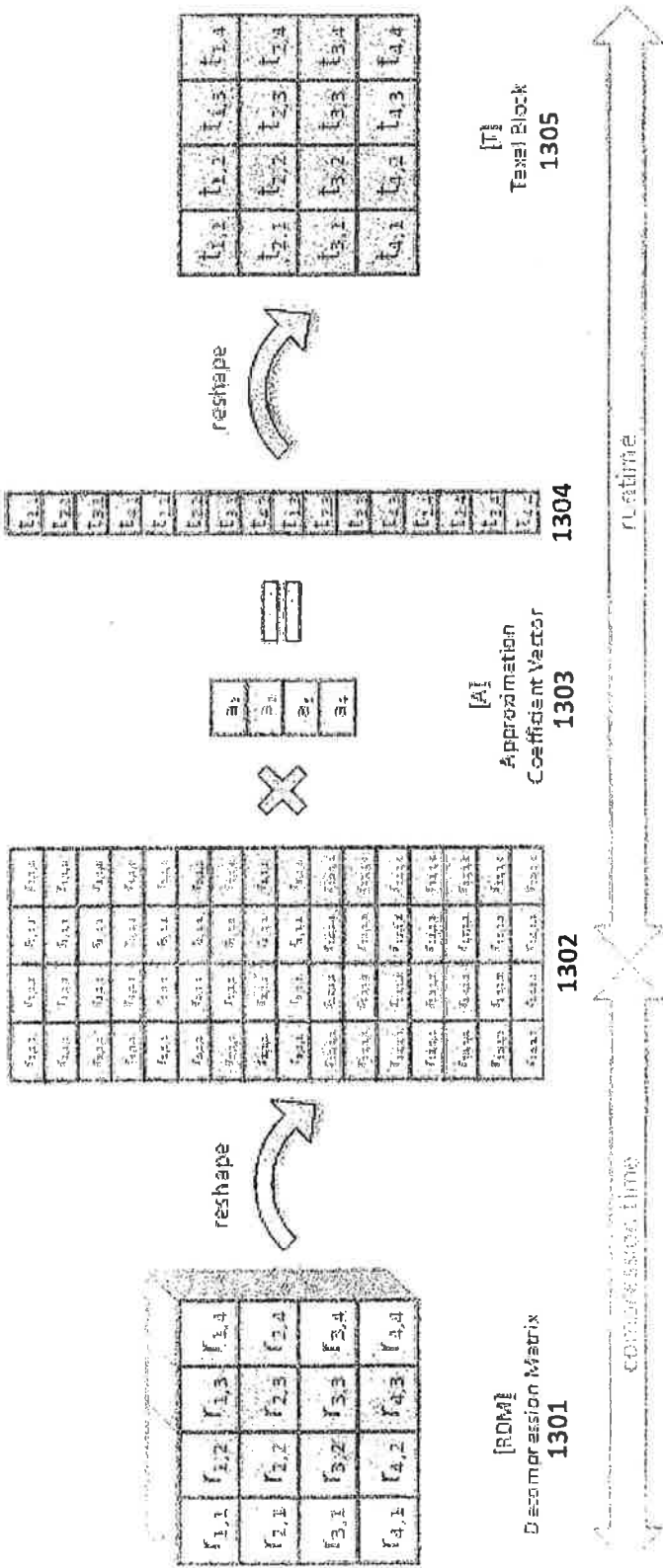


FIG. 13

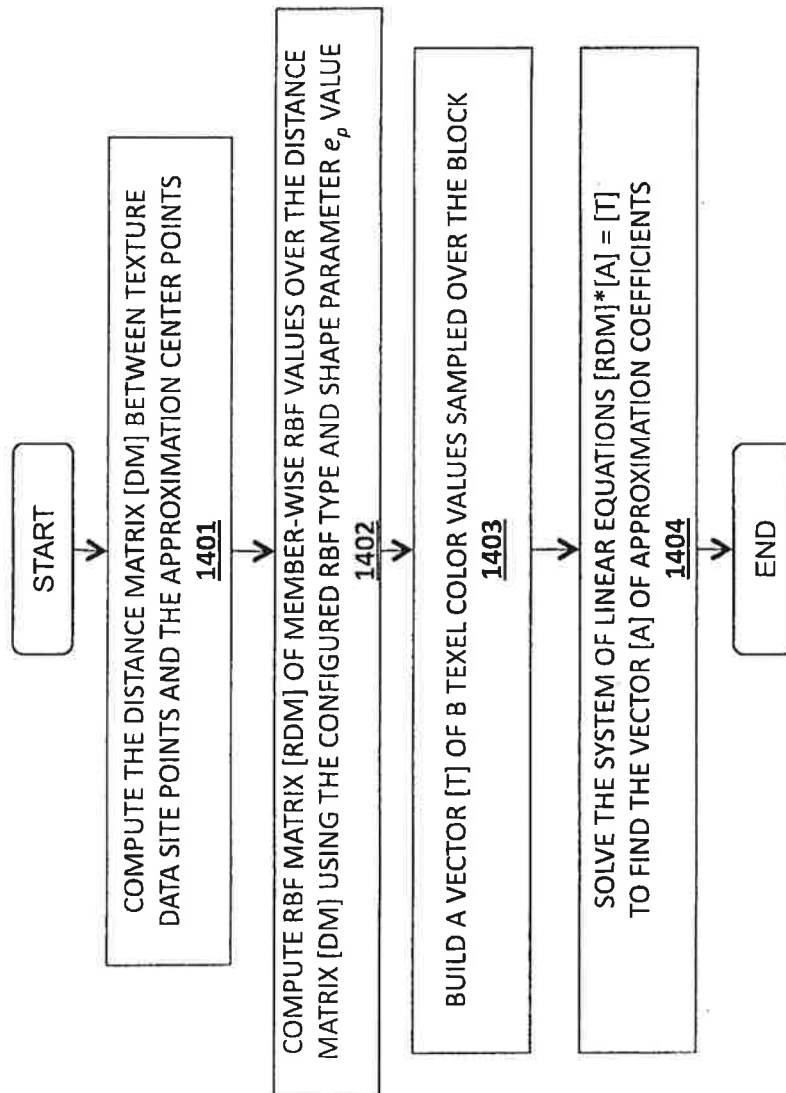


FIG. 14