A DATA BUS STRUCTURE FOR USE WITH MULTIPLE MEMORY STORAGE AND DRIVER RECEIVER TECHNOLOGIES AND A METHOD OF OPERATING SUCH STRUCTURES

Abstract

The disclosed invention relates generally to electronic data storage systems that access data storage memory modules (11) via a data bus (21) comprised of multiple data query lines and, more particularly, to an electronic data storage system provided with a data bus (21) that can be selectively provided with terminations thereby permitting the data storage memory (10) to use either modules (11) that require the data query lines be open-ended, i.e., without terminations or modules that require the data bus (21) be terminated and to a method for operating such a system. The present invention is particularly directed to a single memory system (10) that can accommodate either 3.3V DIMMs (11) or DDR DIMMs.

Fig 1

This Sheet Has Been Replaced

