

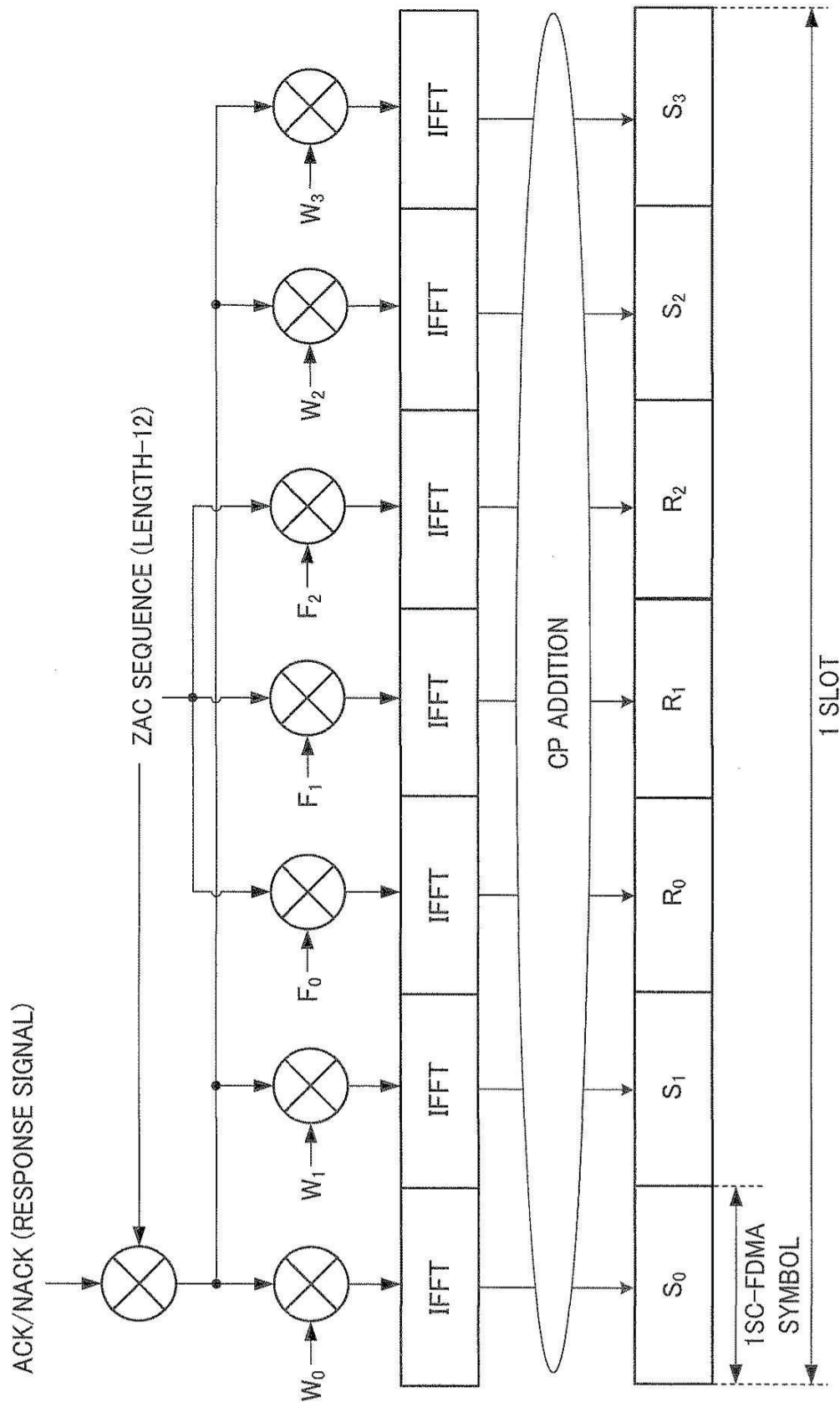


FIG. 1

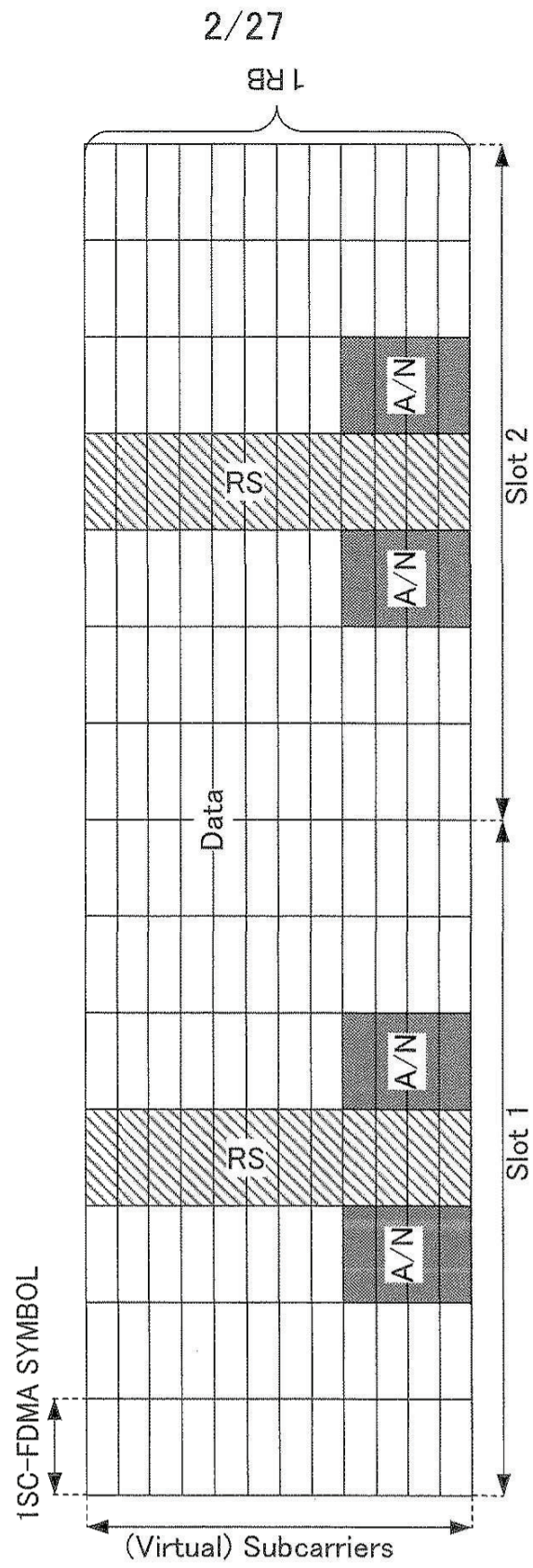


FIG. 2

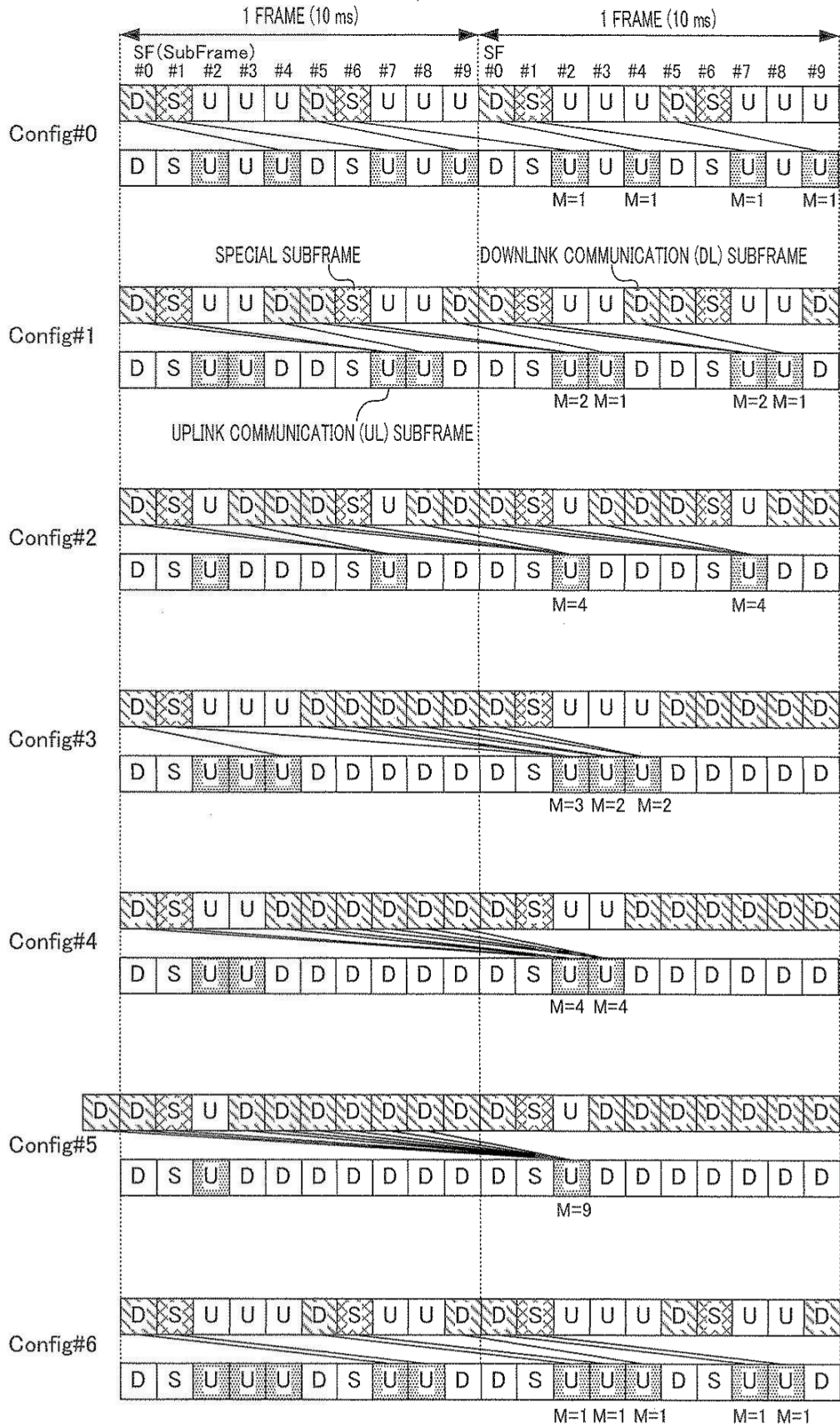


FIG. 3

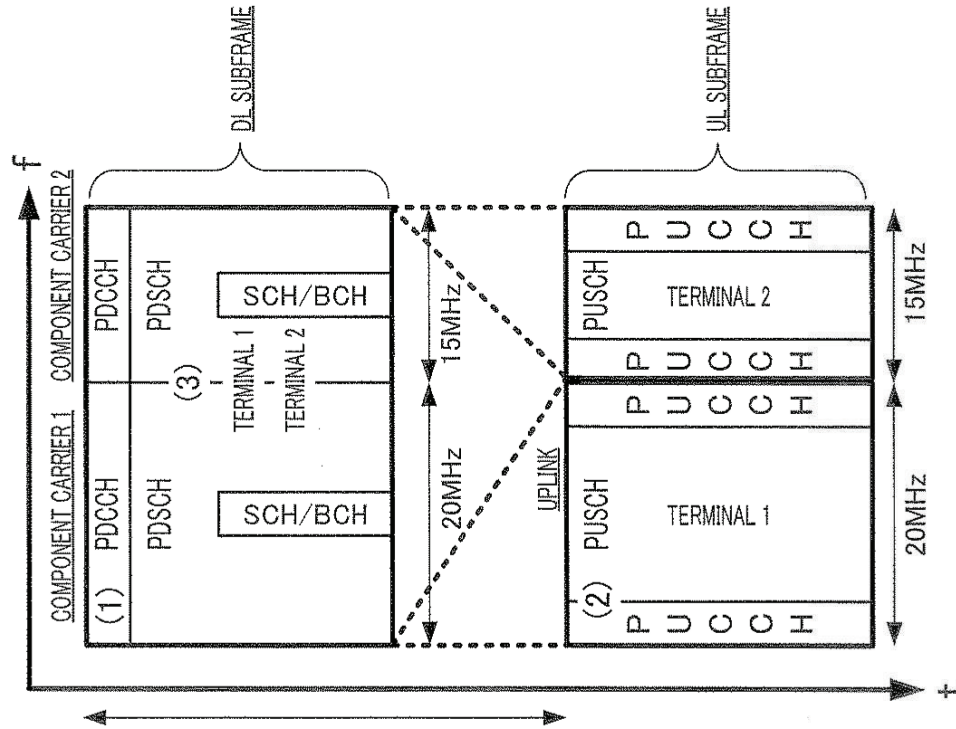


FIG. 4B

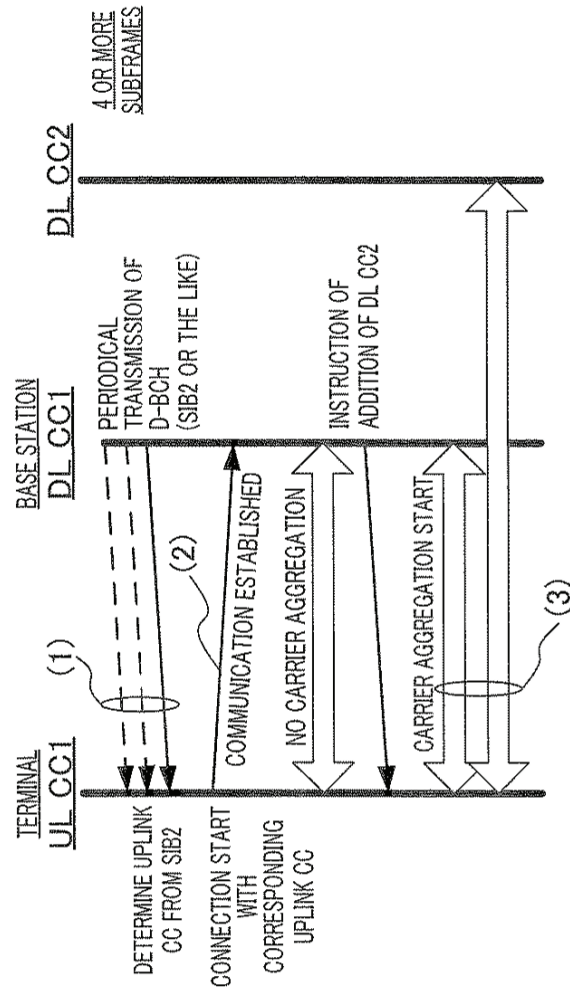


FIG. 4A

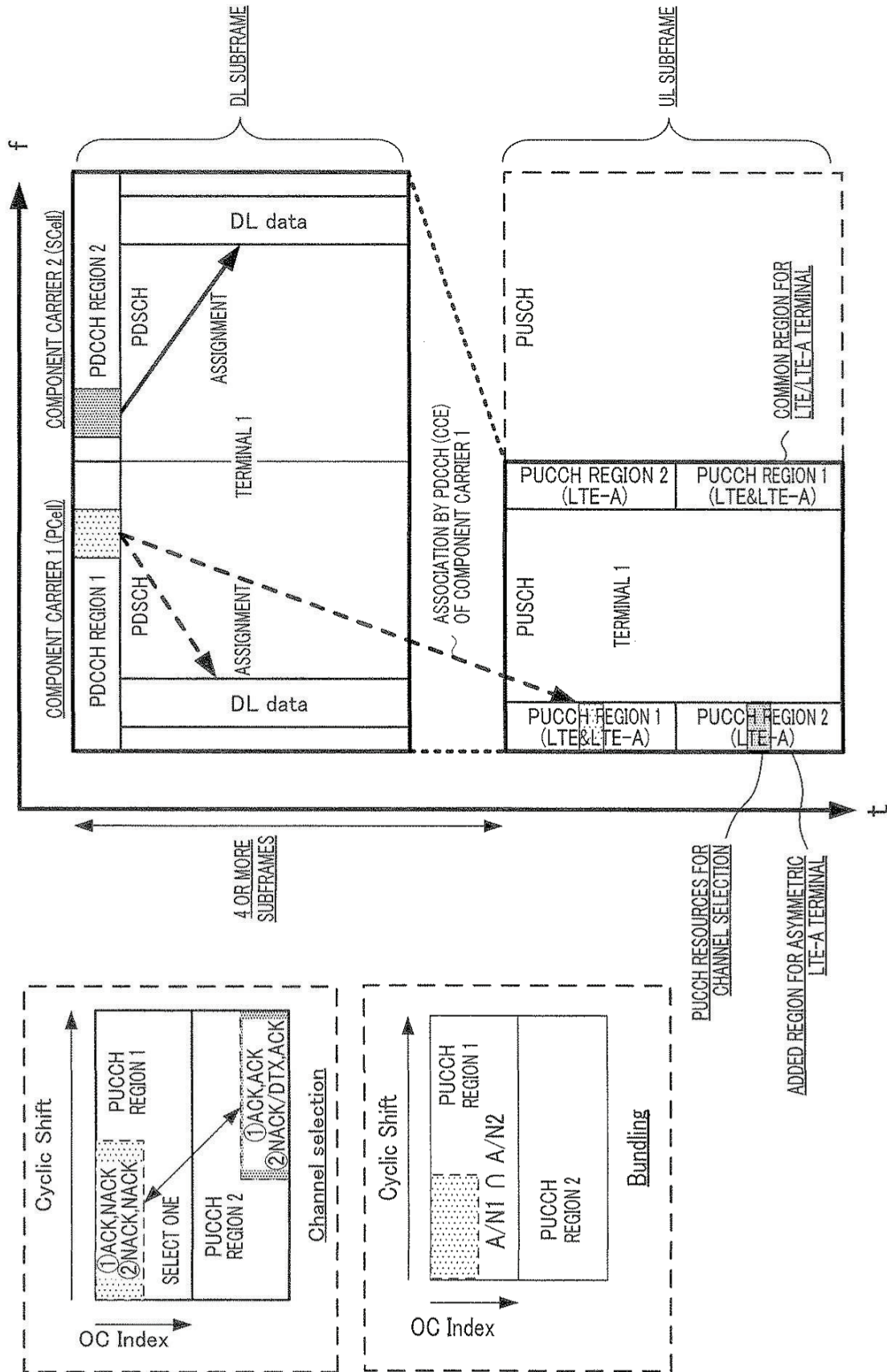


FIG. 5

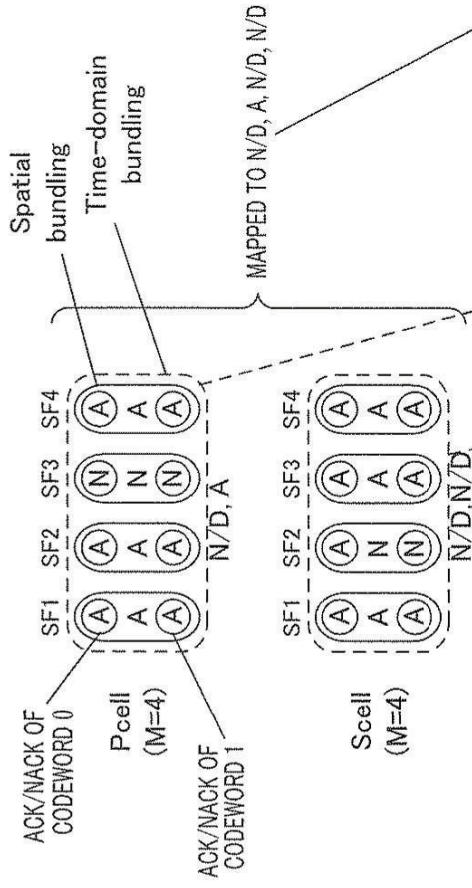


FIG. 6B

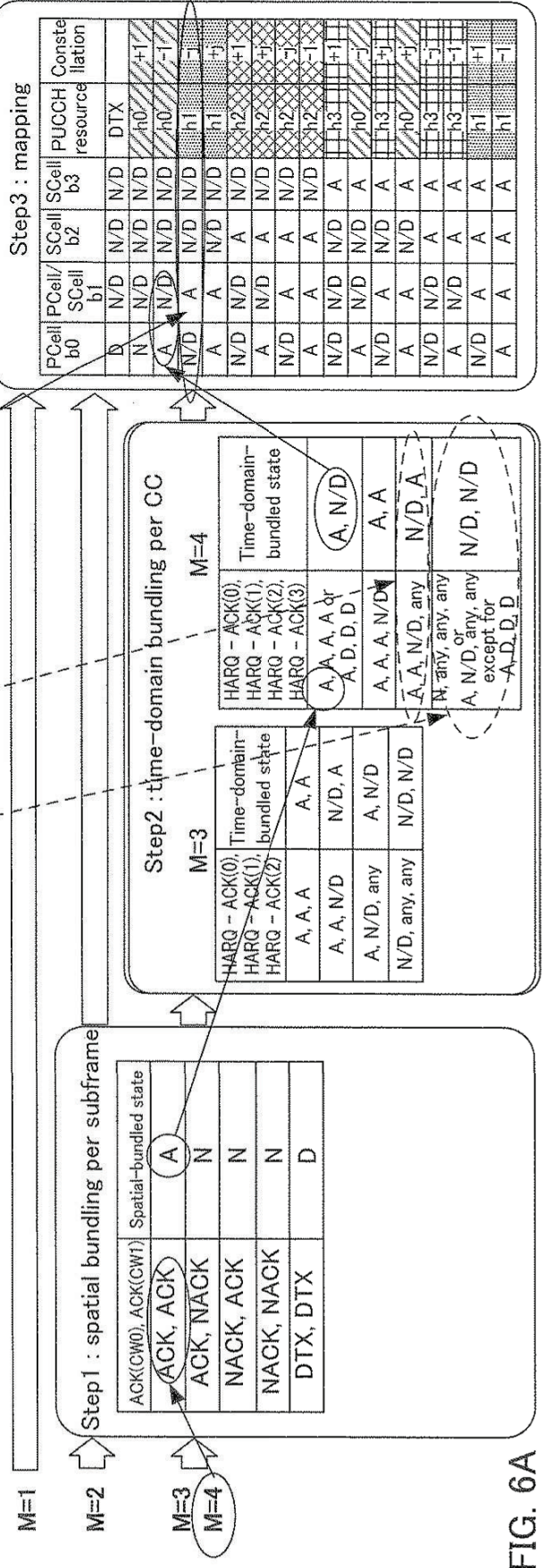


FIG. 6A

UL-DL Config	MAXIMUM NUMBER OF DL HARQ PROCESSES M_{DL_HARQ}
0	4
1	7
2	10
3	9
4	12
5	15
6	6

FIG. 7B

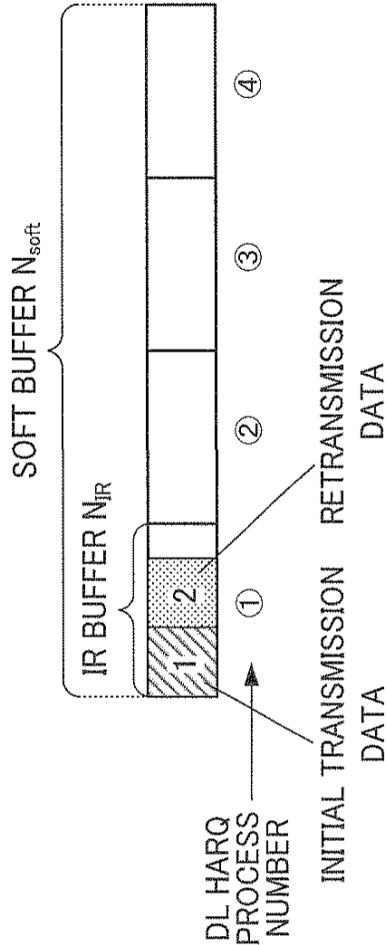


FIG. 7A

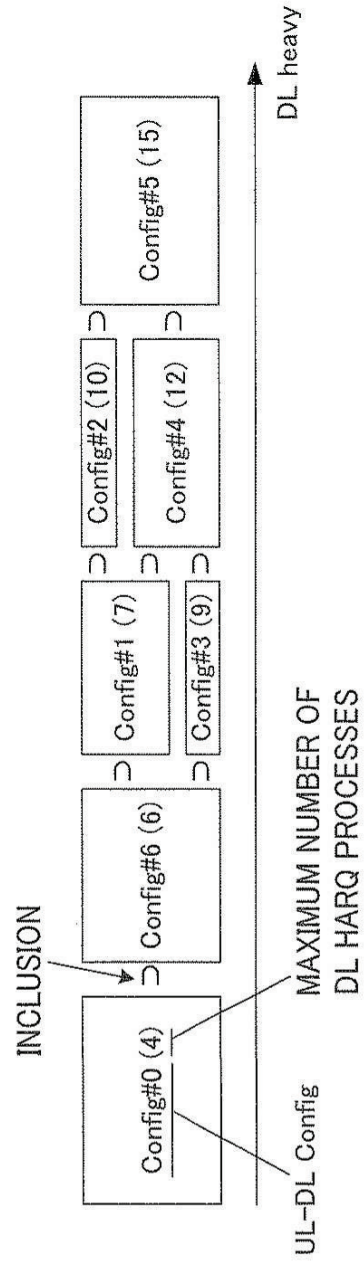


FIG. 8

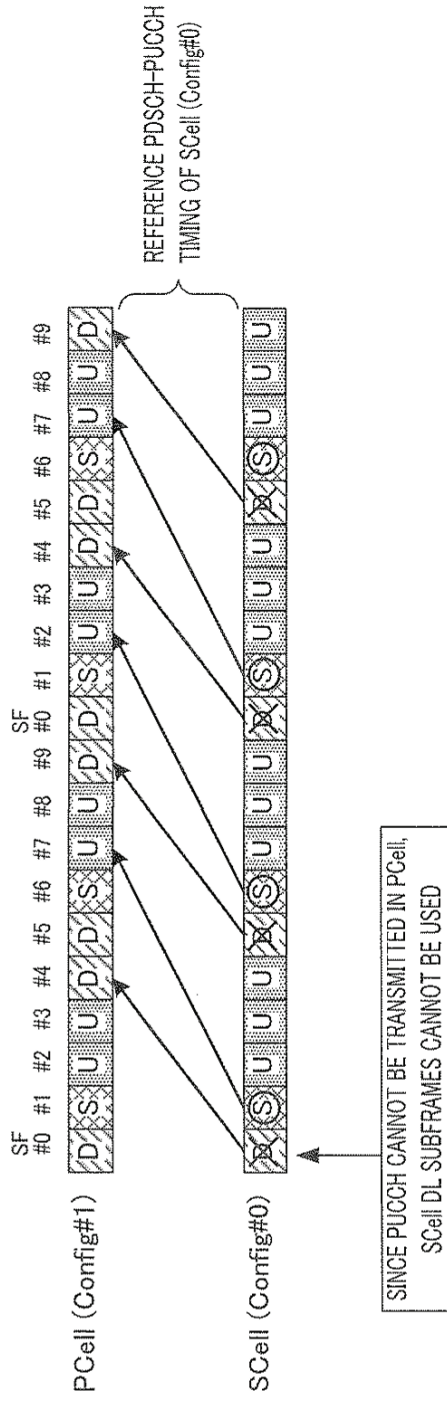


FIG. 9A

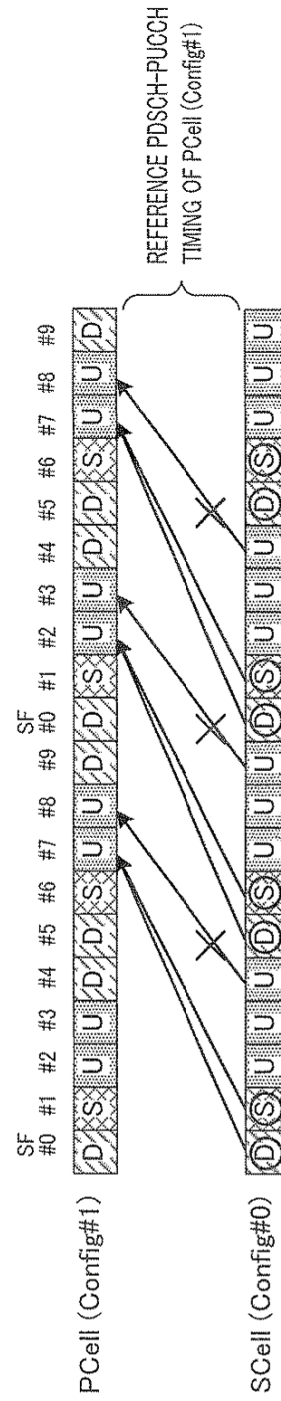


FIG. 9B

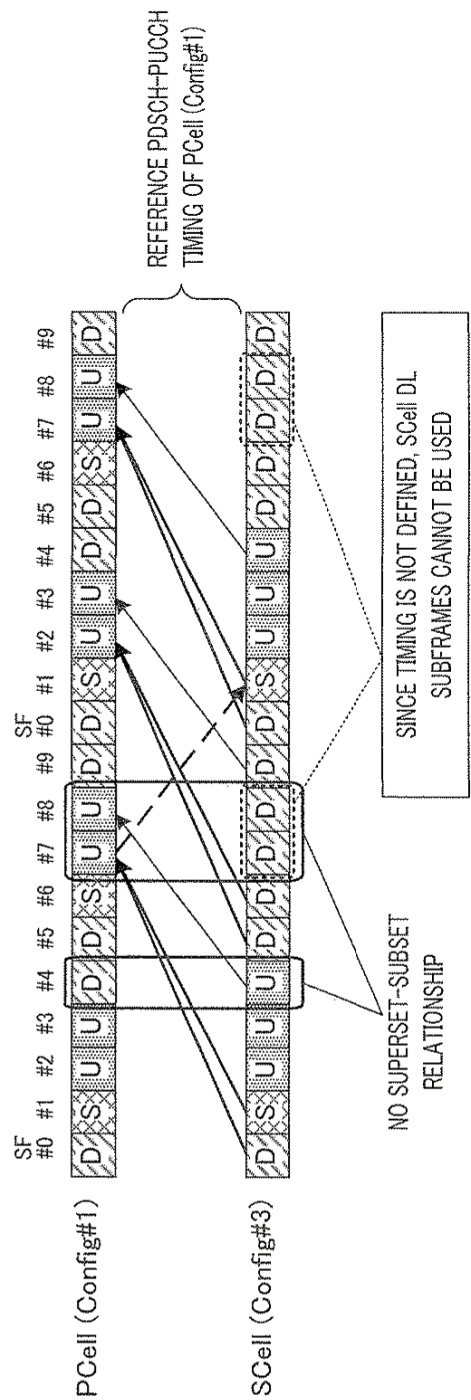


FIG. 10A

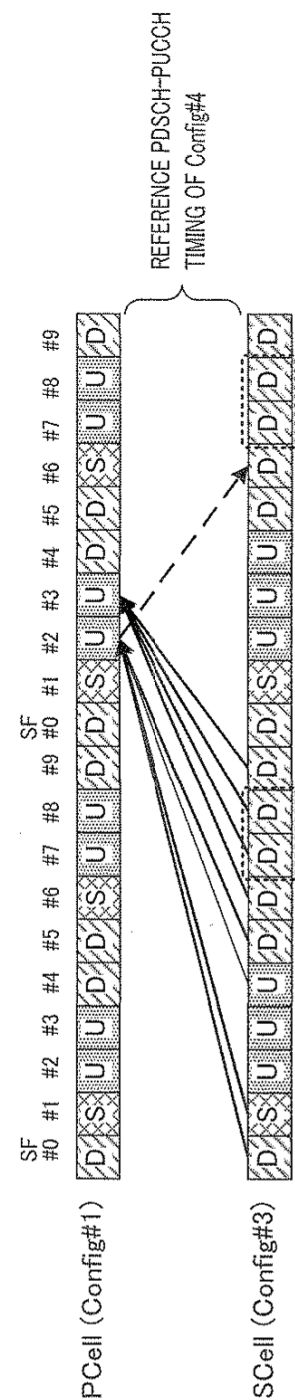
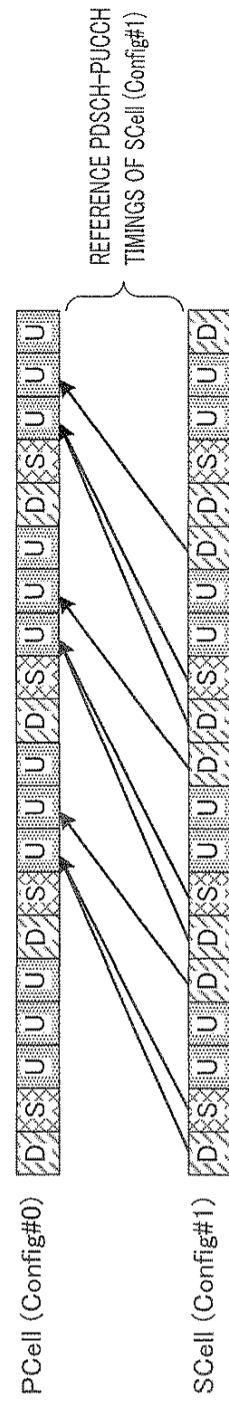


FIG. 10B



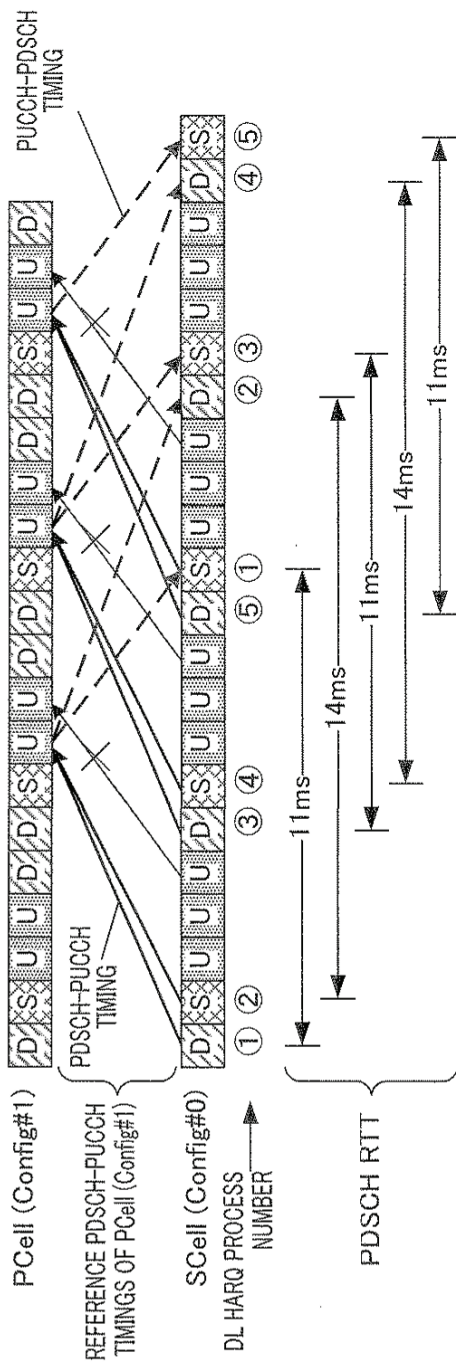


FIG. 12A

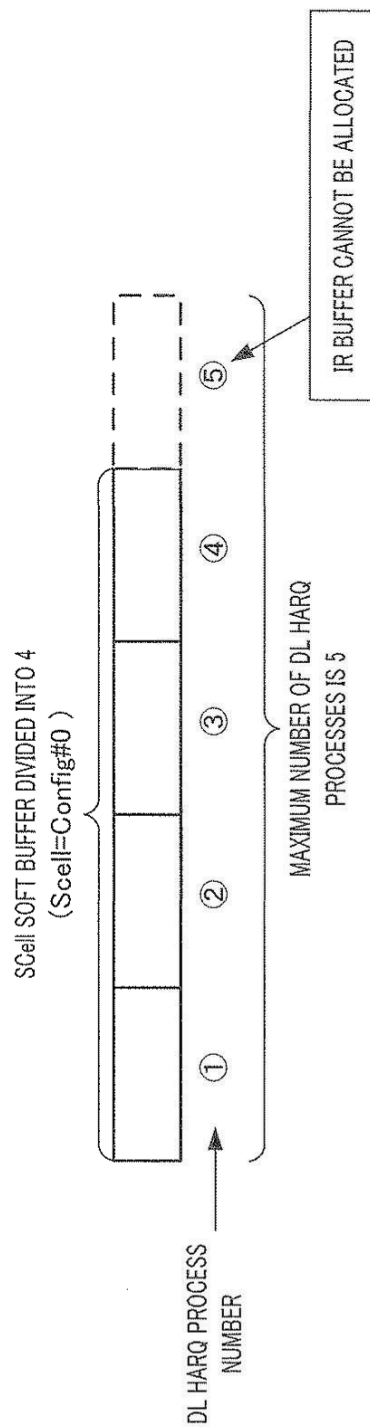


FIG. 12B

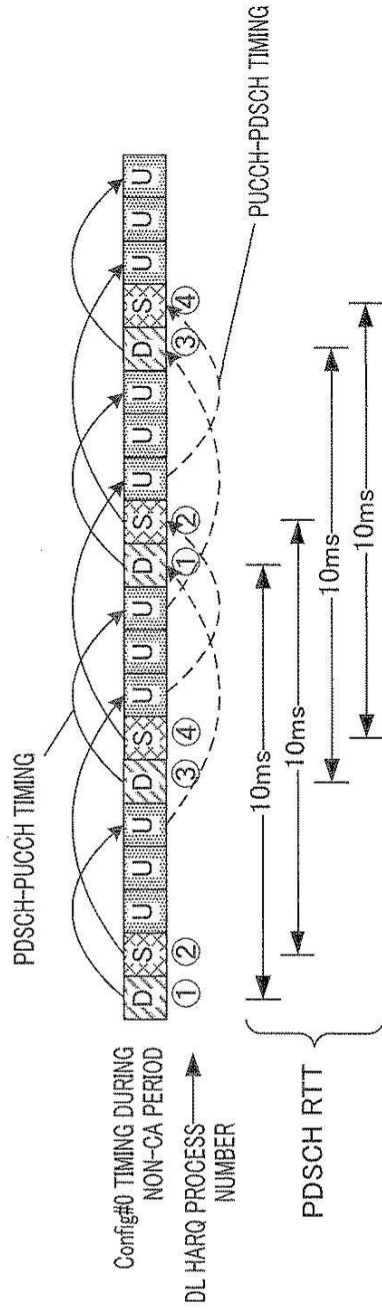


FIG. 13A

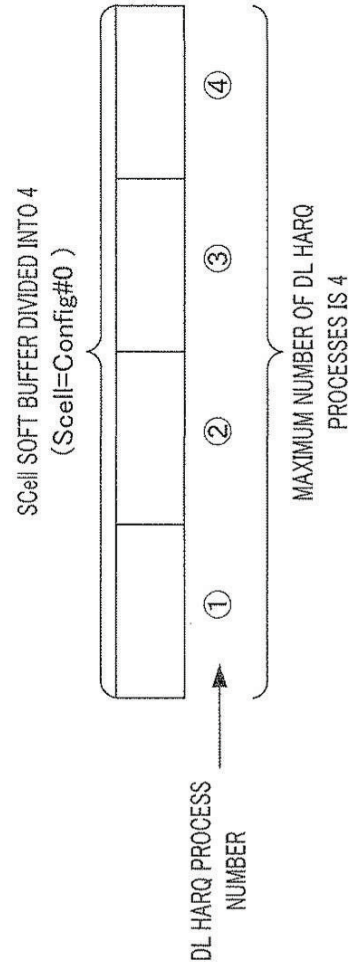


FIG. 13B

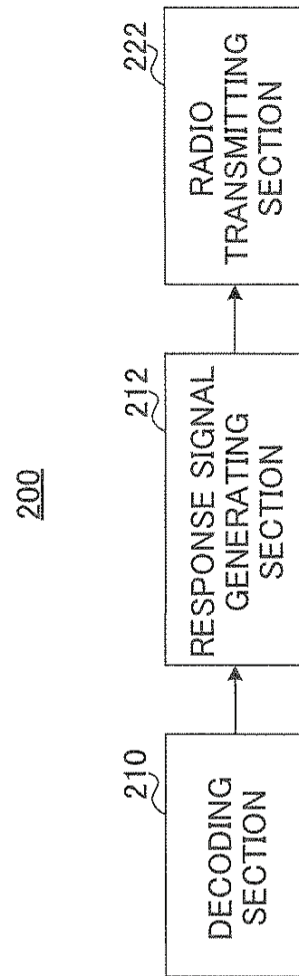


FIG. 14

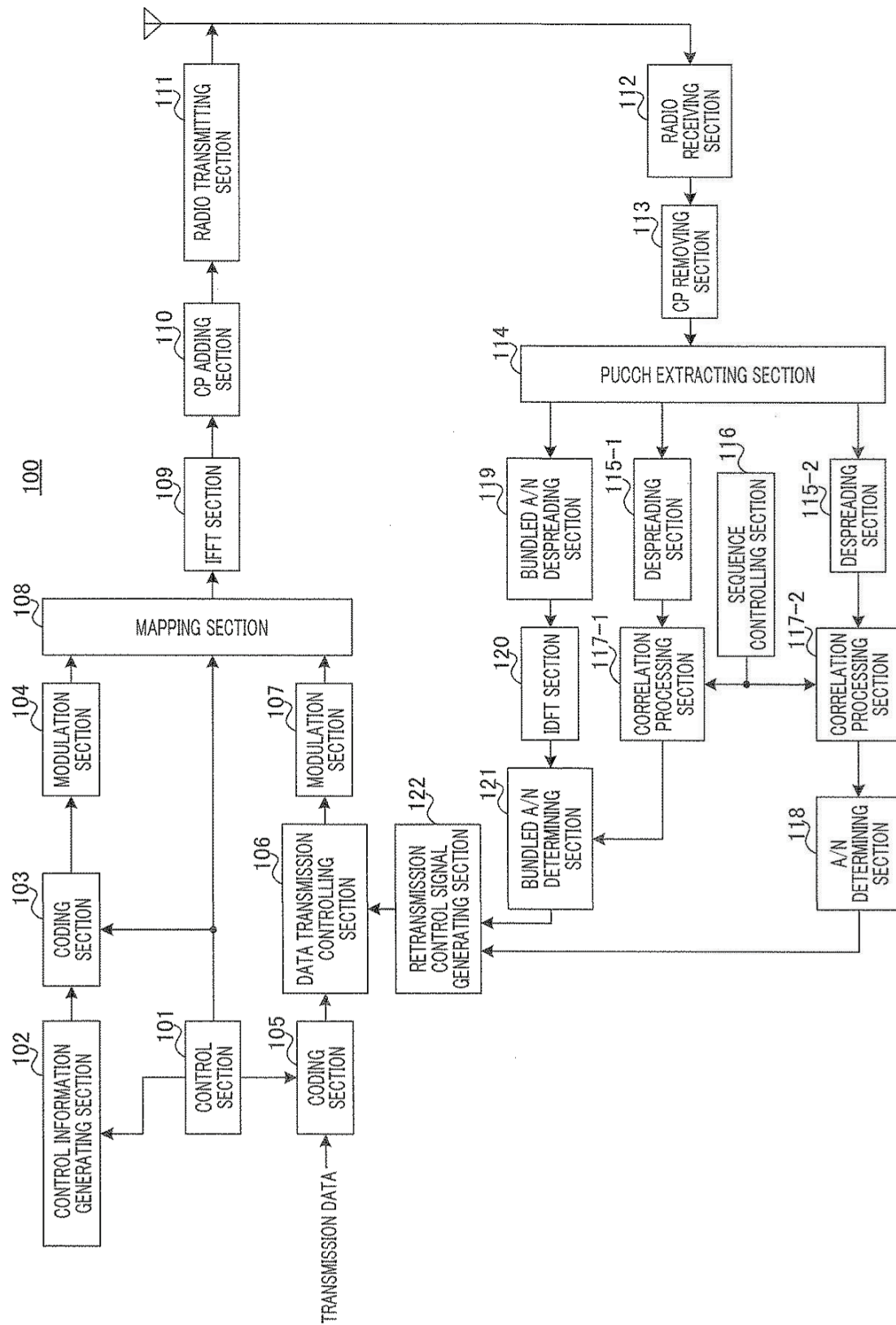


FIG. 15

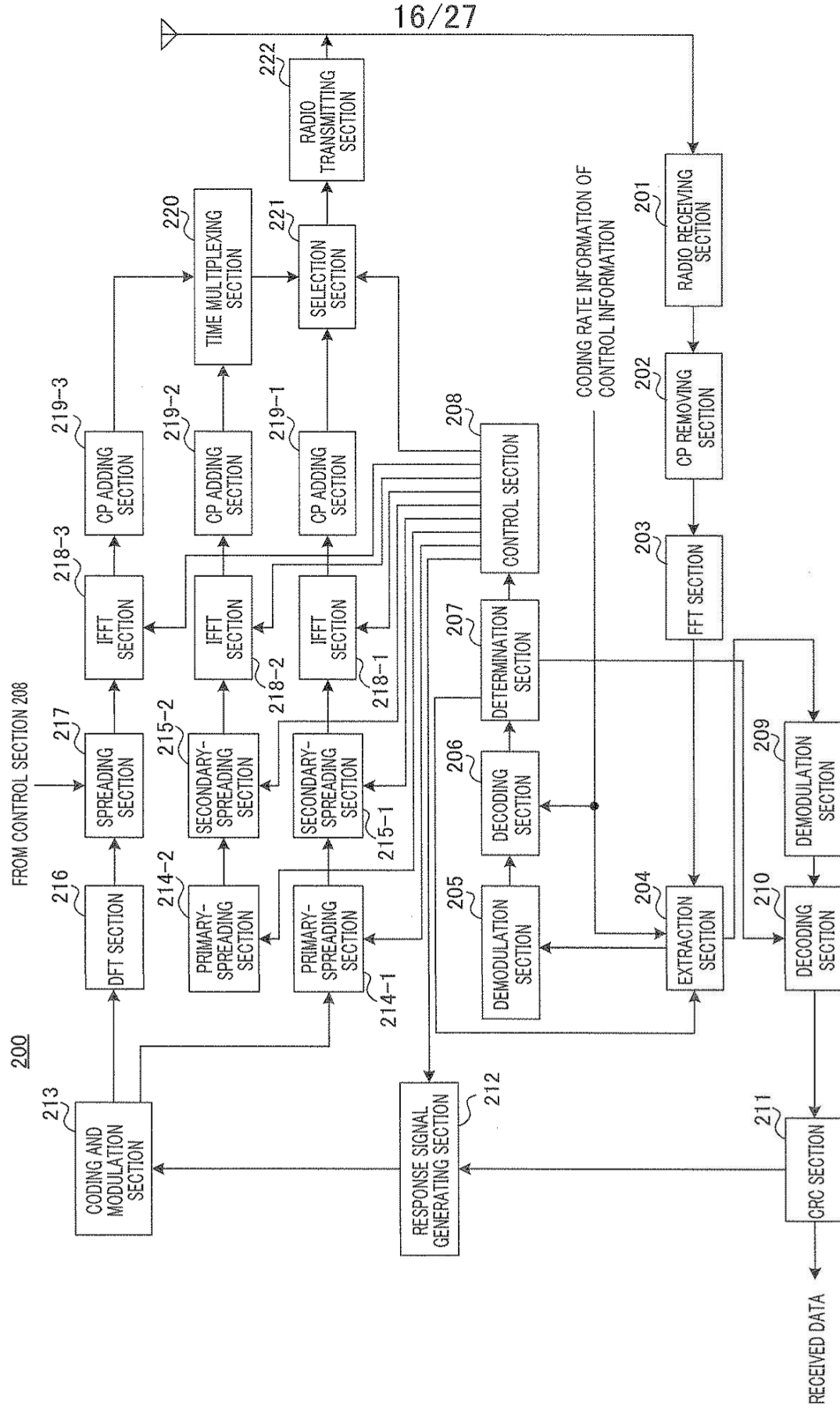


FIG. 16

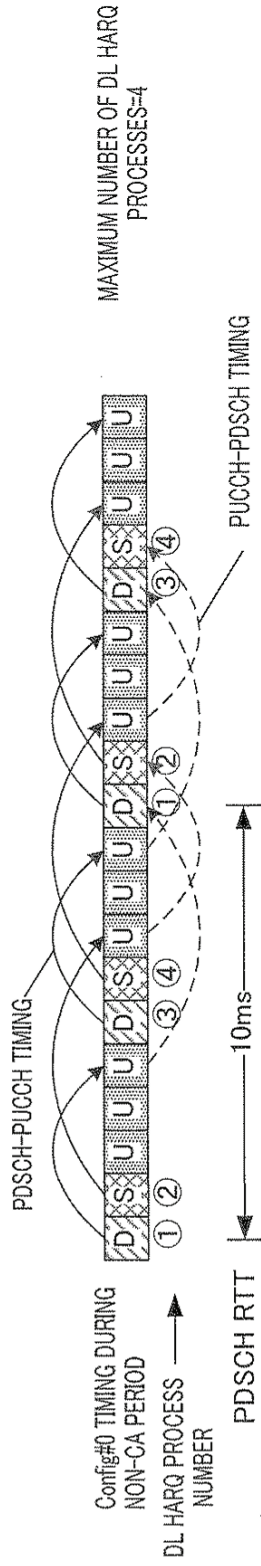


FIG. 17A

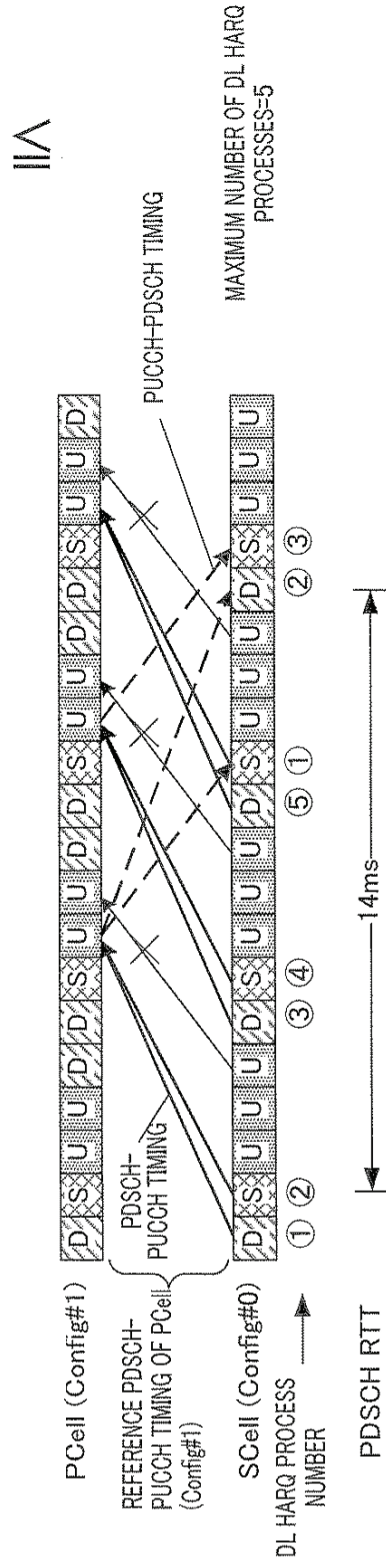


FIG. 17B

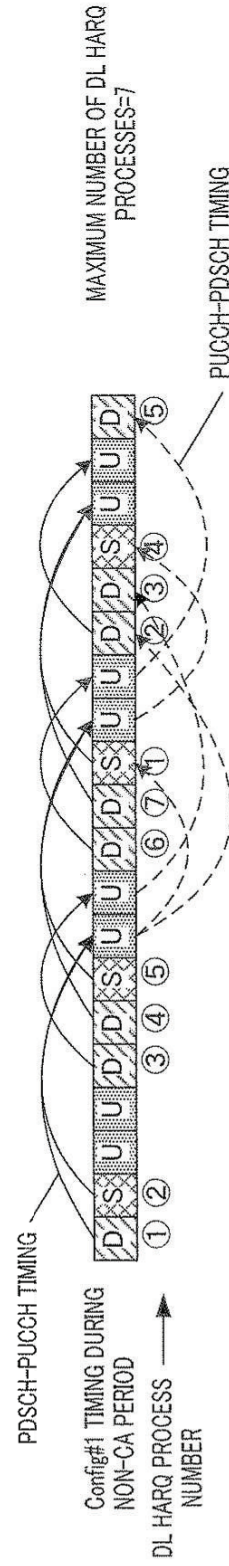


FIG. 17C

UL-DL Config	PDSCH RTT [msec]
0	10
1	11
2	12
3	15
4	16
5	17
6	14

FIG. 18

MAXIMUM NUMBER OF DL HARQ PROCESSES REFERENCED BY SCell

		UL-DL Config set in PCell						
		0	1	2	3	4	5	6
UL-DL Config SET IN SCell	0		4+1	4+2	4+2	4+3	4+3	4+1
	1	7		7+1	7+2	7+3	7+4	7
	2	10	10		10+4	10+4	10+4	10
	3	9	9+1	9+1		9+1	9+2	9
	4	12	12	12+1	12		12+1	12
	5	15	15	15	15	15		15
	6	6	6+0	6+1	6+1	6+2	6+3	

FIG. 19A

UL-DL CONFIGURATION REFERENCED BY SCell TIMING

		UL-DL Config set in PCell						
		0	1	2	3	4	5	6
UL-DL Config SET IN SCell	0		1	2	3	4	5	6
	1	1		2	3	4	5	6
	2	2	2		4	4	5	1
	3	3	4	5	5	5	5	2
	4	4	4	5	4	4	5	3
	5	5	5	5	5	5	5	4
	6	6	1	2	3	4	5	5

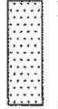
-  WHEN PCell IS DL HEAVY
-  WHEN PCell IS NEITHER DL HEAVY NOR UL HEAVY
-  WHEN PCell IS UL HEAVY

FIG. 19B

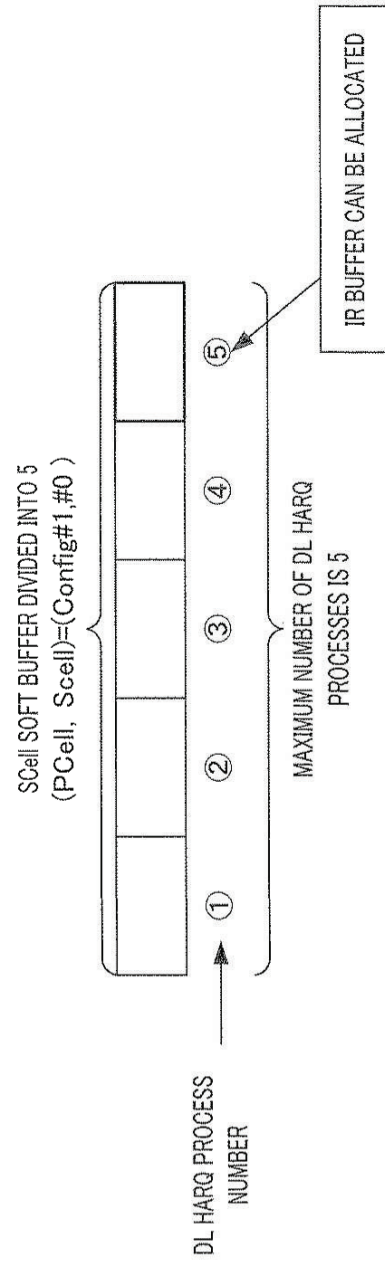


FIG. 20

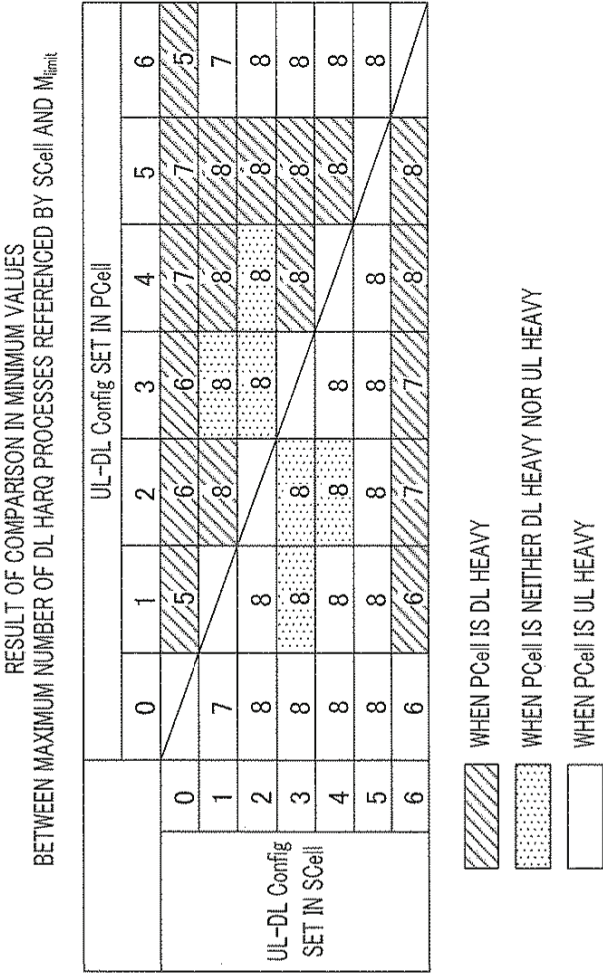


FIG. 21

UL-DL CONFIGURATION REFERENCED BY SCell TIMING	
UL-DL Config	UL-DL Config SET IN PCell
0	0
1	1
2	2
3	3
4	4
5	5
6	6

UL-DL Config	MAXIMUM NUMBER OF DL HARQ PROCESSES REFERENCED BY SCell $M_{REF_DL_HARQ,SCell}$
0	4
1	7
2	10
3	9
4	12
5	15
6	6

FIG. 22B

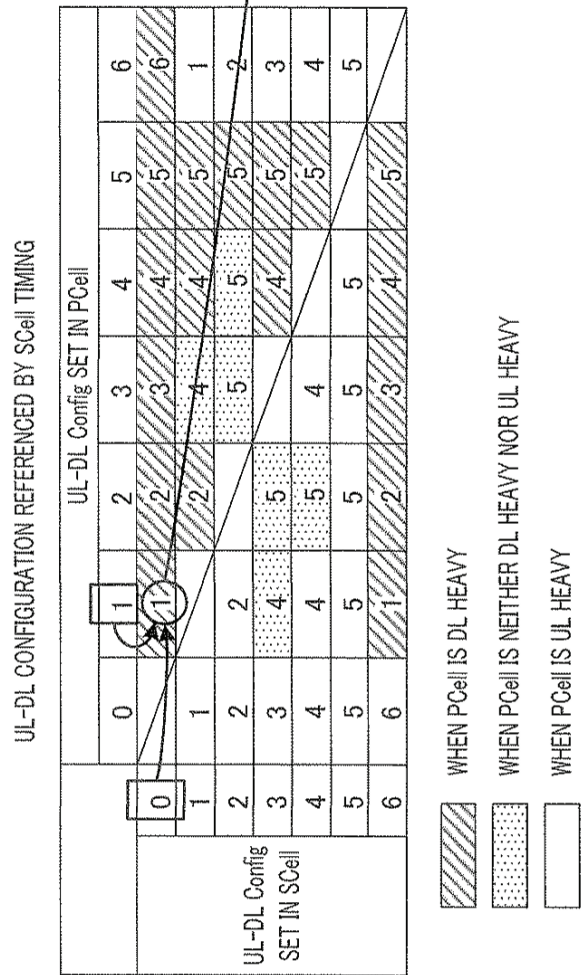


FIG. 22A

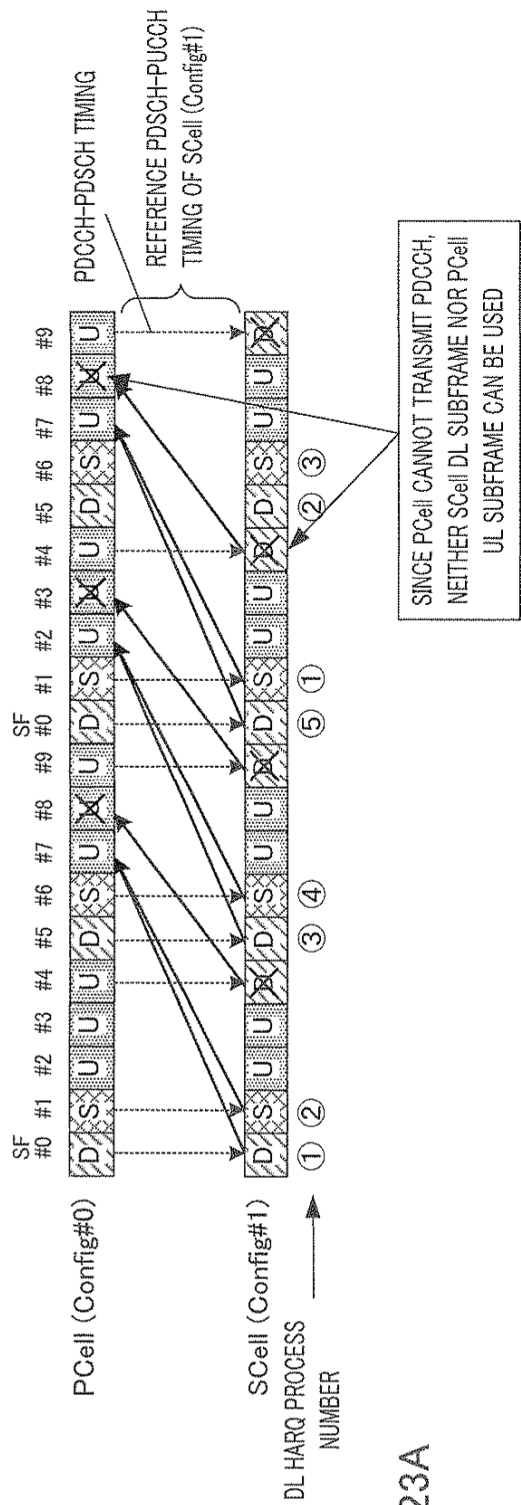


FIG. 23A

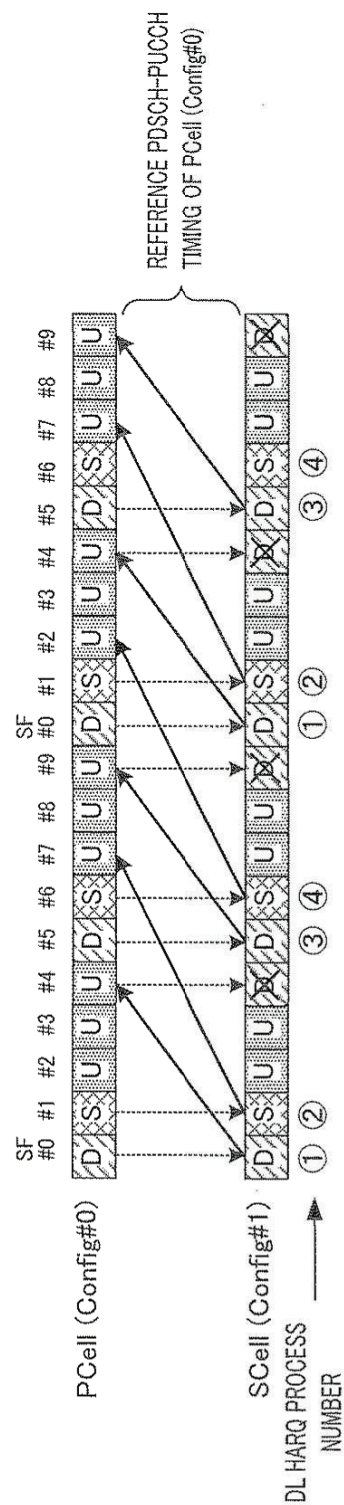


FIG. 23B

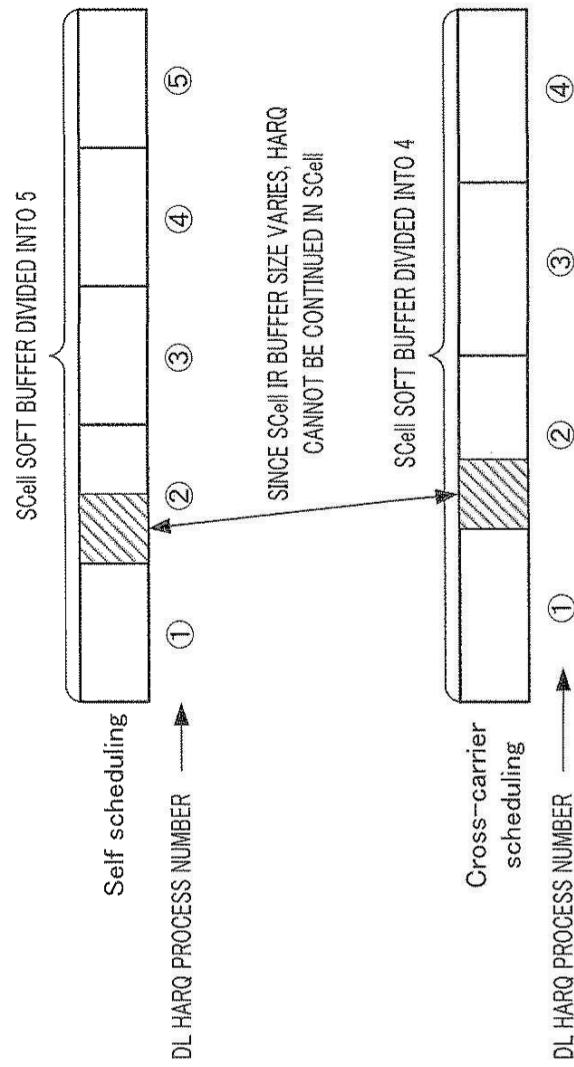


FIG. 24

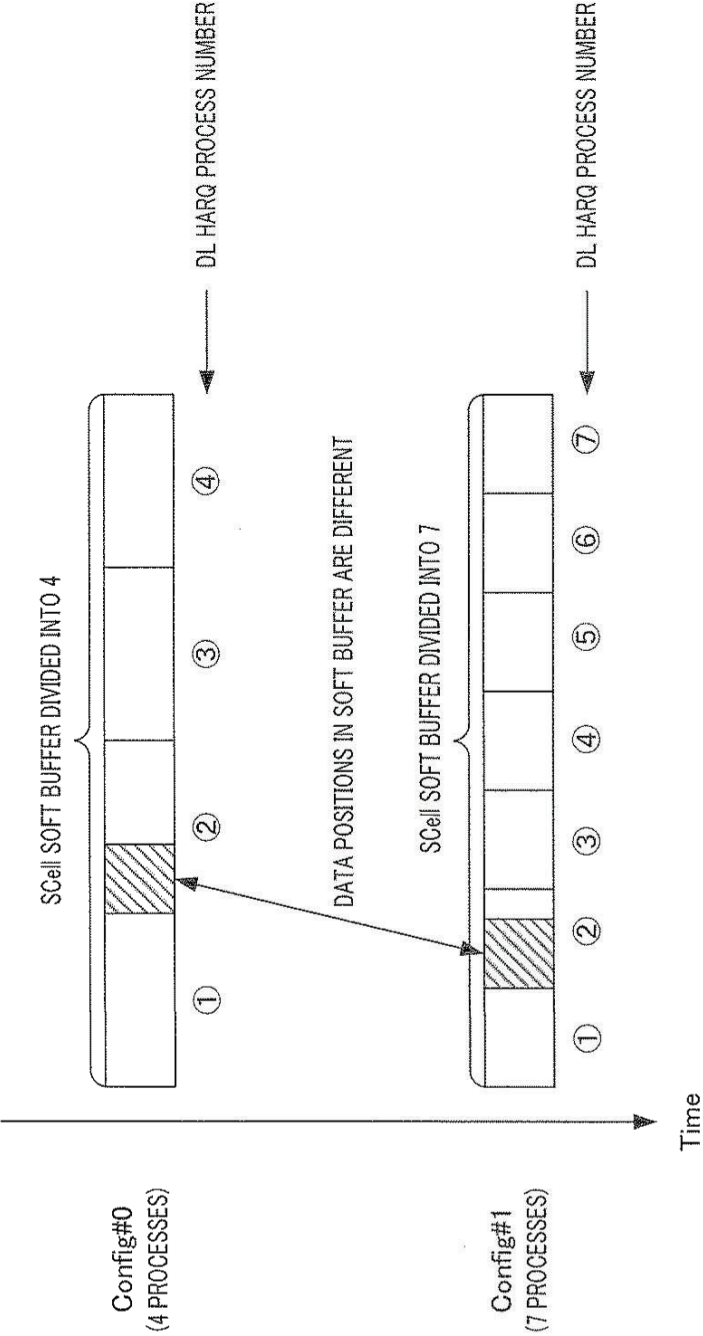


FIG. 26

MAXIMUM NUMBER OF DL HARQ PROCESSES REFERENCED BY SCell

		UL-DL Config SET IN PCell						
		0	1	2	3	4	5	6
UL-DL Config SET IN SCell	0	4	4+1	4+2	4+2	4+3	4+3	4+1
	1	7	7	7+1	7+2	7+3	7+4	7
	2	10	10	10	10+4	10+4	10+4	10
	3	9	9+1	9+1	9	9+1	9+2	9
	4	12	12	12+1	12	12	12+1	12
	5	15	15	15	15	15	15	15
	6	6	6+0	6+1	6+1	6+2	6+3	6

FIG. 27A

UL-DL CONFIGURATION REFERENCED BY SCell TIMING

		UL-DL Config SET IN PCell						
		0	1	2	3	4	5	6
UL-DL Config SET IN SCell	0	0	1	2	3	4	5	6
	1	1	1	2	3	4	5	6
	2	2	2	2	4	4	5	1
	3	3	4	5	5	5	5	2
	4	4	4	5	3	4	5	3
	5	5	5	5	4	4	5	4
	6	6	1	2	3	4	5	6

FIG. 27B

- WHEN PCell IS DL HEAVY
- WHEN PCell IS NEITHER DL HEAVY NOR UL HEAVY
- WHEN PCell IS UL HEAVY
- WHEN PCell AND SCell HAVE THE SAME UL-DL CONFIGURATION