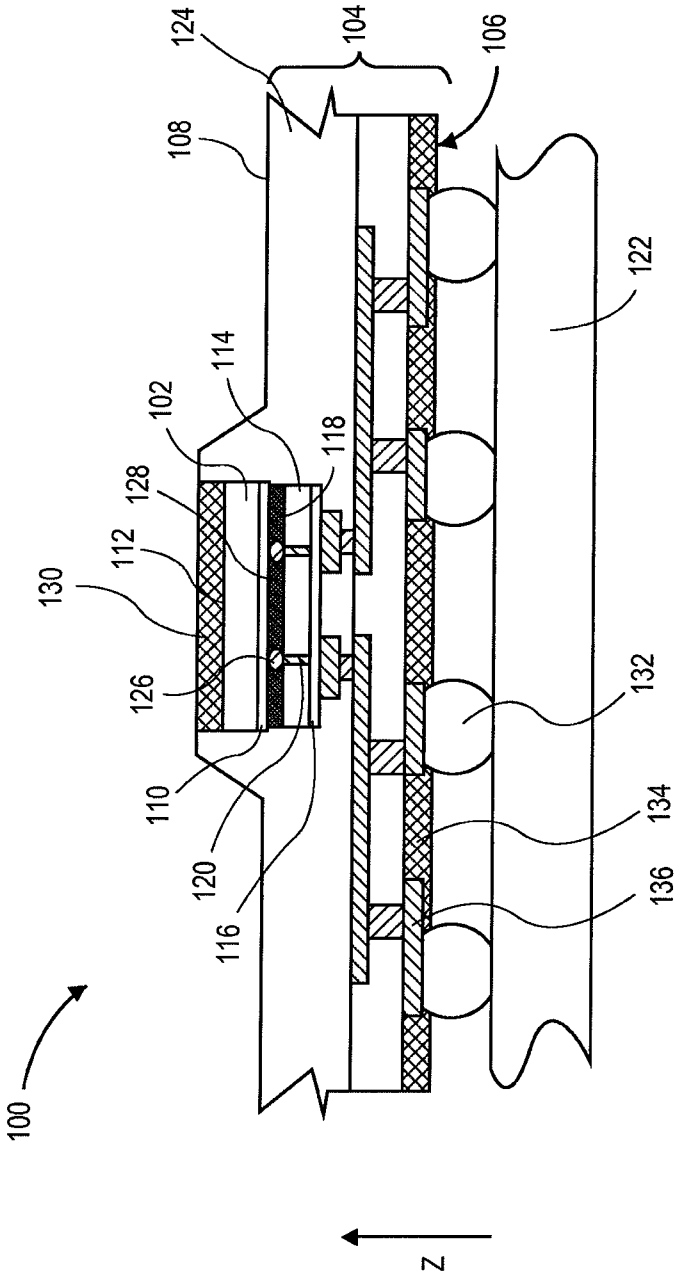

124
104
106
108
122
114
102
118
128
112
130
126
132
110
134
120
116
136
100
Z

FIG. 1

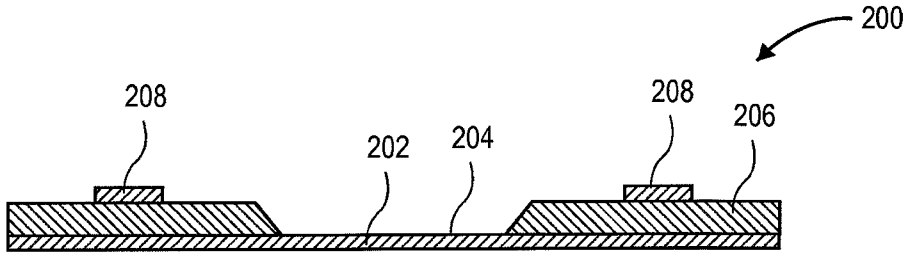


FIG. 2A

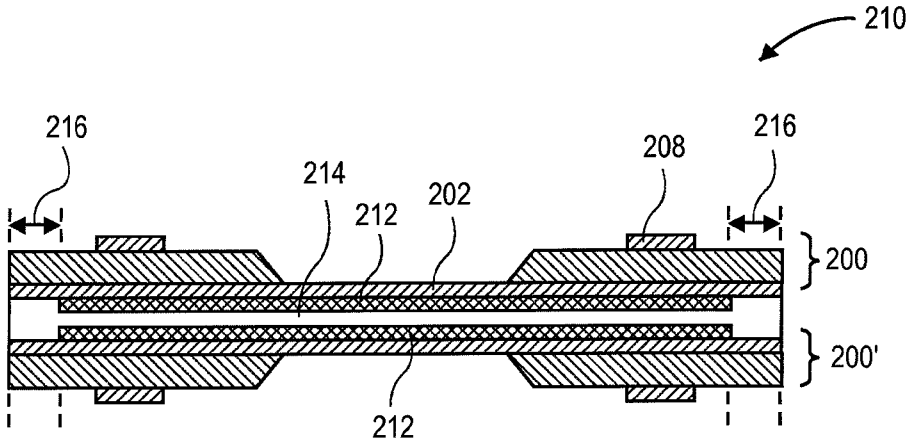


FIG. 2B

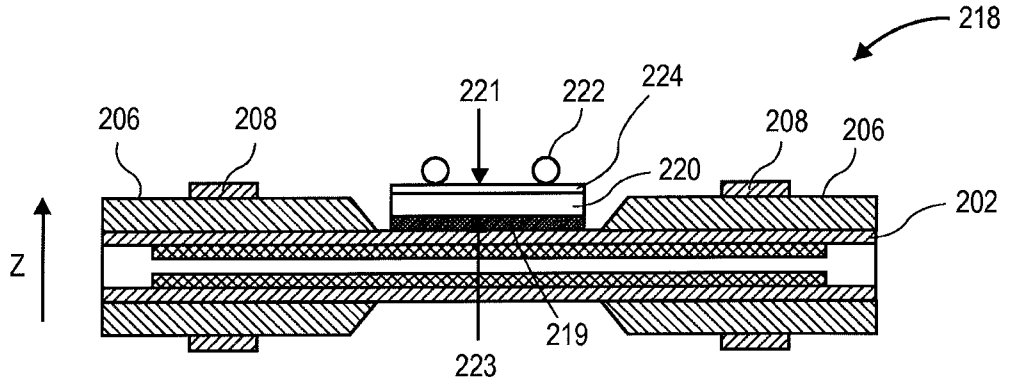


FIG. 2C

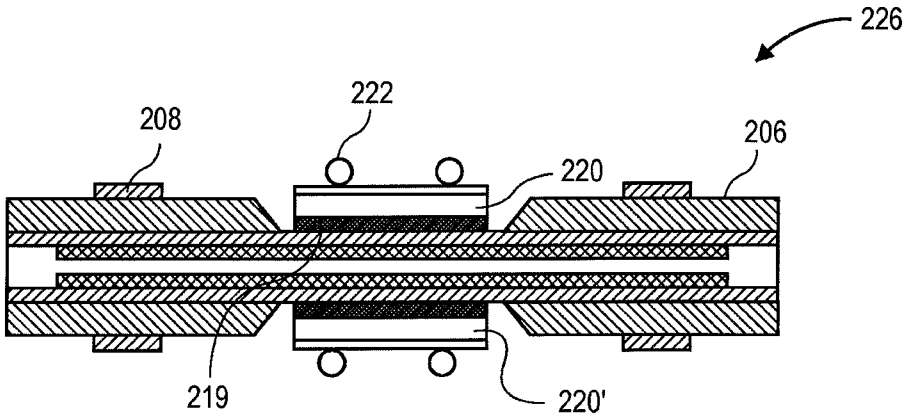

226
222
208
206
220
219
220'

FIG. 2D

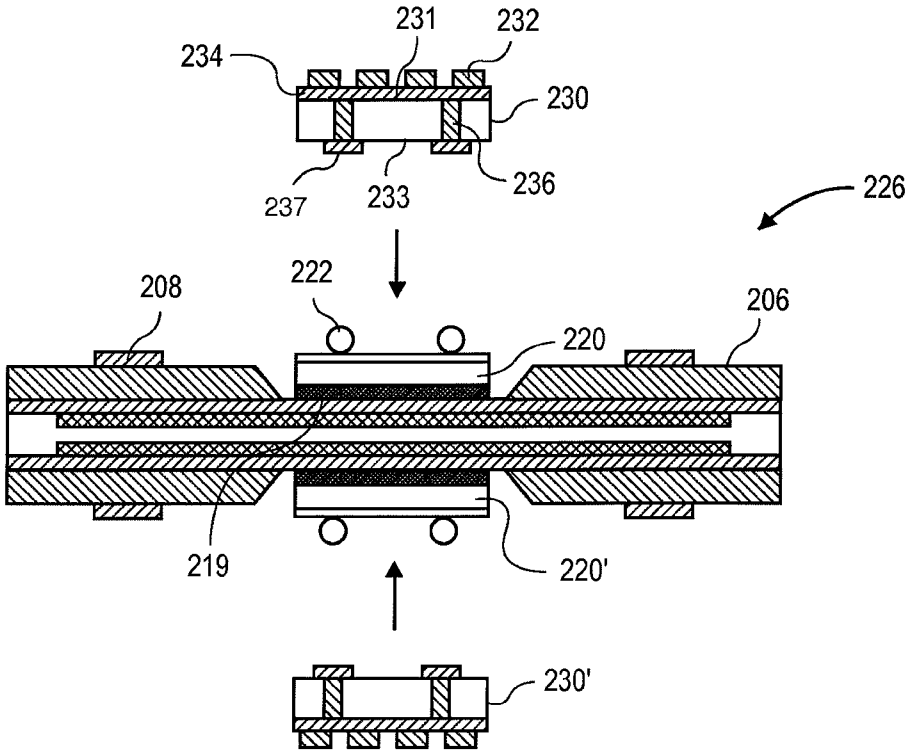

231
232
234
230
237
233
236
226
222
208
206
220
219
220'
230'

FIG. 2E

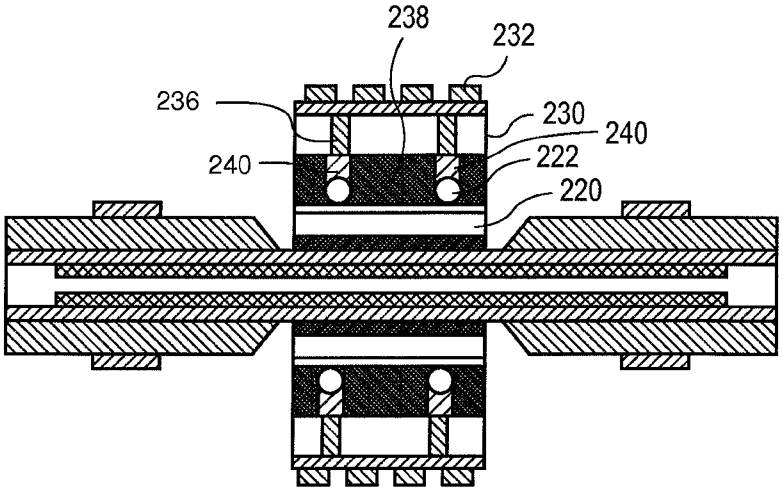


FIG. 2F

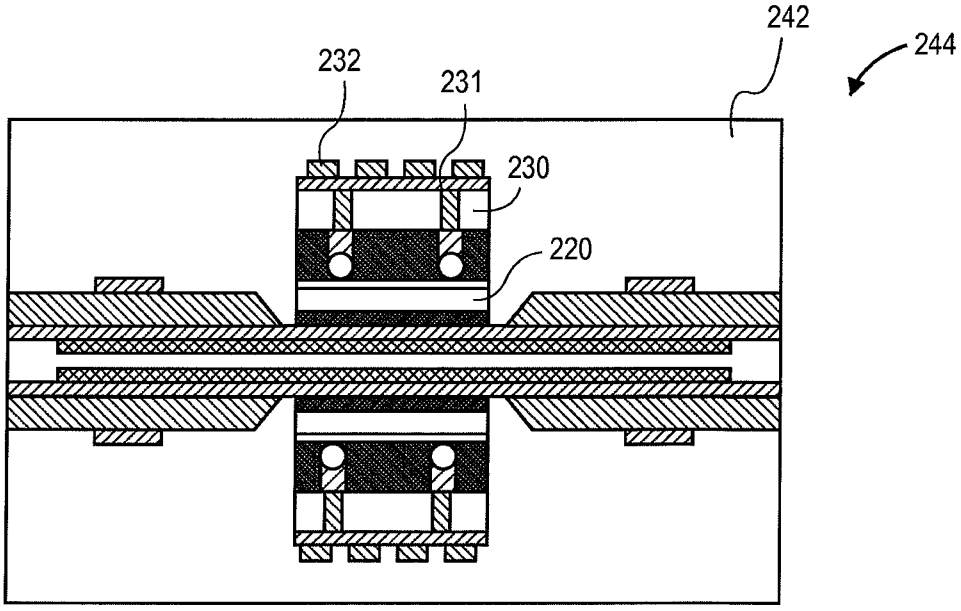


FIG. 2G

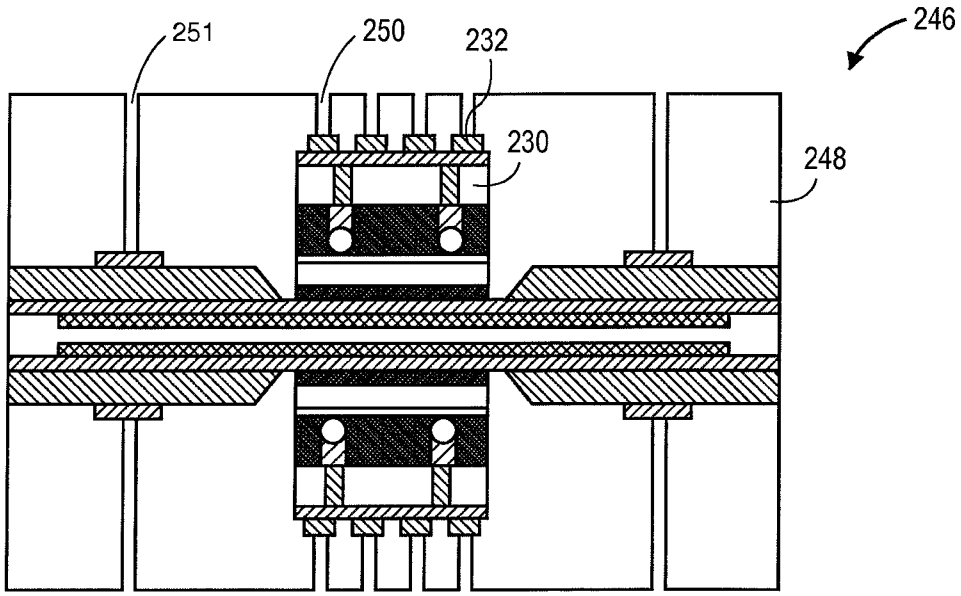


FIG. 2H

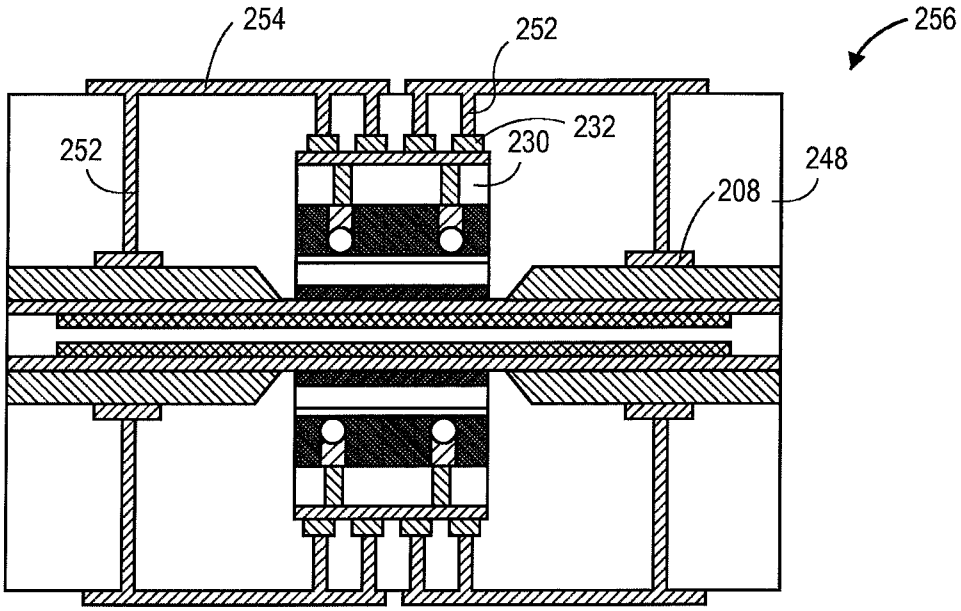


FIG. 2I

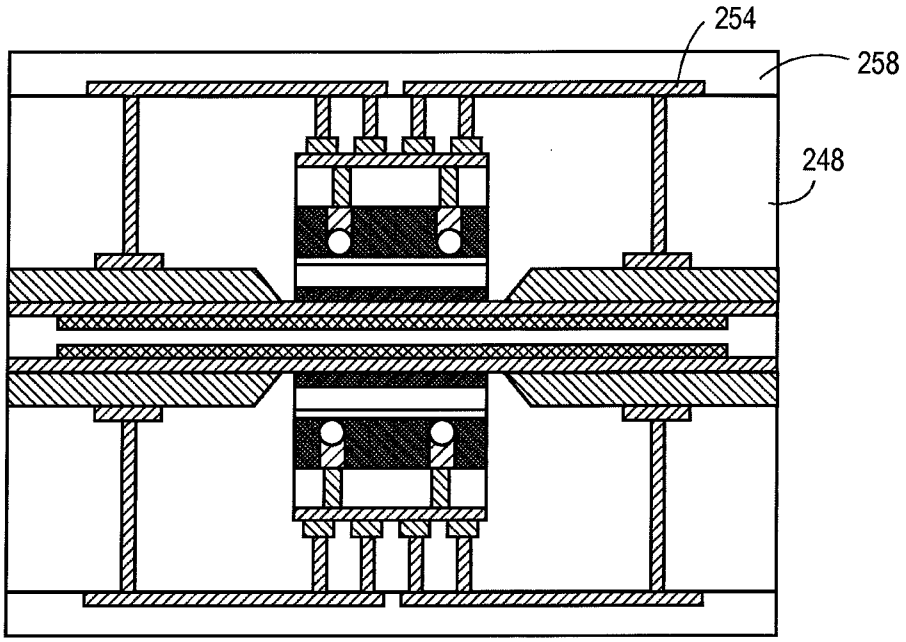


FIG. 2J

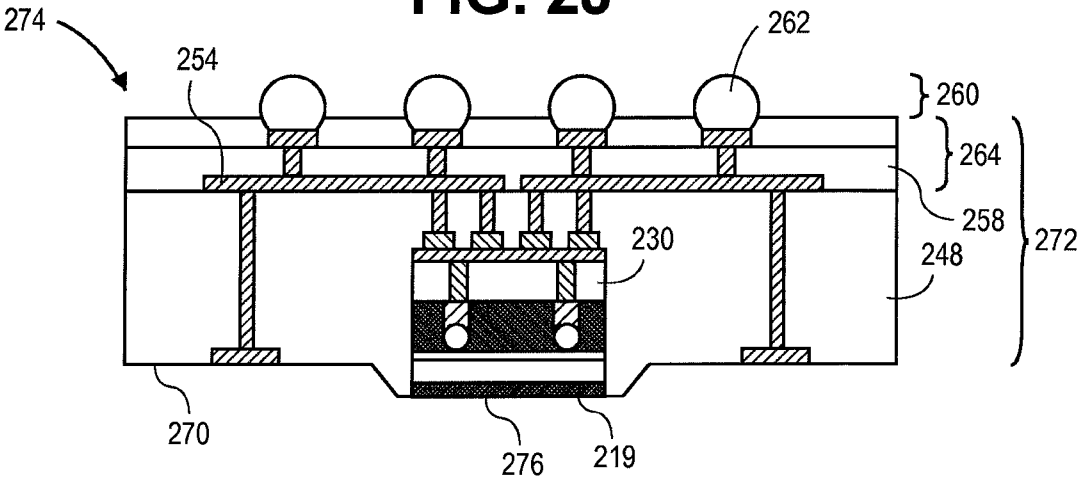


FIG. 2K

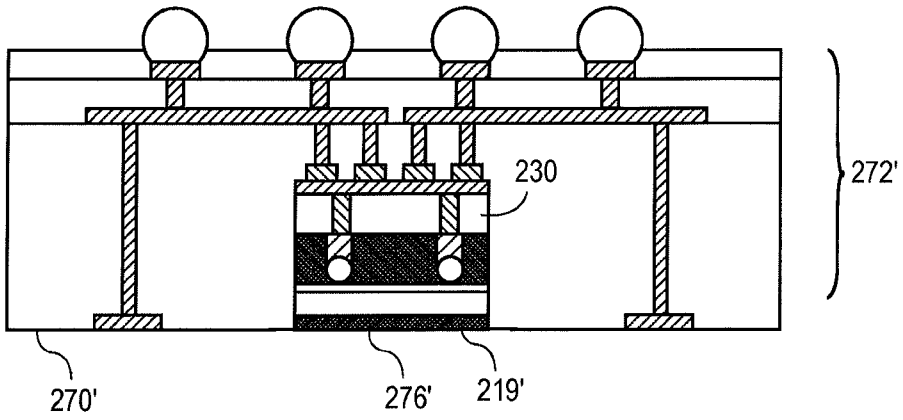


FIG. 2K'

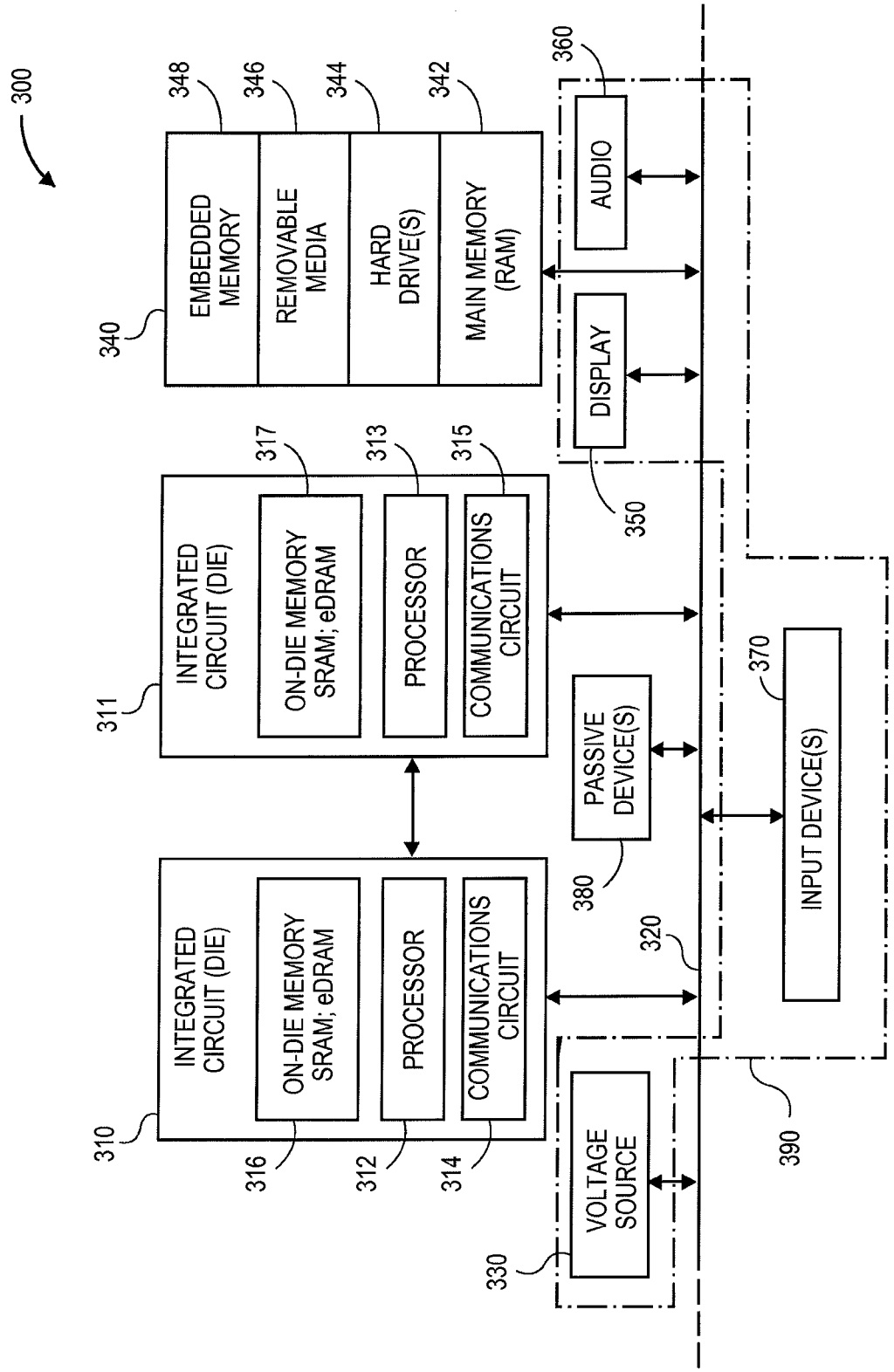

300
340
EMBEDDED MEMORY
348
REMOVABLE MEDIA
346
HARD DRIVE(S)
344
MAIN MEMORY (RAM)
342
360
AUDIO
DISPLAY
350
311
INTEGRATED CIRCUIT (DIE)
ON-DIE MEMORY SRAM; eDRAM
317
PROCESSOR
313
COMMUNICATIONS CIRCUIT
315
PASSIVE DEVICE(S)
380
370
INPUT DEVICE(S)
320
310
INTEGRATED CIRCUIT (DIE)
ON-DIE MEMORY SRAM; eDRAM
316
PROCESSOR
312
COMMUNICATIONS CIRCUIT
314
VOLTAGE SOURCE
330
390

FIG. 3