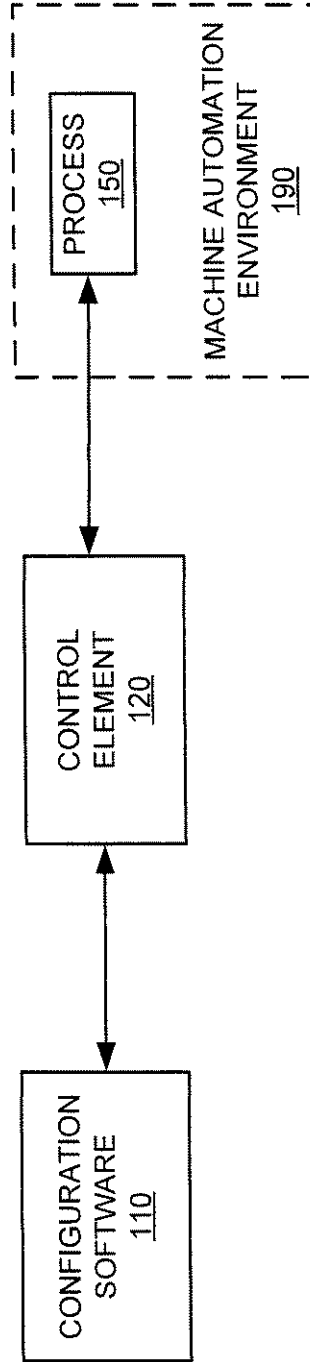




100 ↗

FIGURE 1

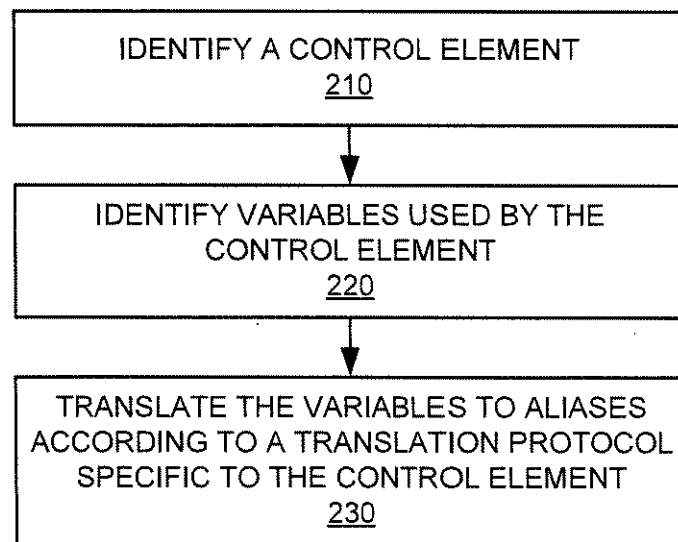


FIGURE 2

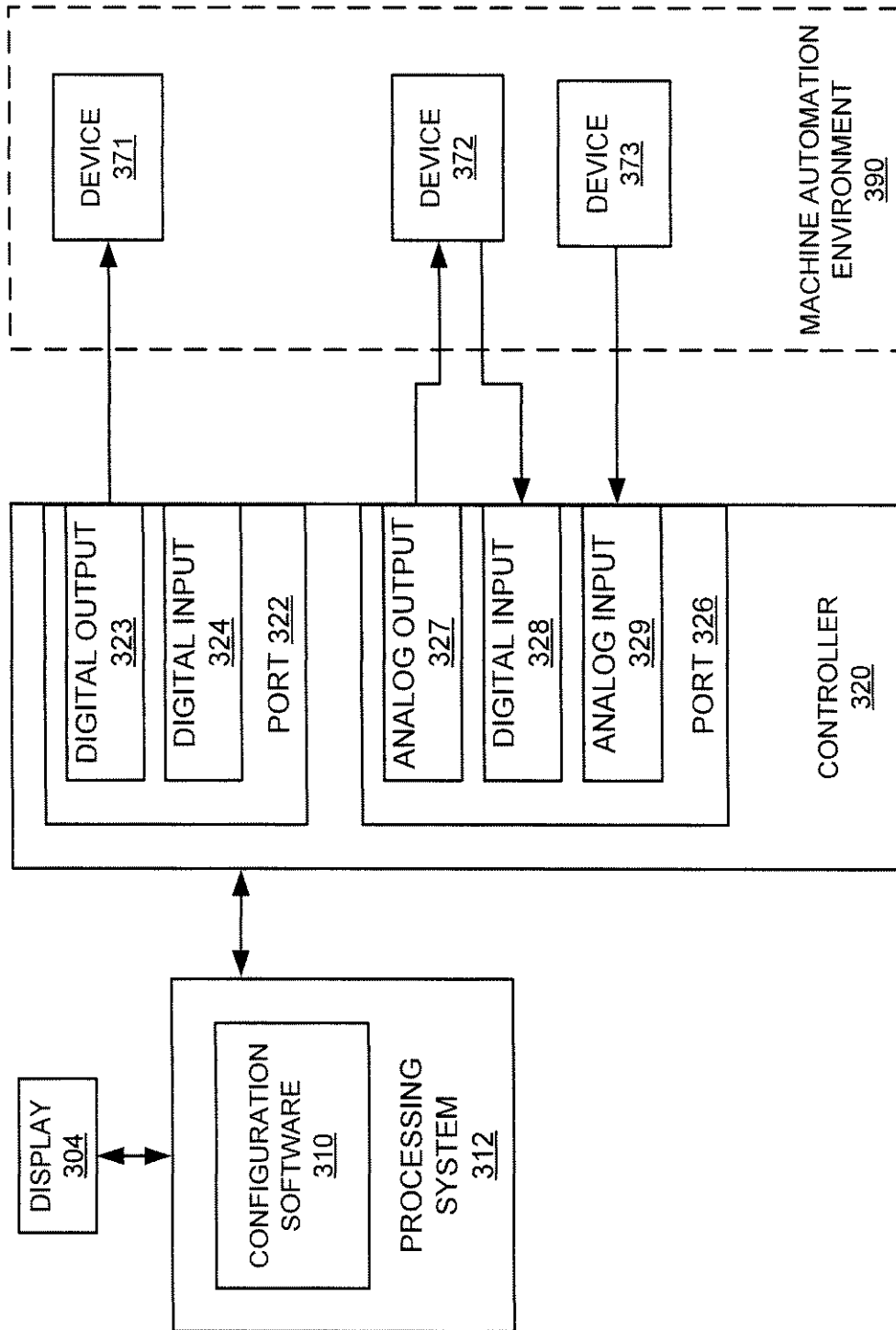


FIGURE 3

Type of Input/Output	Variable (IEC address)	Data Type	Alias
Embedded Digital Input	%IX0.1.0	Boolean Input	_IO_EM_DI_00
Embedded Digital Output	%QX0.0.5	Boolean Output	_IO_EM_DO_05
UPM Slot 1 Digital Input	%IX101.1.1	Boolean Input	_IO_P1_DI_01
UPM Slot 1 Digital Output	%QX101.0.5	Boolean Output	_IO_P1_DO_05
UPM Slot 1 Analog Input	%IW101.1.1	Word Input	_IO_P1_AI_01
UPM Slot 1 Analog Output	%QW101.0.5	Word Output	_IO_P1_AO_05
UPM Slot 3 Digital Input	%IX103.1.1	Boolean Input	_IO_P3_DI_01
UPM Slot 3 Digital Output	%QX103.0.5	Boolean Input	_IO_P3_DO_05
NEO Slot 4 Digital Input	%IX204.1.1	Boolean Input	_IO_X4_DI_01
NEO Slot 4 Digital Output	%QX204.0.5	Boolean Output	_IO_X4_DO_05
NEO Slot 1 Analog Input	%IW201.1.1	Word Input	_IO_X1_AI_01
NEO Slot 1 Analog Output	%QW201.0.5	Word Output	_IO_X1_AO_05

FIGURE 4

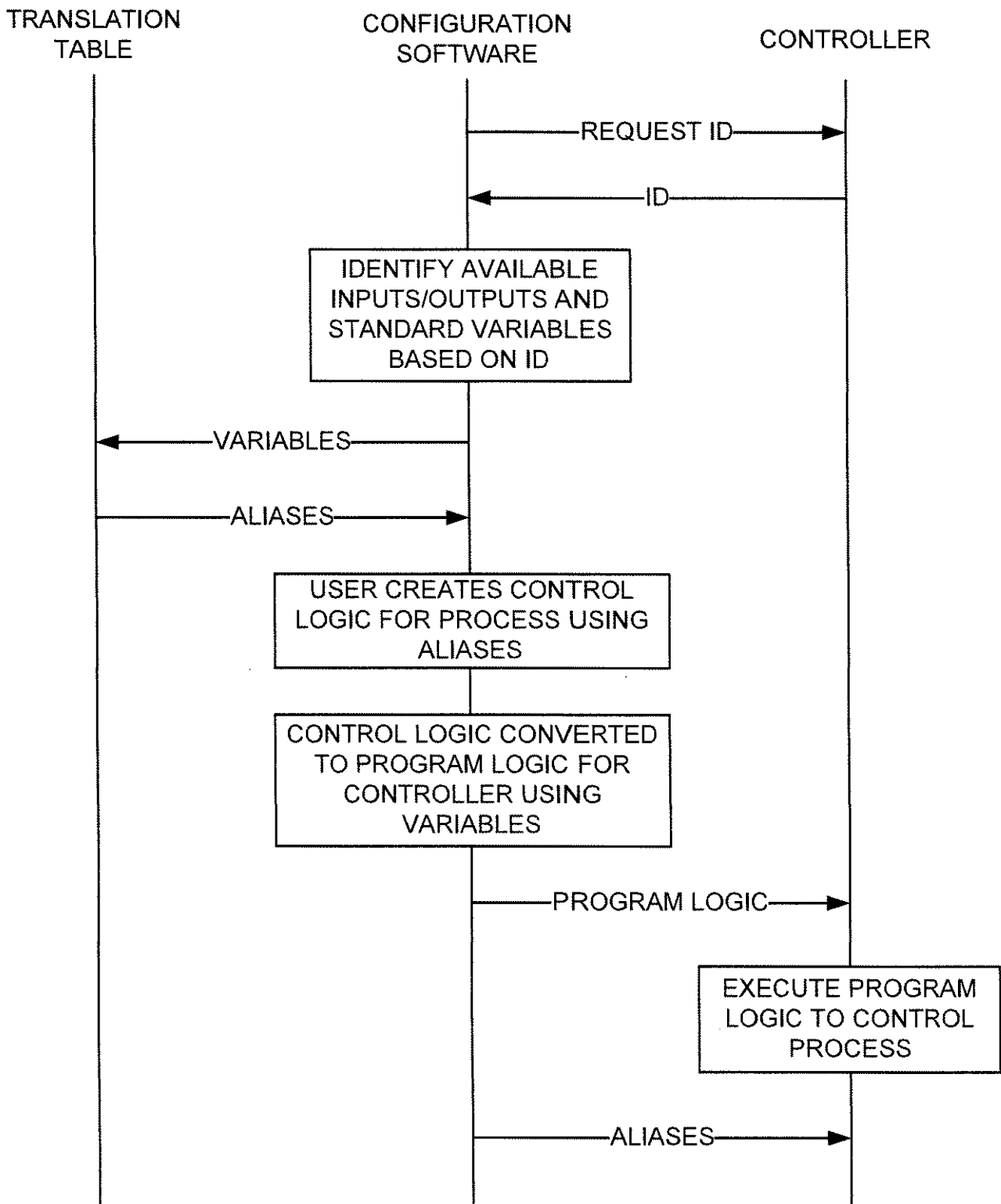


FIGURE 5

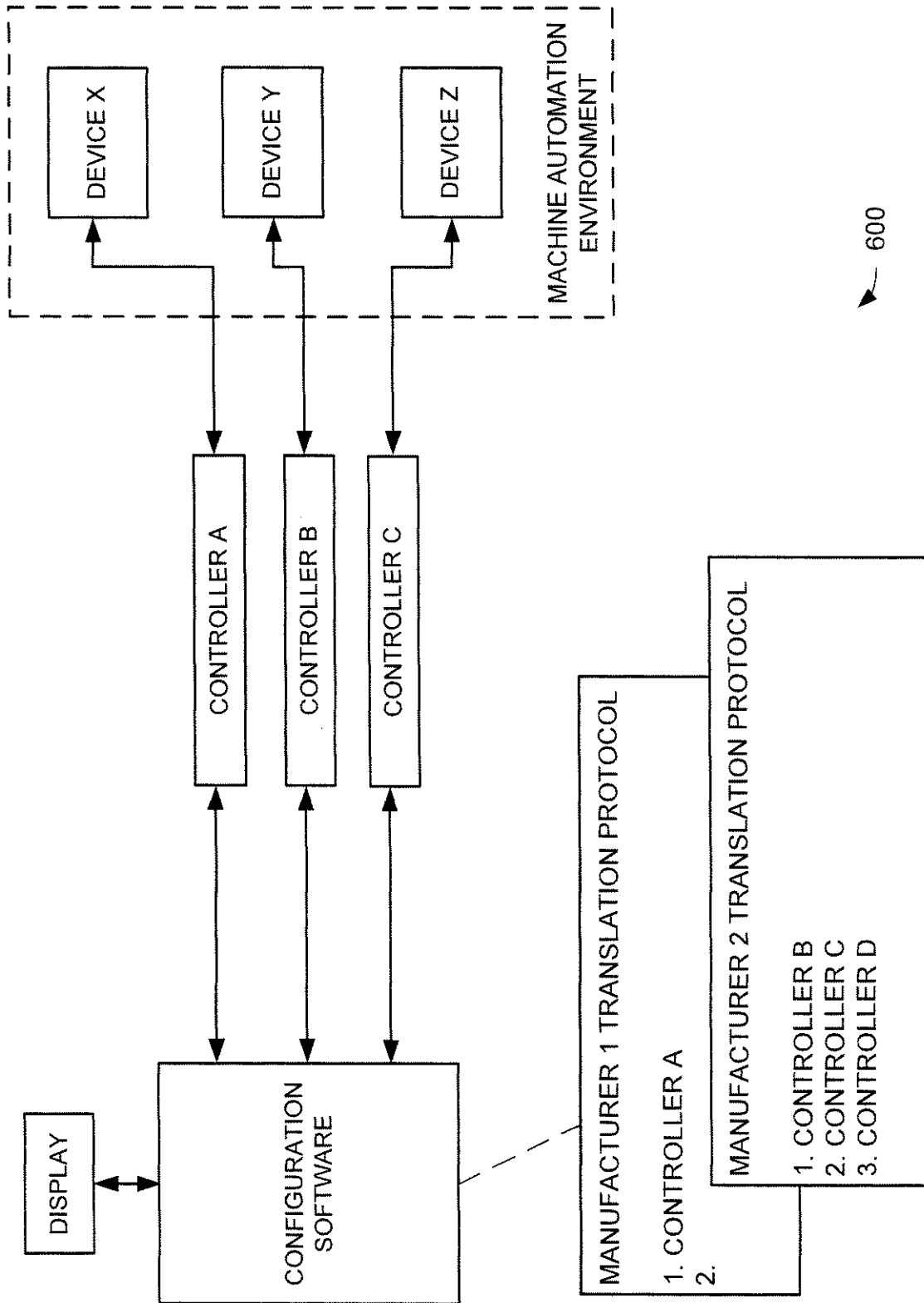


FIGURE 6

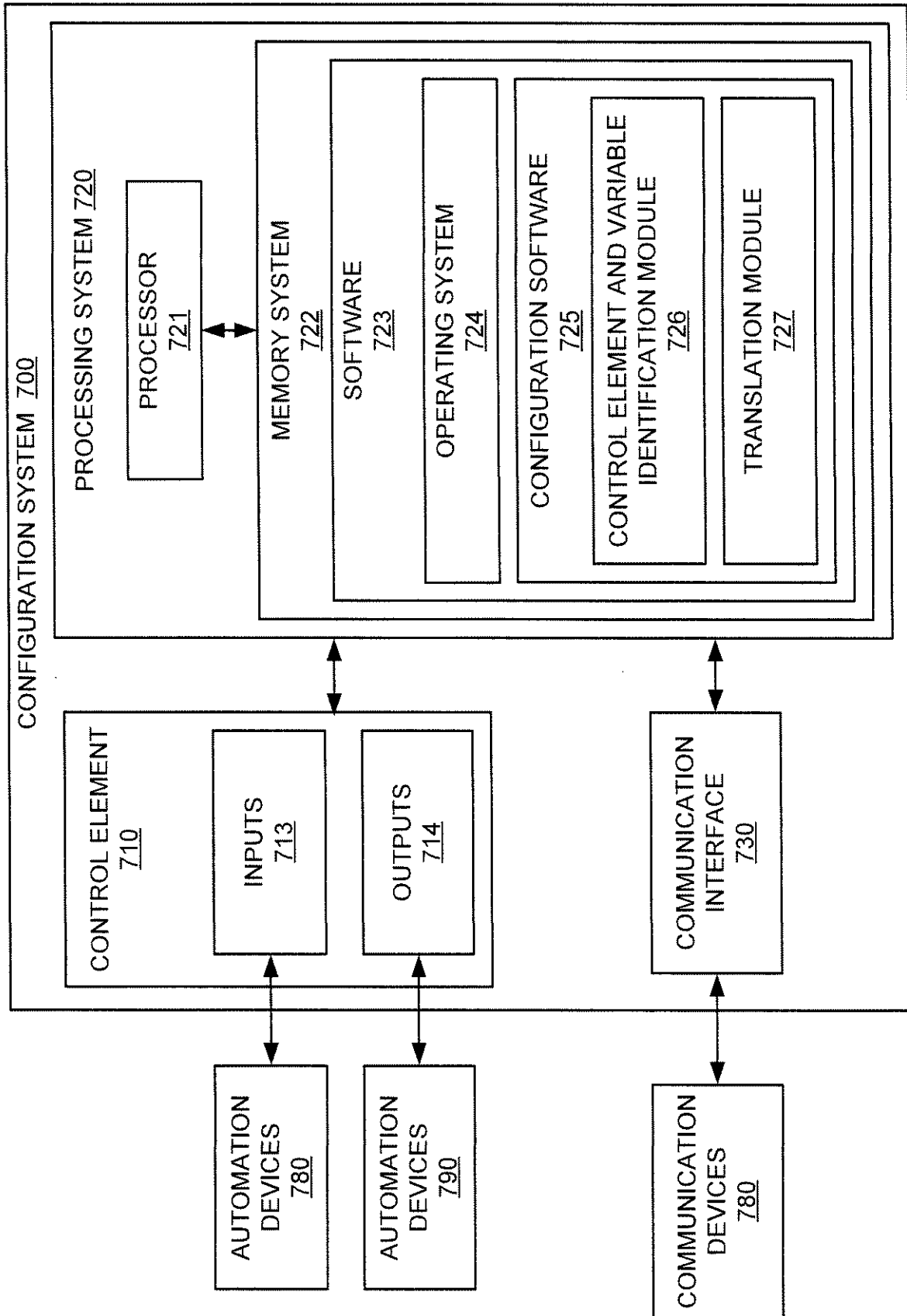


FIGURE 7