

ABSTRACT

METHOD AND APPARATUS FOR DISTRIBUTING CLOCKS

Insertion delay associated with receiving and distributing a clock within an IC is removed using a DLL. The structure of a differential receiver (404) in the feedback circuit (410, 416, 422, 424) of the DLL is substantially matched
5 to the structure of an input differential receiver (402) which receives and inputs a differential input clock into the DLL. In this manner, the insertion delay associated with the input differential receiver is removed from a delay-corrected clock output from the DLL. By substantially matching one or more aspects of the feedback circuit to one or more aspects of a clock distribution
10 circuit (408, 426, 432, 434) for distributing the delay-corrected clock, the insertion delay associated with the clock distribution circuit can also be removed from the delay-corrected clock.

FIG. 3A

