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(54) Title: A LEAKAGE SUPPRESSING CIRCUIT

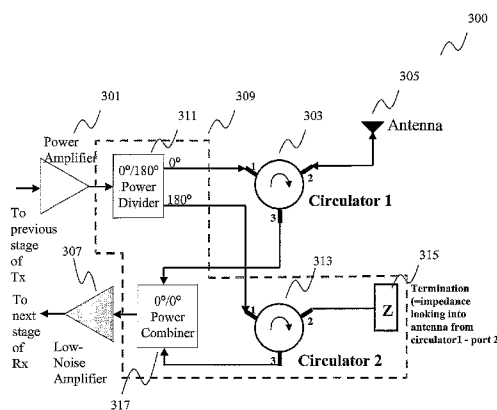


Figure 3

(57) Abstract: A leakage suppressing circuit for a communication device is provided. The leakage suppressing circuit comprises a signal transforming unit having an input for receiving an input signal, a first output and a second output, the signal transforming unit being configured such that the phase of an output signal at the first output is at least substantially the same as or at least substantially opposite to the phase of an output signal at the second output, and the power of the output signal at the first output is at least substantially equal to the power of the output signal at the second output, a first signal directing unit, a second signal directing unit, and a combining unit.



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