

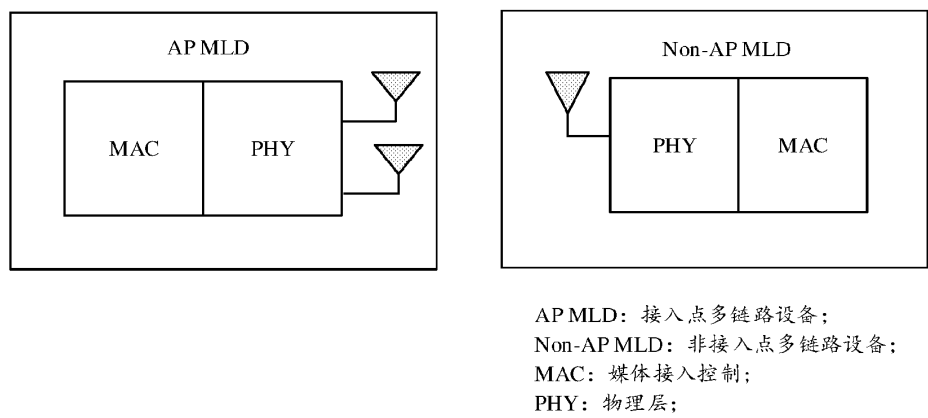


图 1

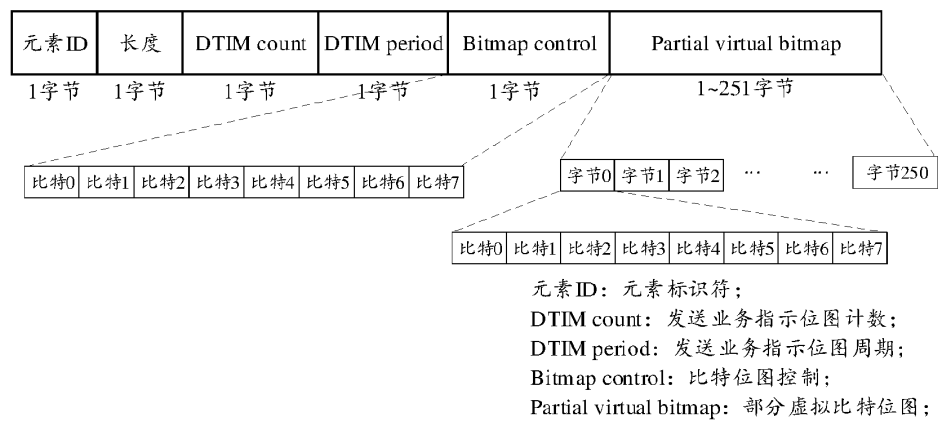


图 2

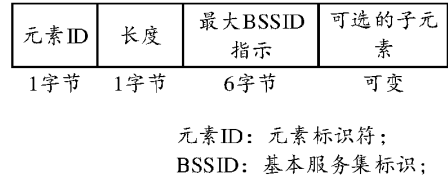


图 3

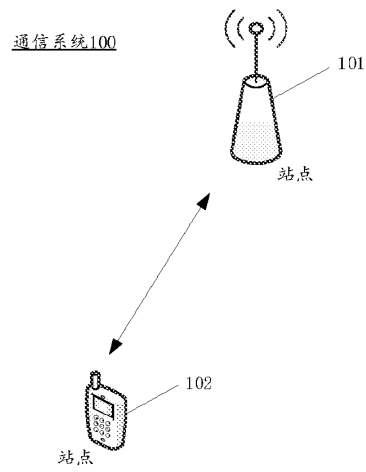
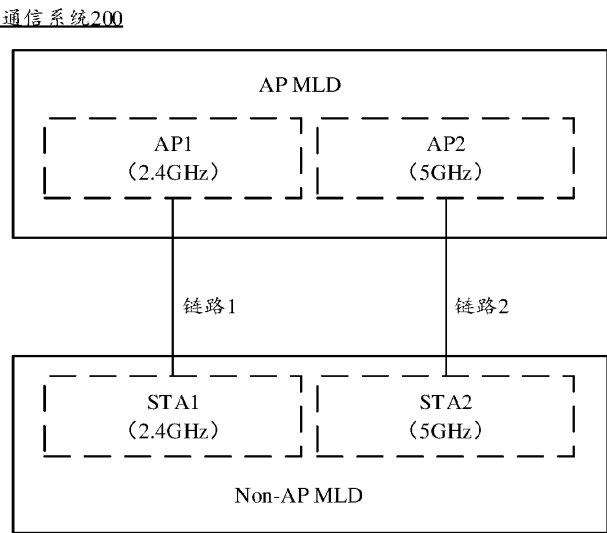


图 4a



AP MLD: 接入点多链路设备;
Non-AP MLD: 非接入点多链路设备;
AP: 接入点;
STA: 站点;

图 4b

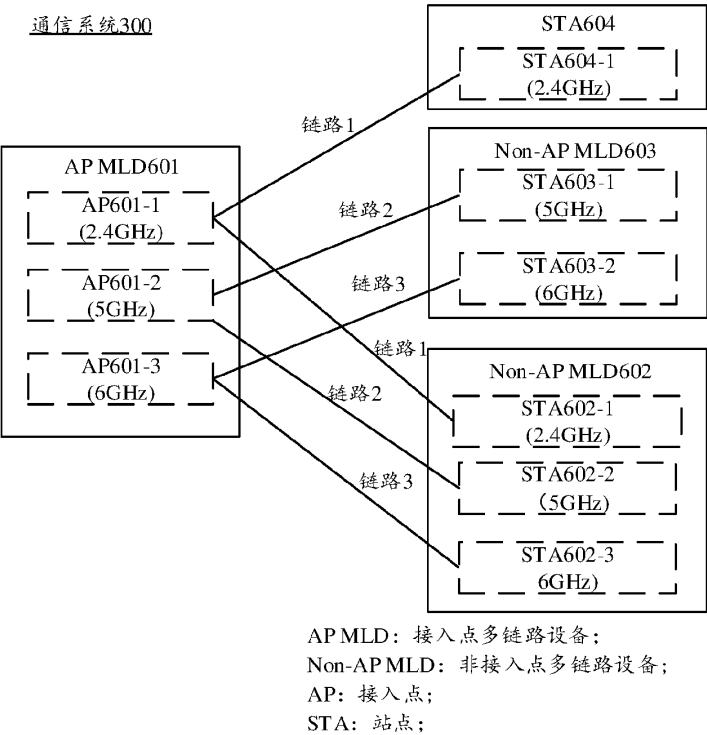


图 4c

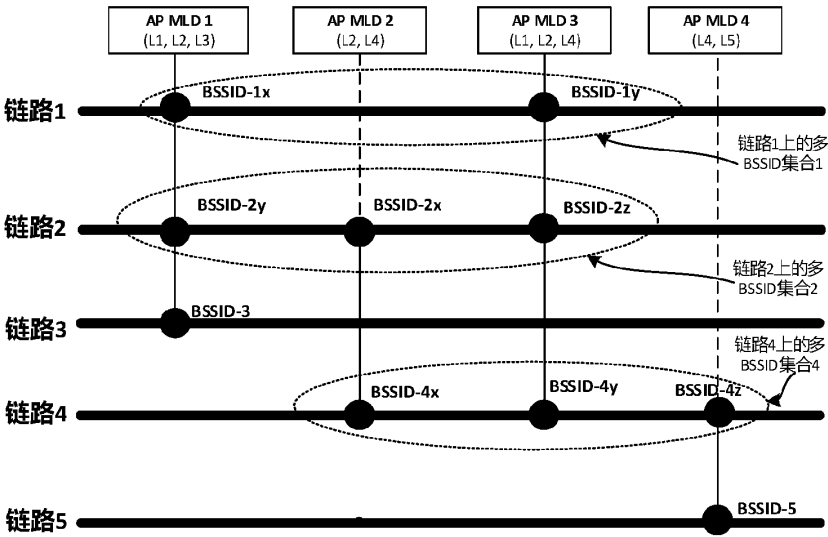


图 5

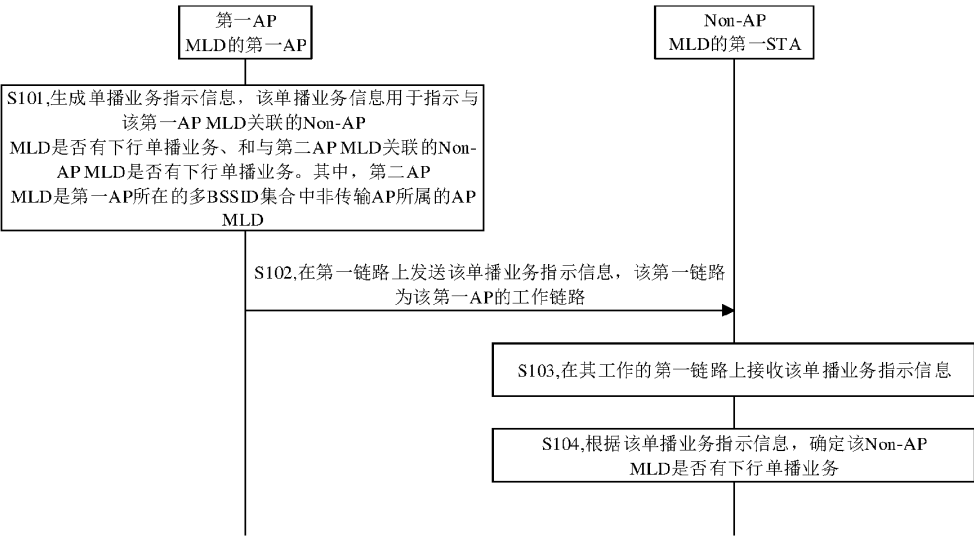
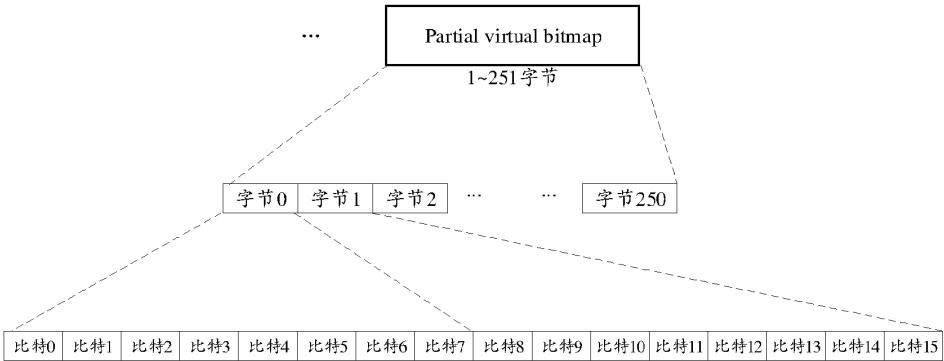


图 6



Partial virtual bitmap: 部分虚拟比特位图;

图 7

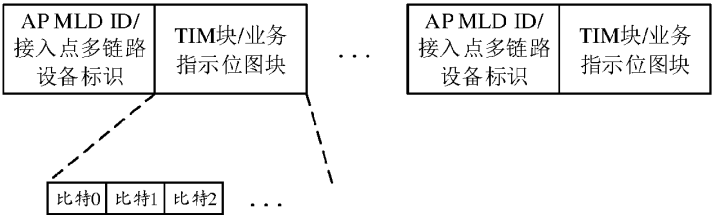


图 8

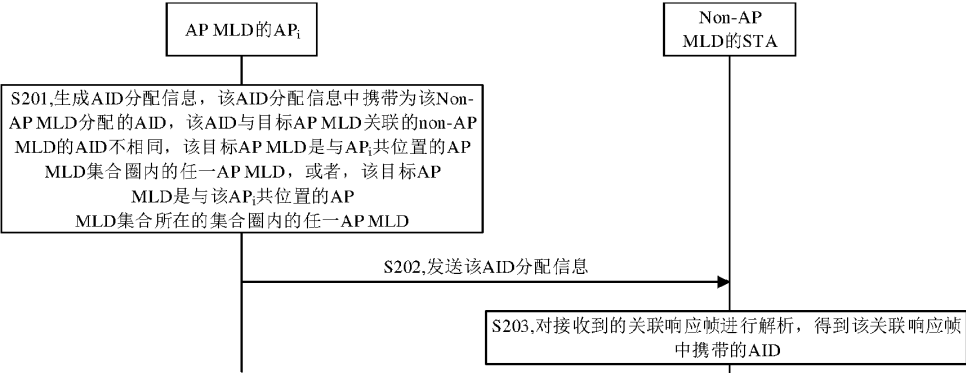


图 9

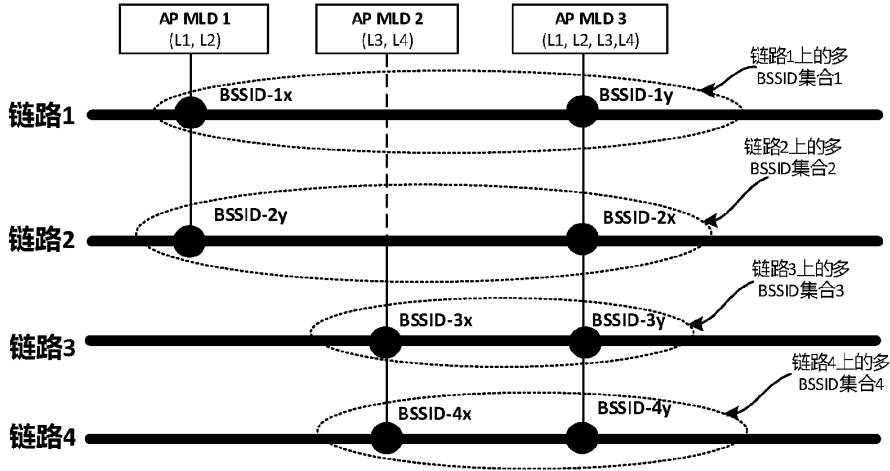


图 10a

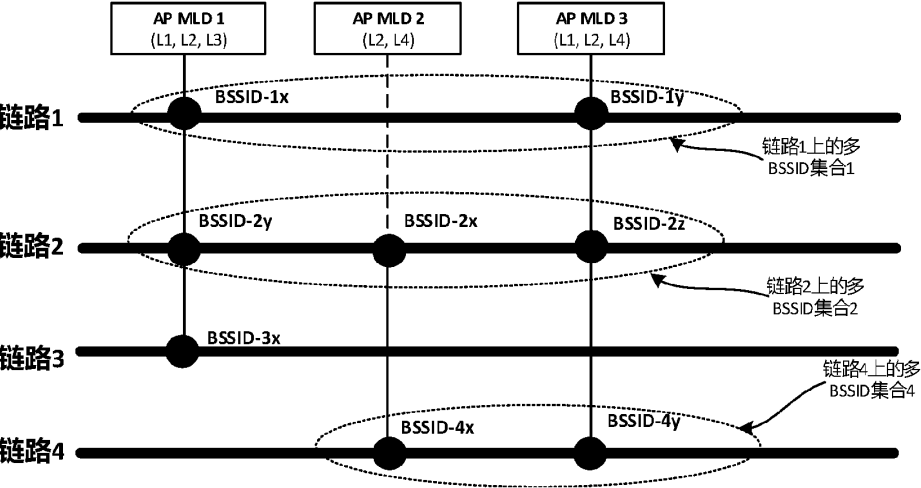


图 10b

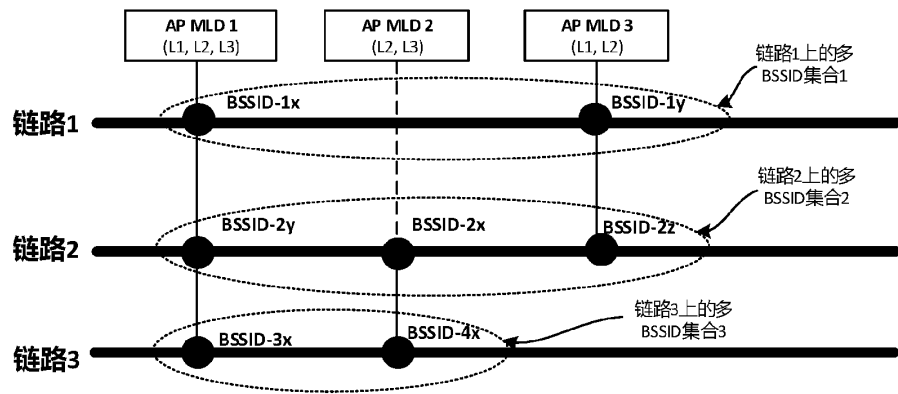


图 10c

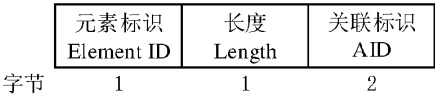


图 11

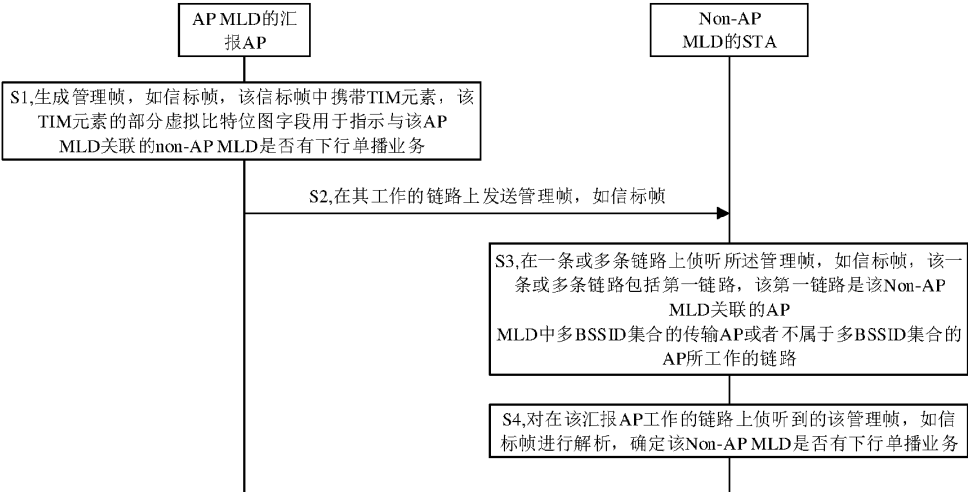


图 12

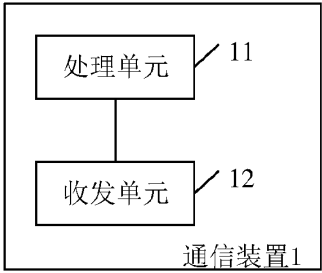


图 13

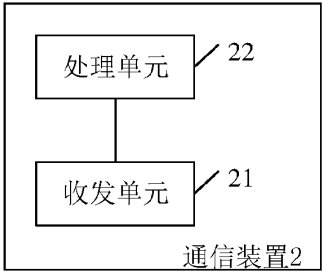


图 14

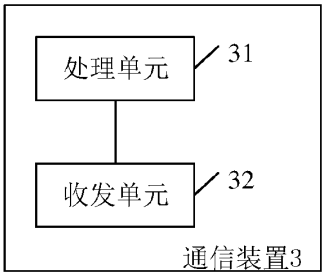


图 15

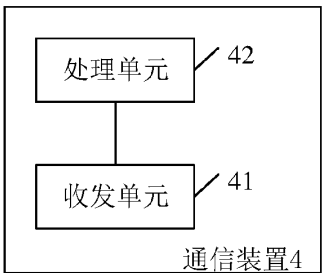


图 16

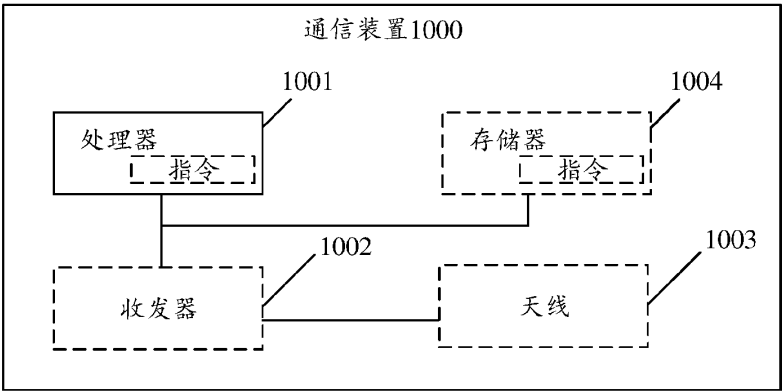


图 17