

FIG. 1

FIG. 1A

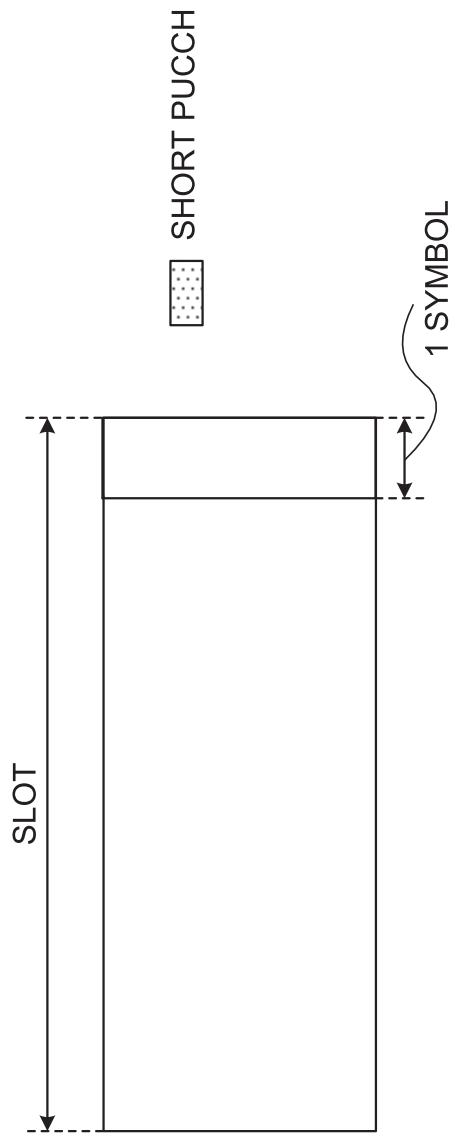
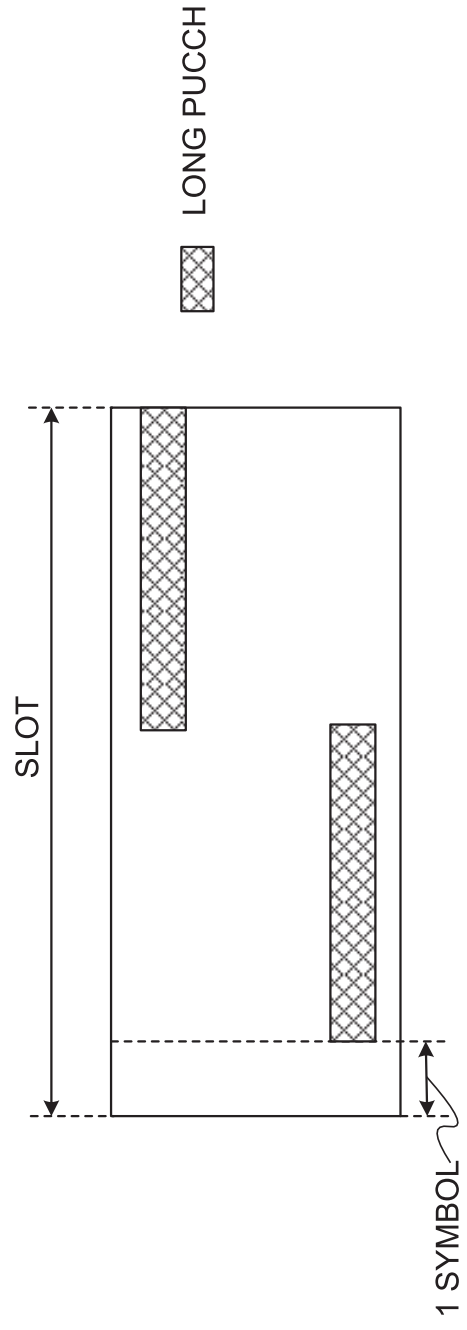


FIG. 1B



PUCCH FORMAT	NUMBER OF SYMBOLS IN SLOT	NUMBER OF BITS	Pre-DFT OCC
0	1-2	≤ 2	N.A.
1	4-14	≤ 2	N.A.
2	1-2	> 2	N.A.
3	4-14	> 2	No
4	4-14	> 2	Yes

FIG. 2

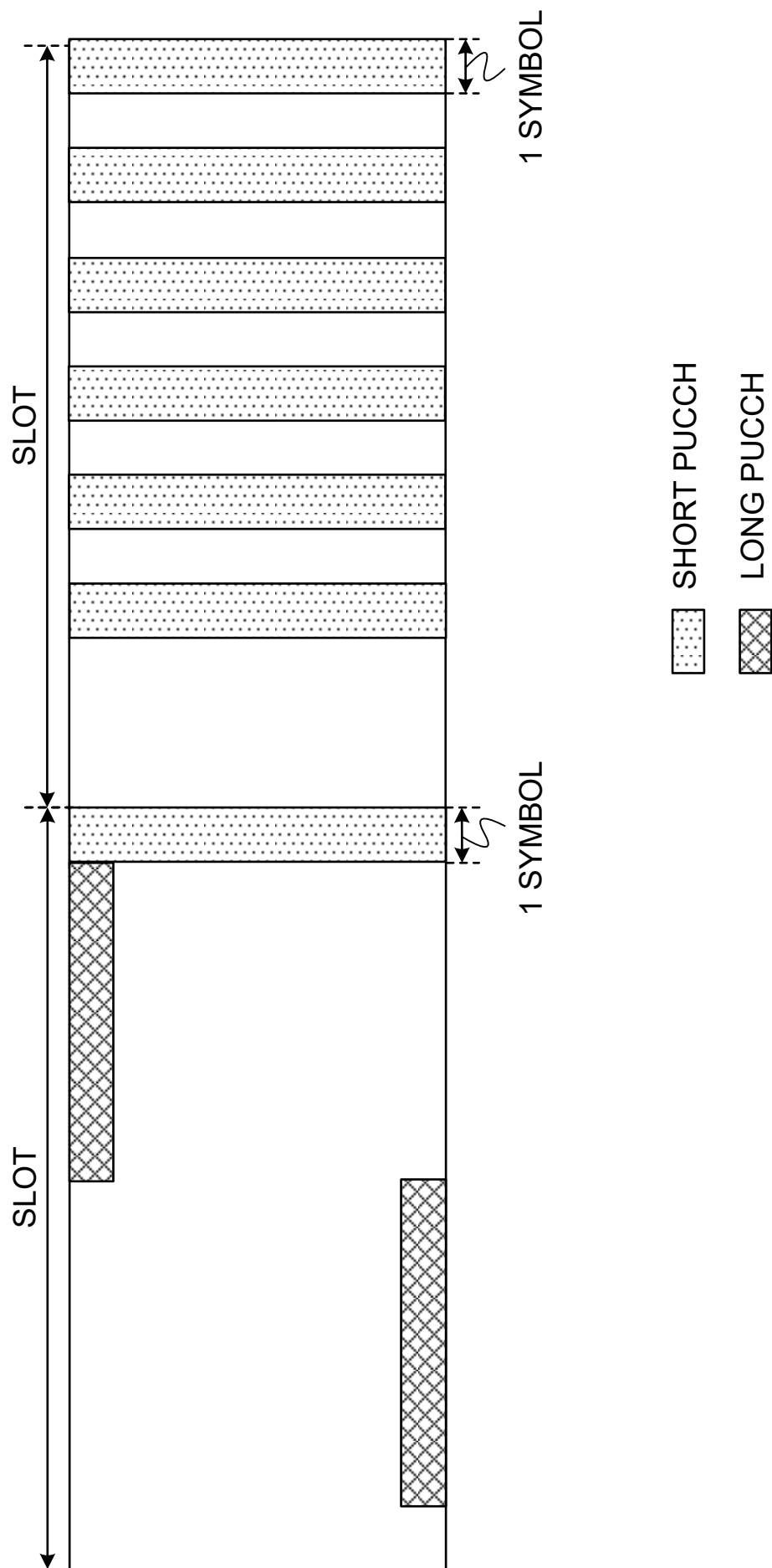


FIG. 3

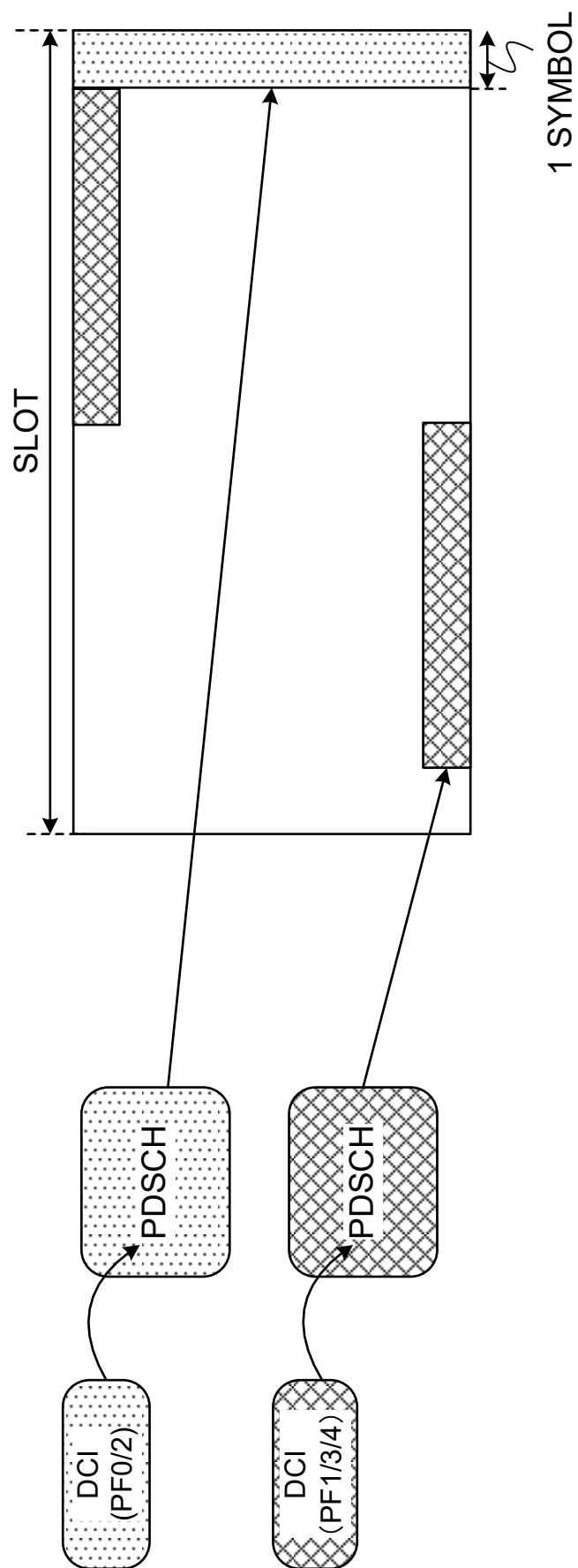


FIG. 4

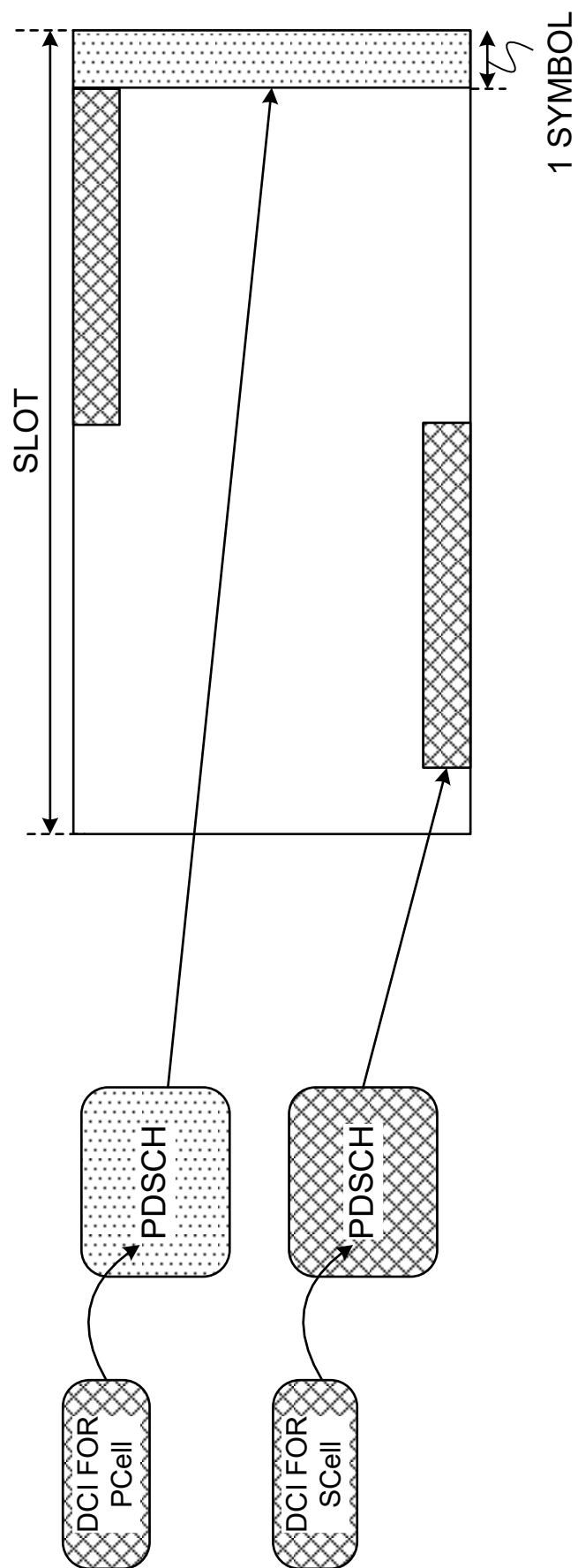


FIG. 5

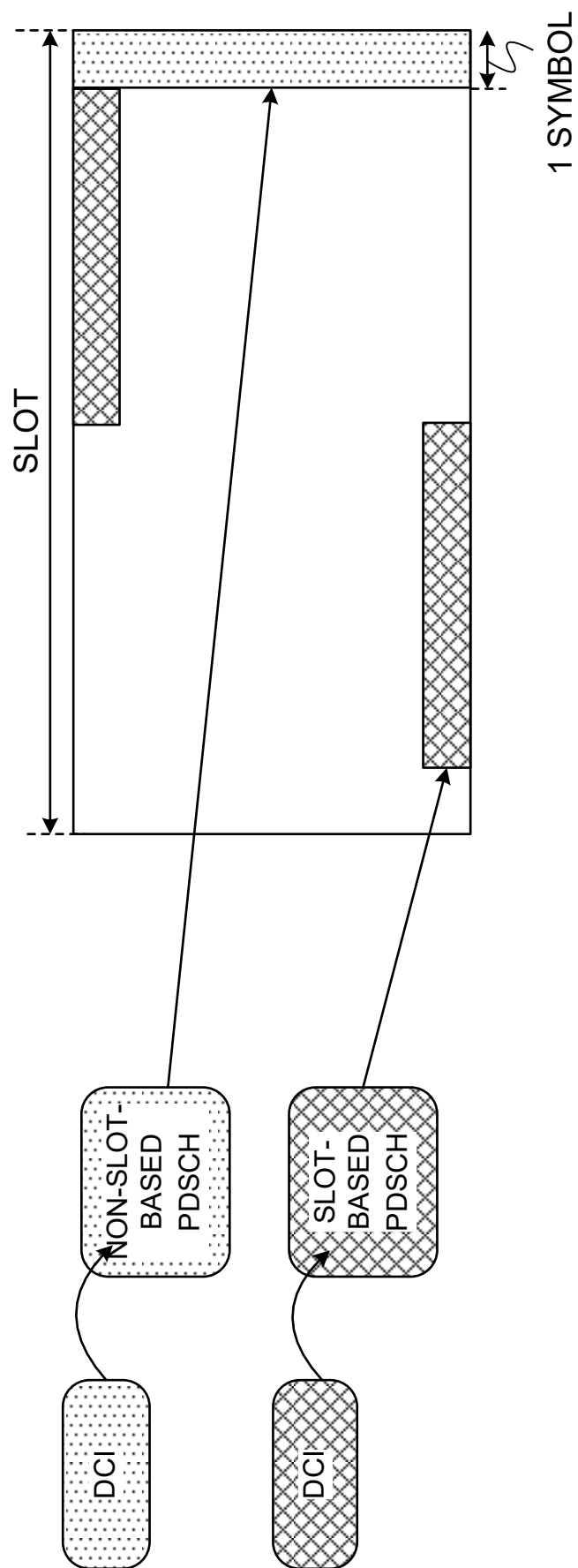


FIG. 6

FIG. 7

FIG. 7A

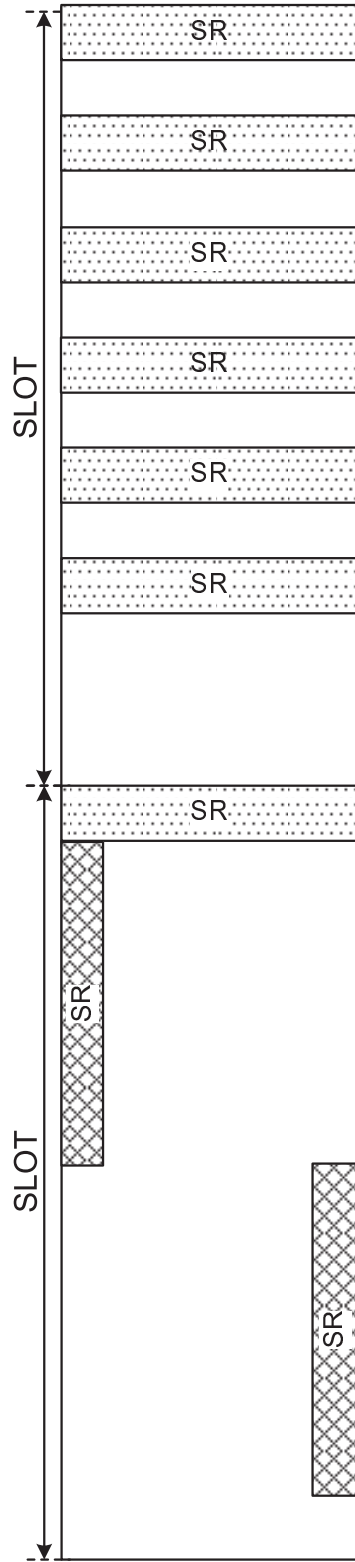


FIG. 7B

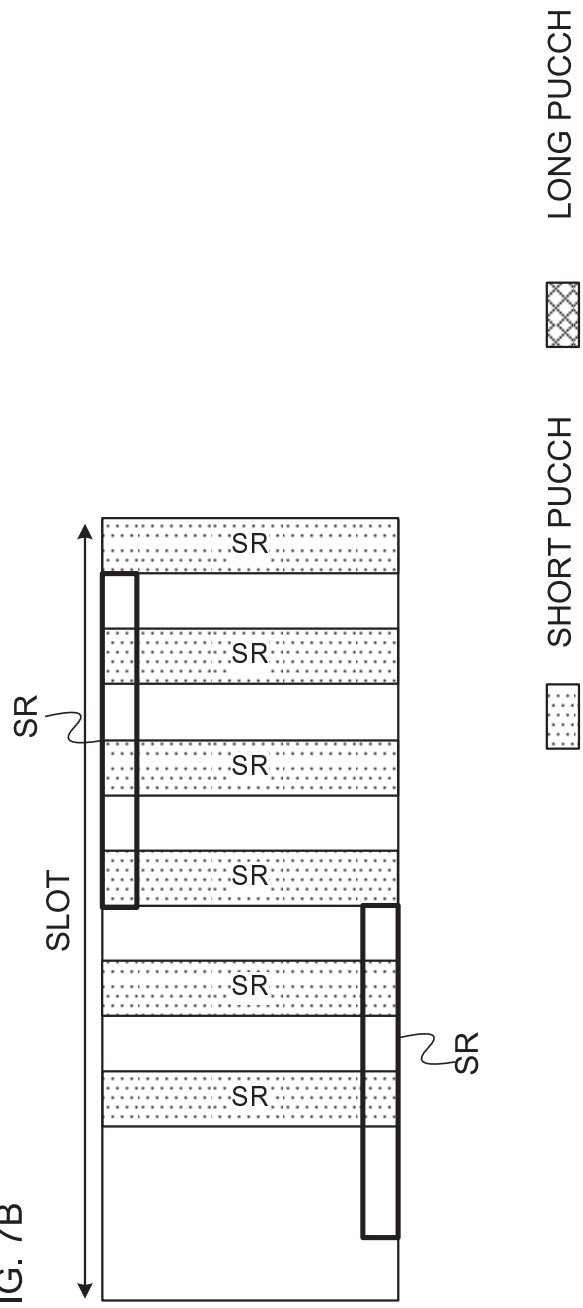


FIG. 8

FIG. 8A

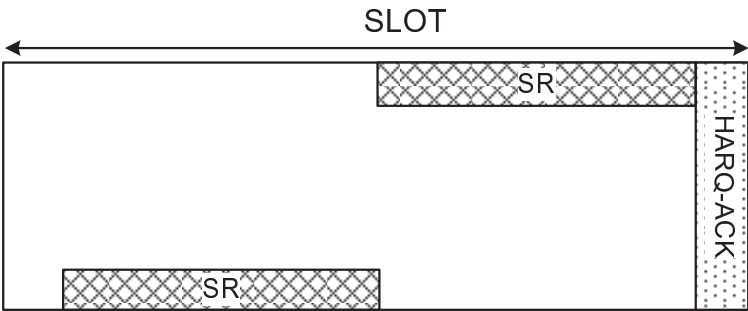


FIG. 8B

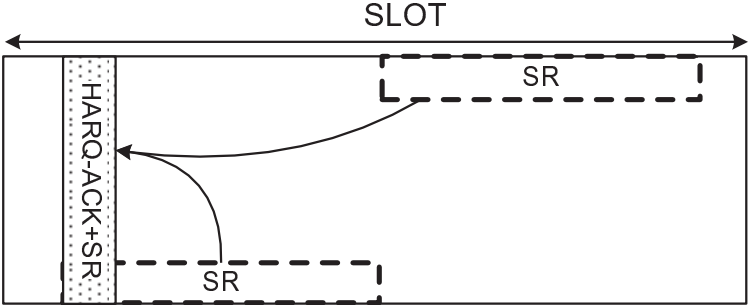


FIG. 8C

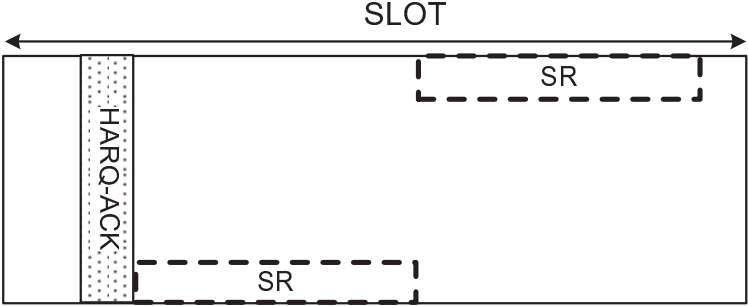


FIG. 8D

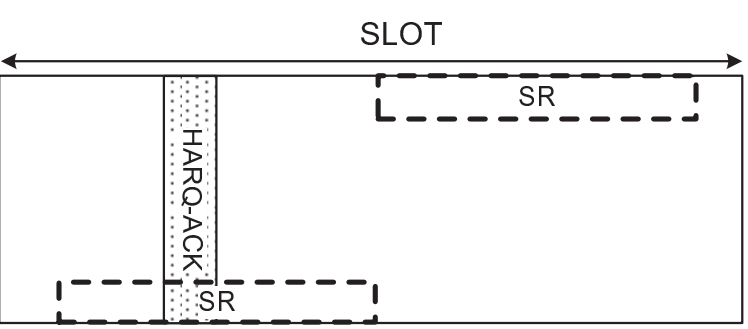


FIG. 9

FIG. 9A

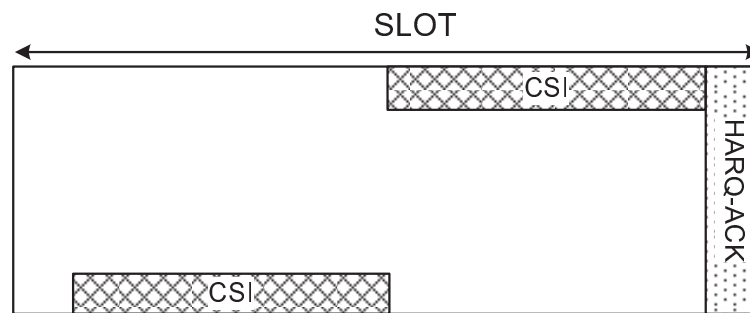


FIG. 9B

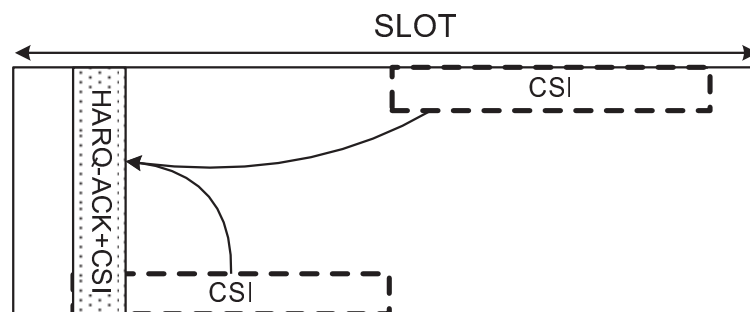


FIG. 9C

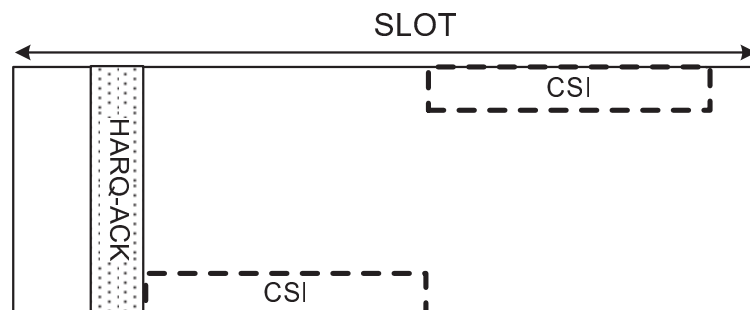


FIG. 9D

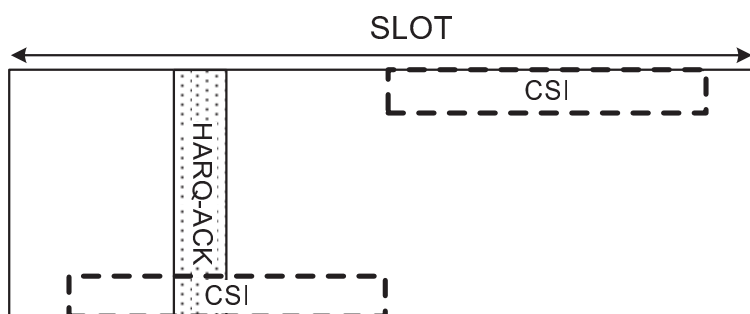


FIG. 10

FIG. 10A

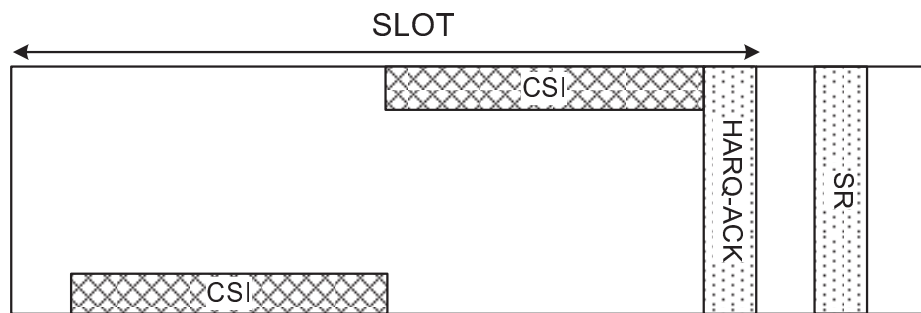


FIG. 10B

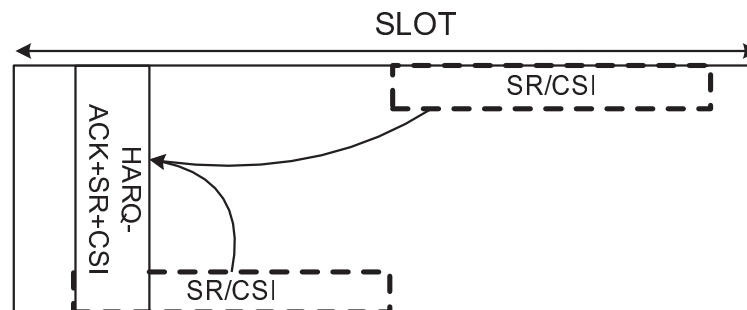


FIG. 10C

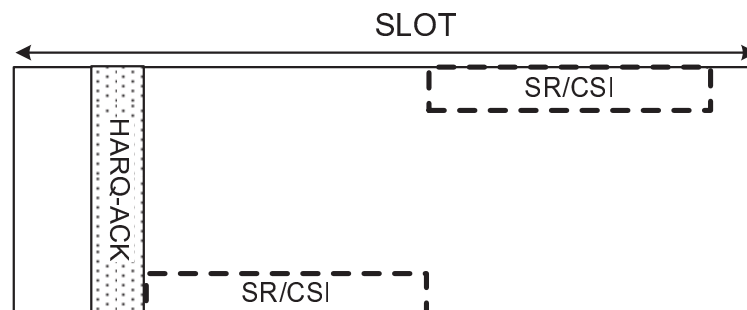


FIG. 10D

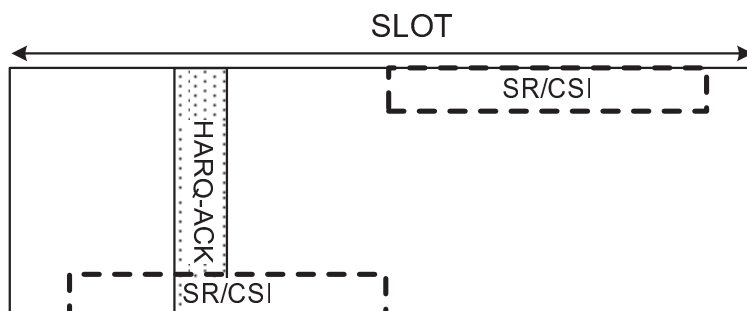


FIG. 11

FIG. 11A

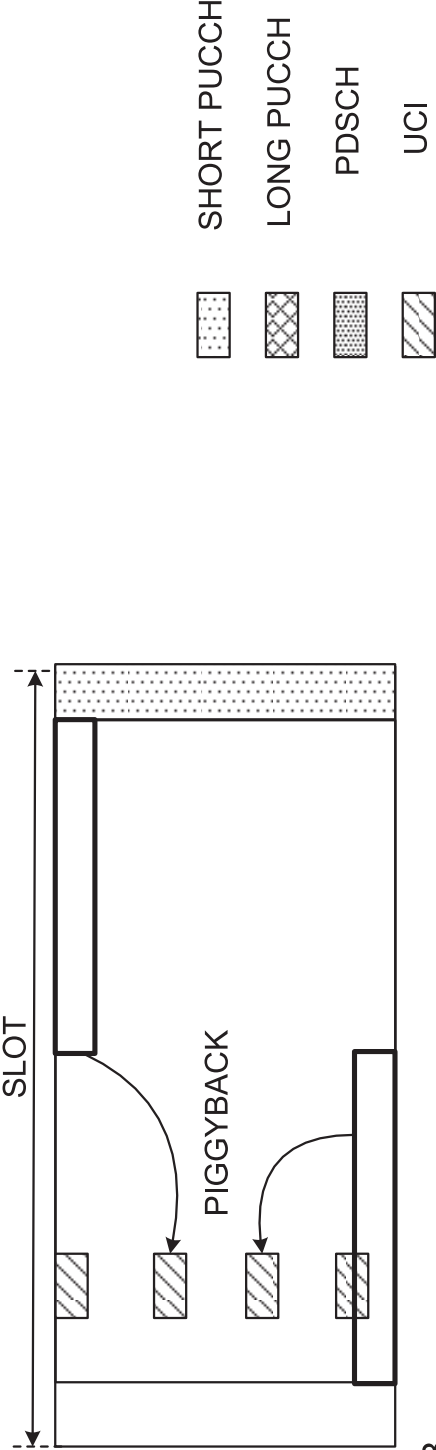
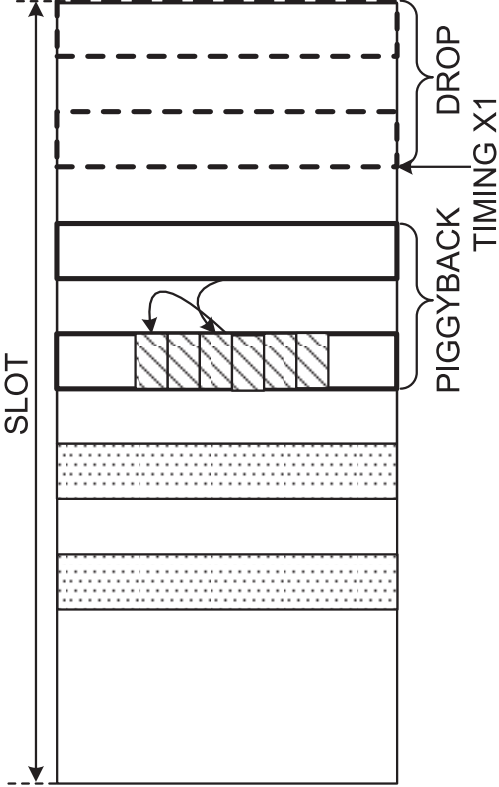


FIG. 11B



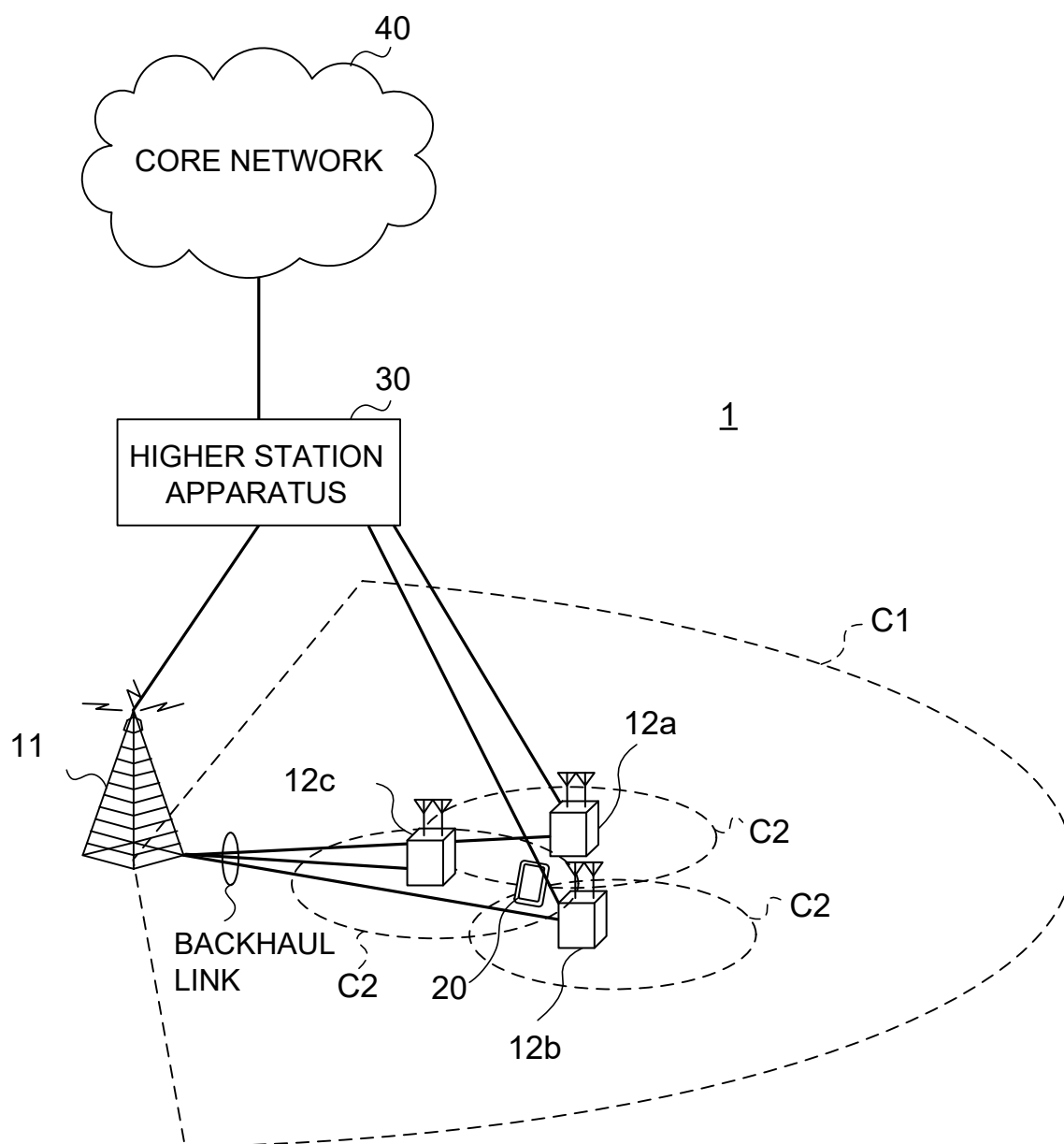


FIG. 12

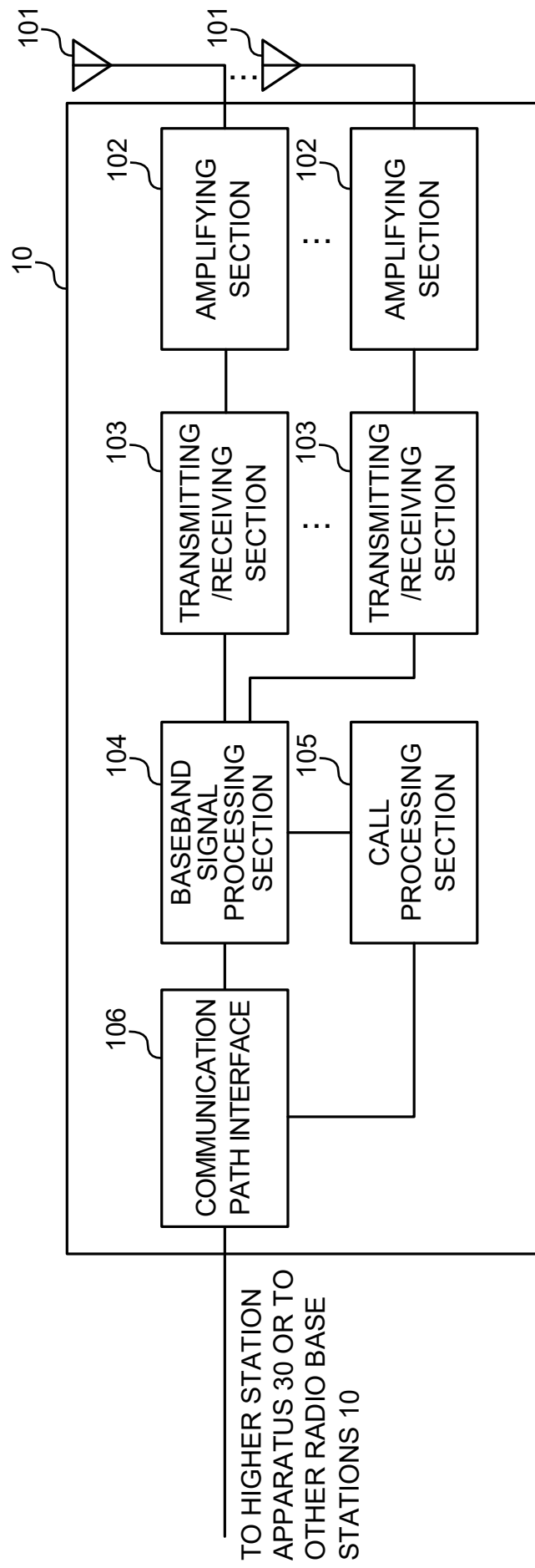


FIG. 13

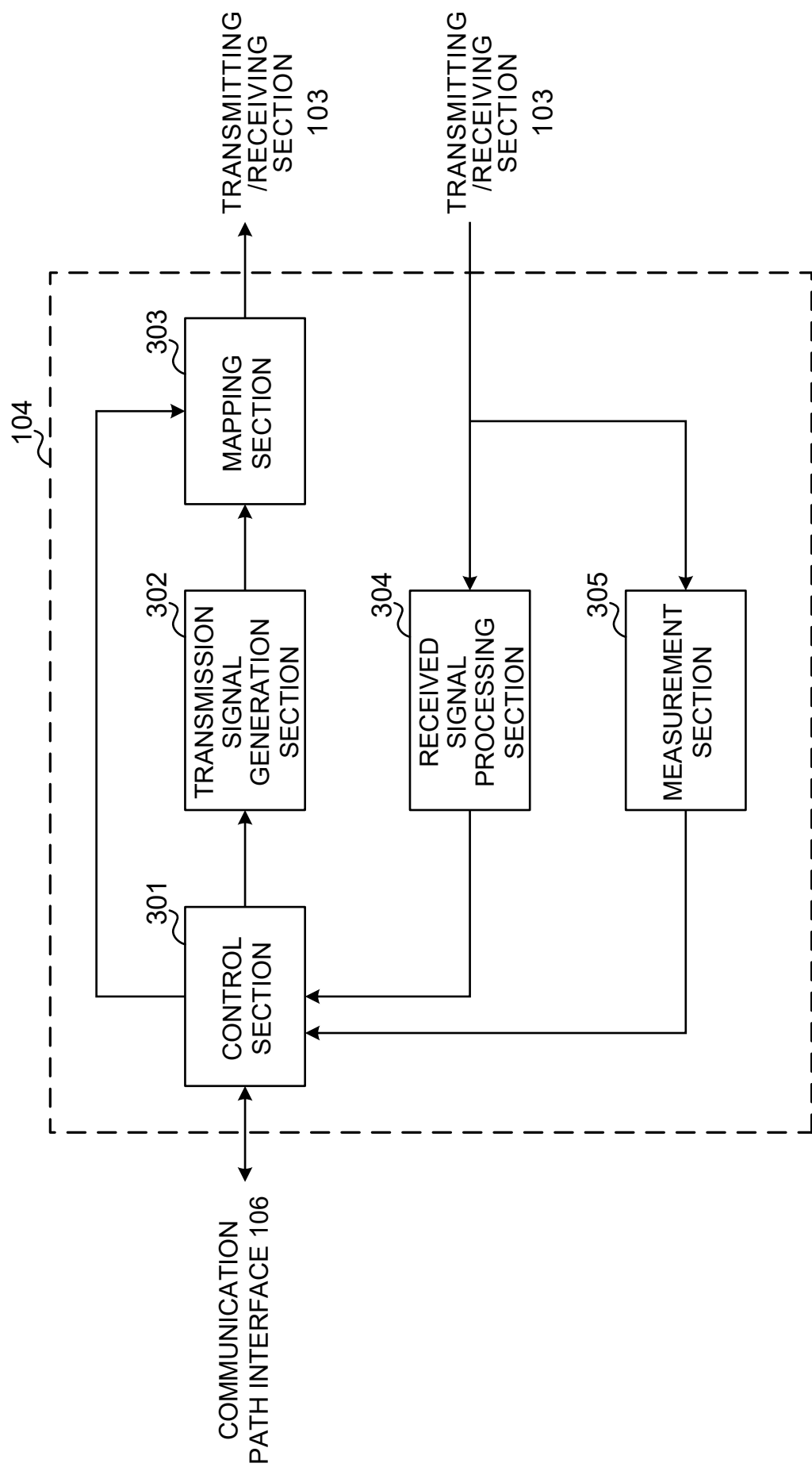


FIG. 14

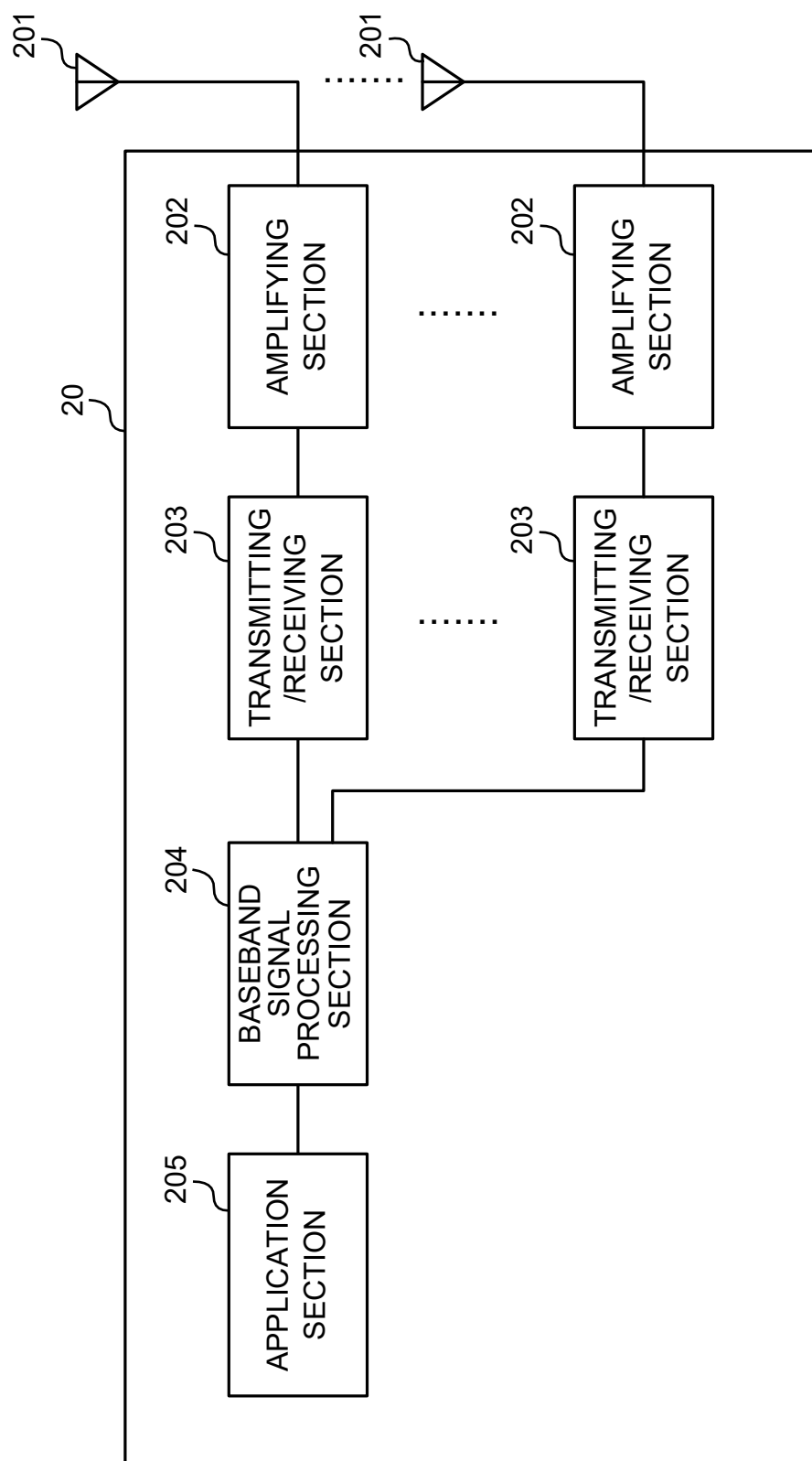


FIG. 15

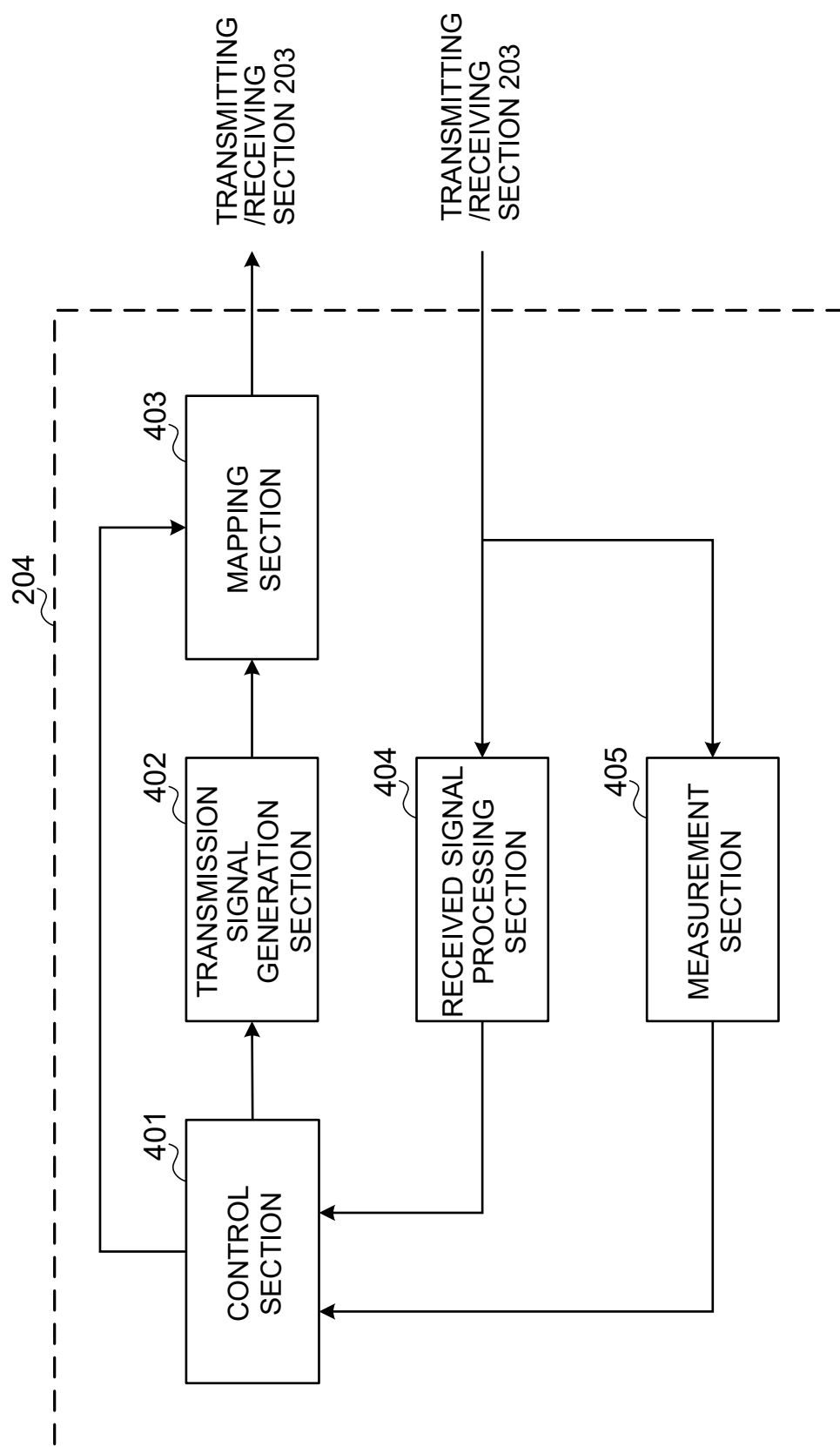


FIG. 16

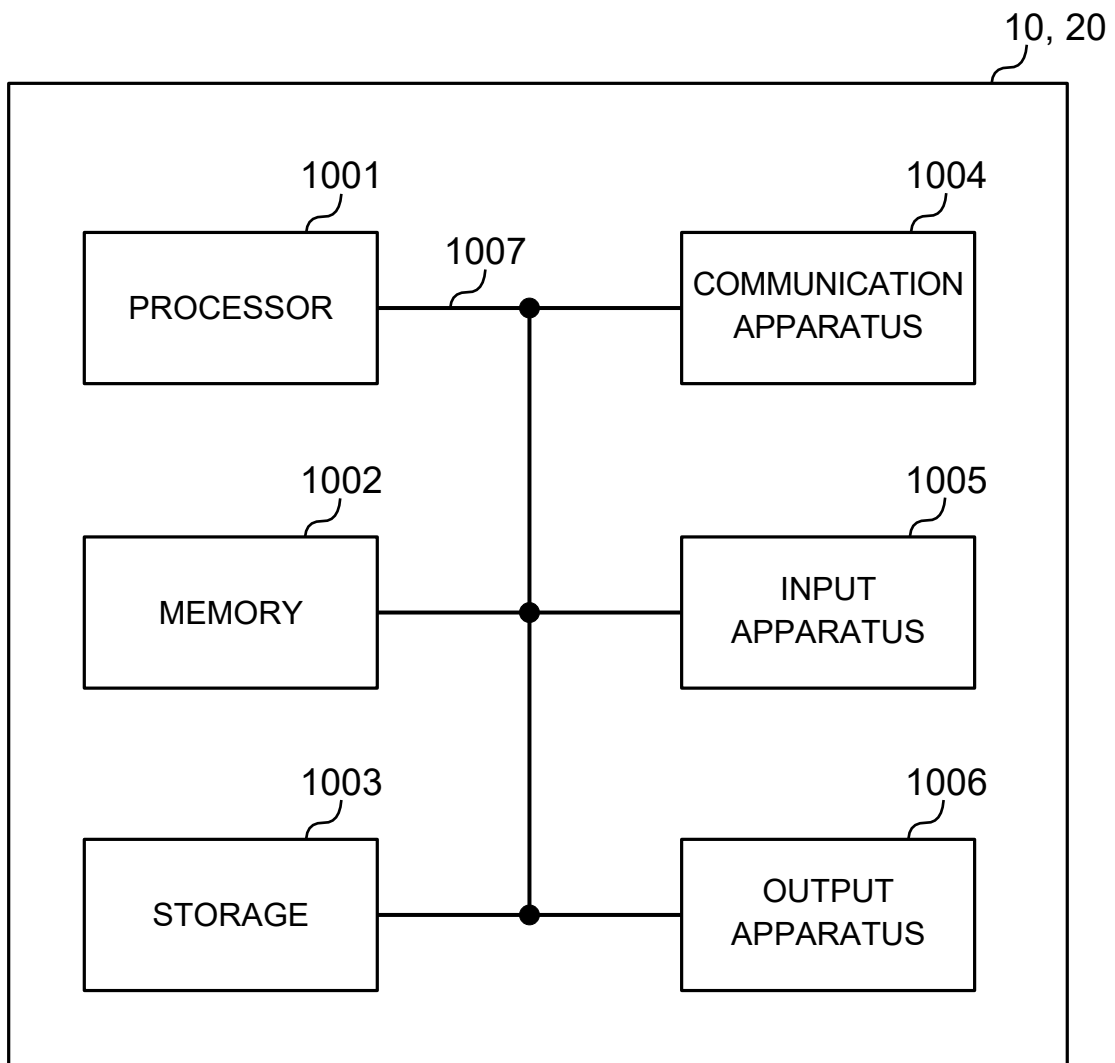


FIG. 17