

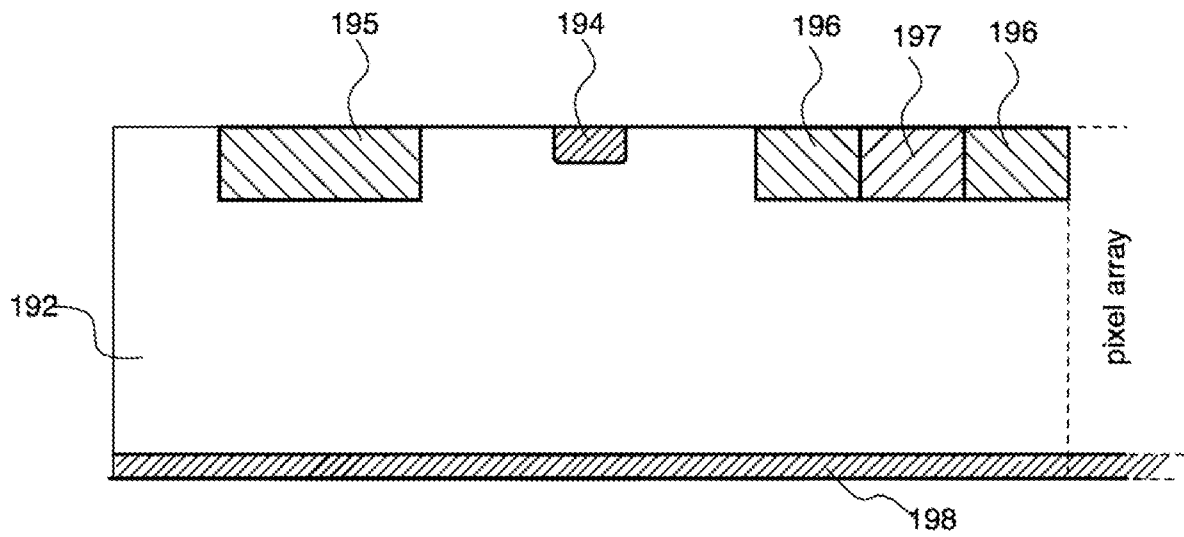


FIG. 0A (prior art)

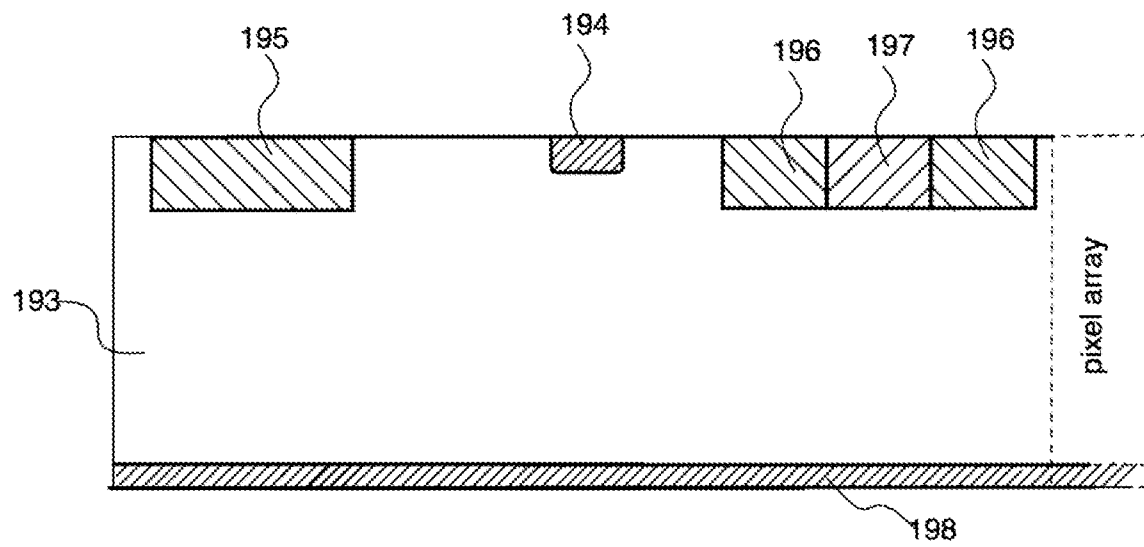


FIG. 0B (prior art)

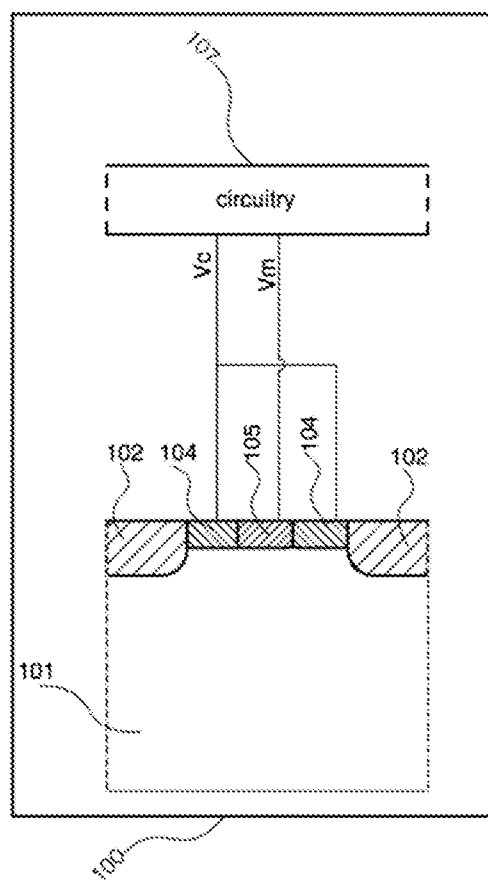


FIG. 1A

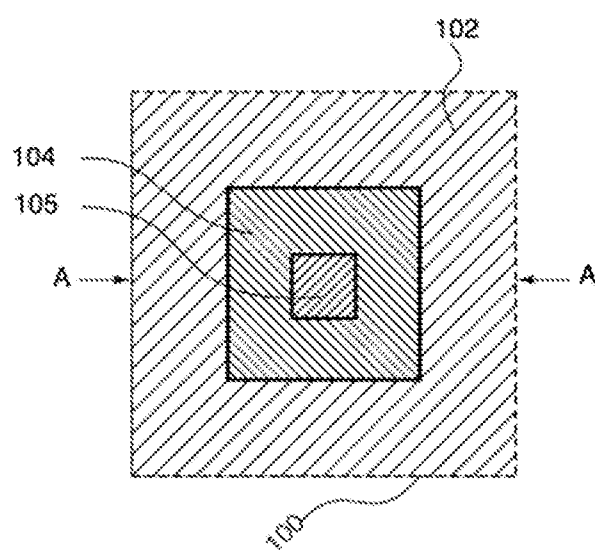


FIG. 1B

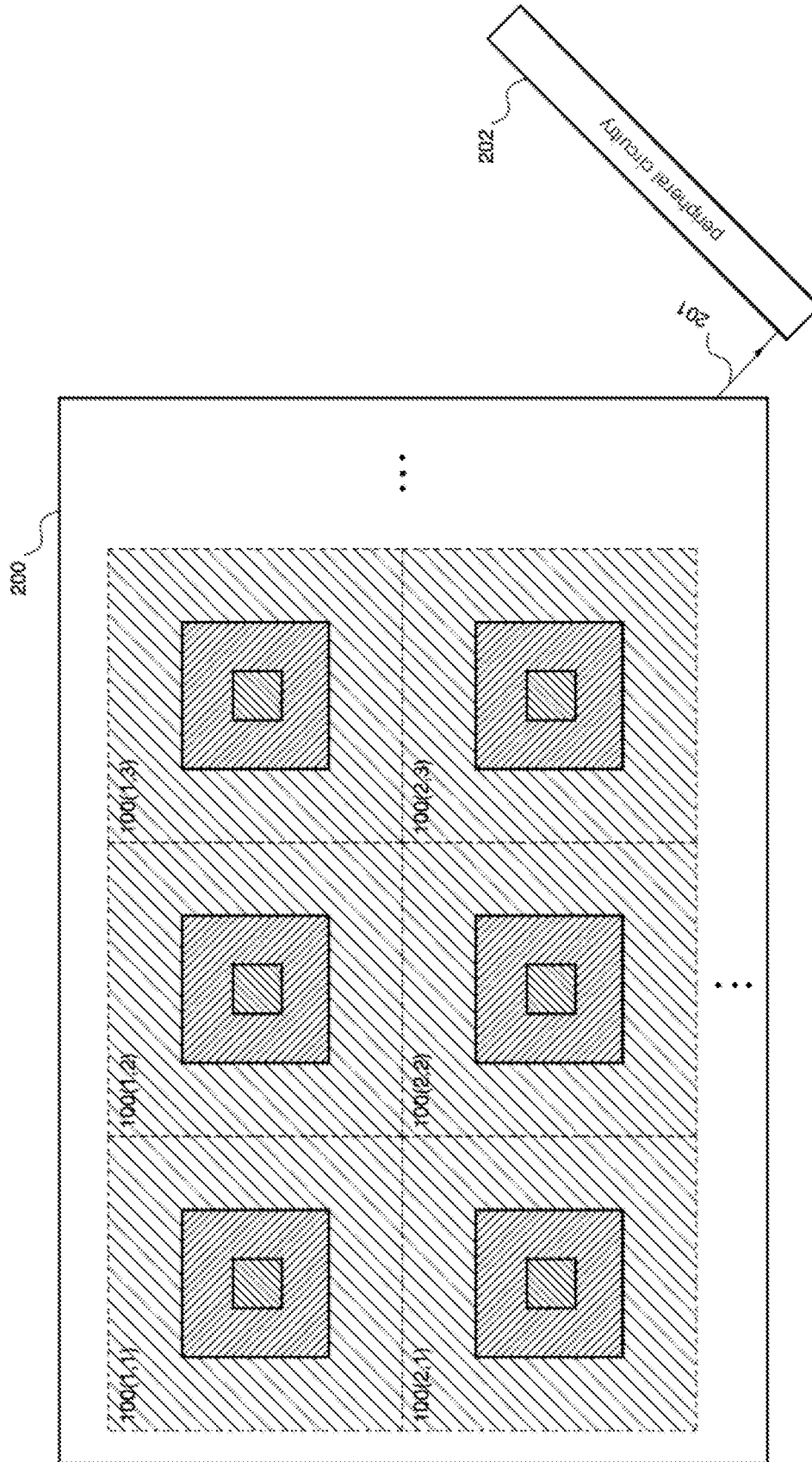


FIG. 1C

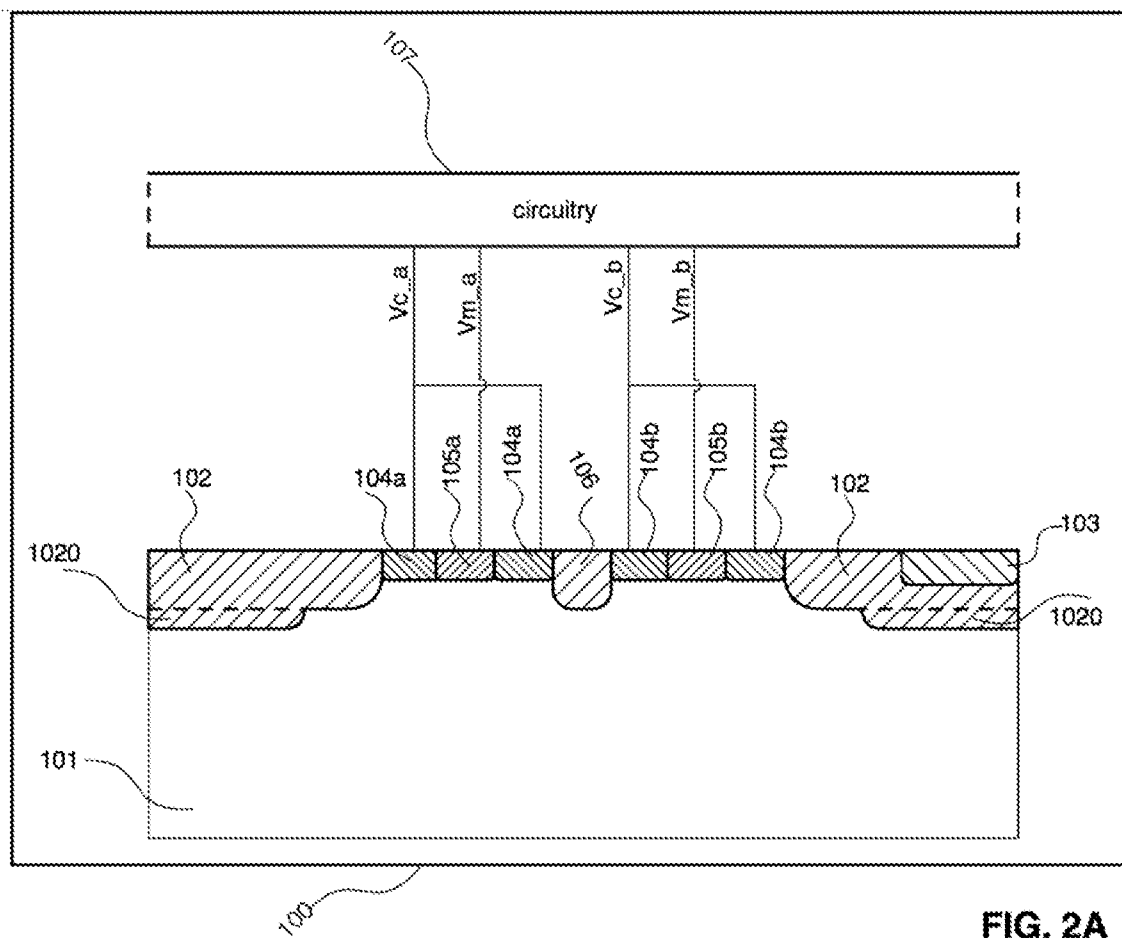


FIG. 2A

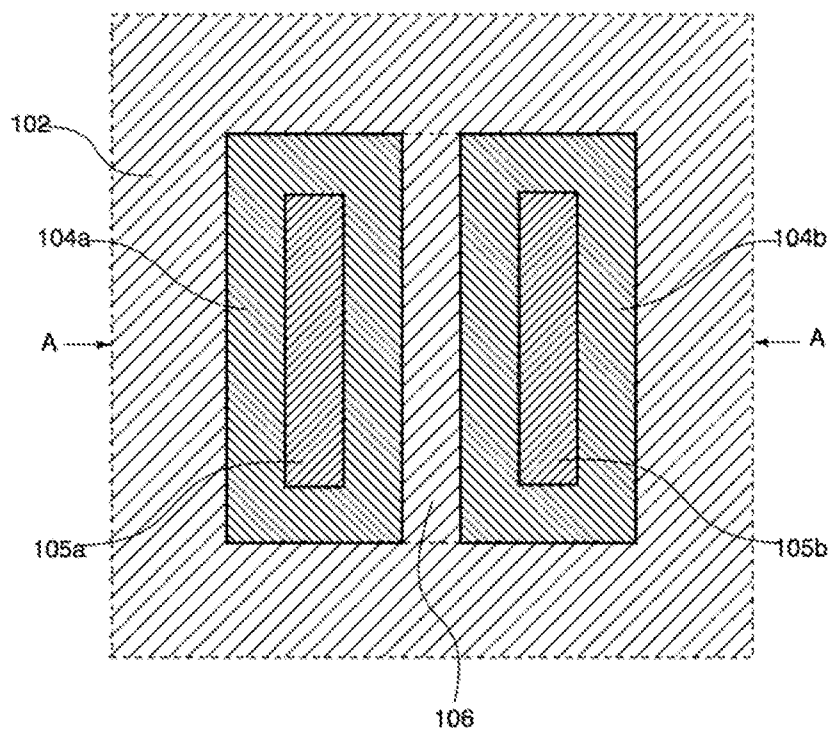


FIG. 2B

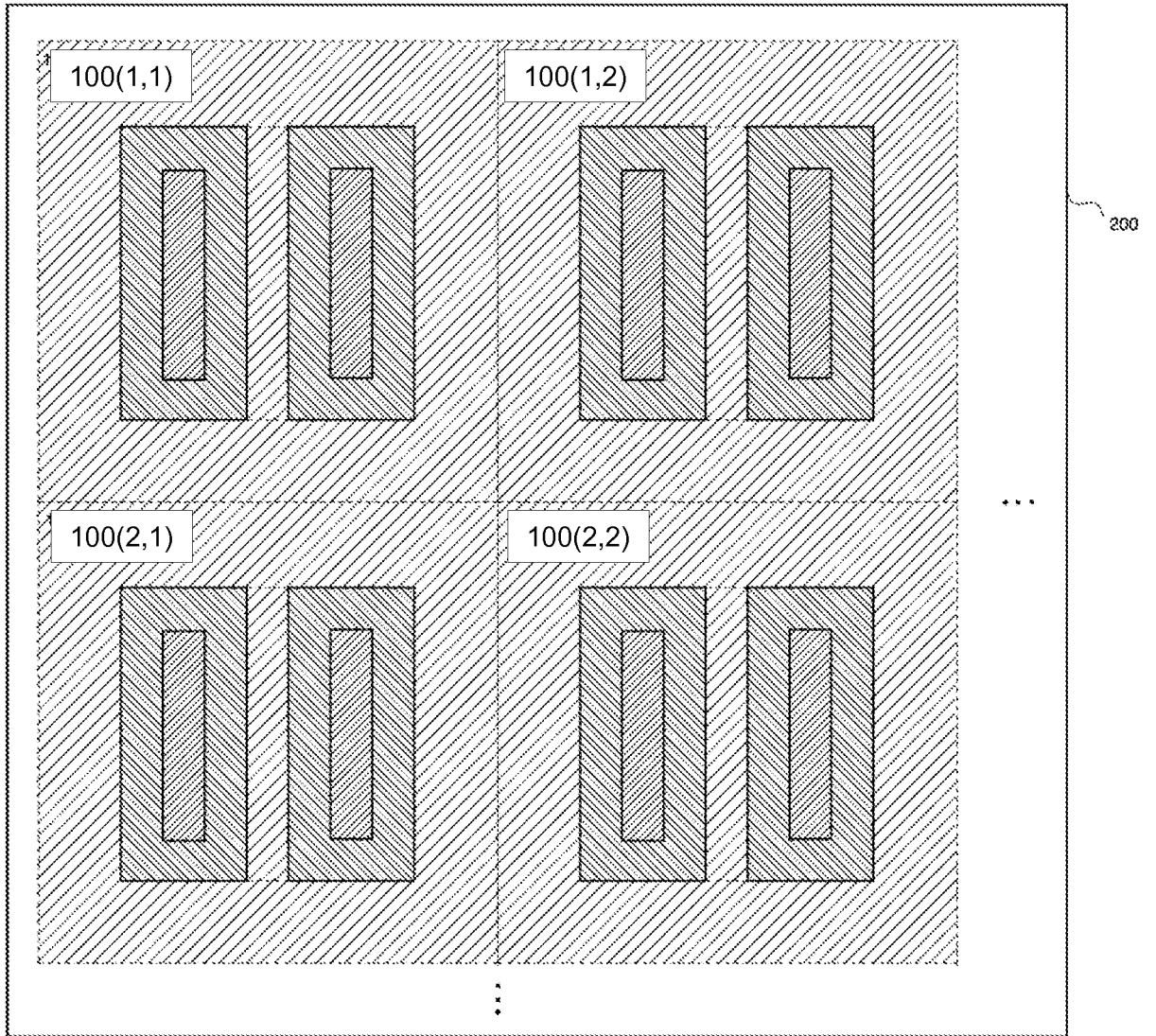


FIG. 3

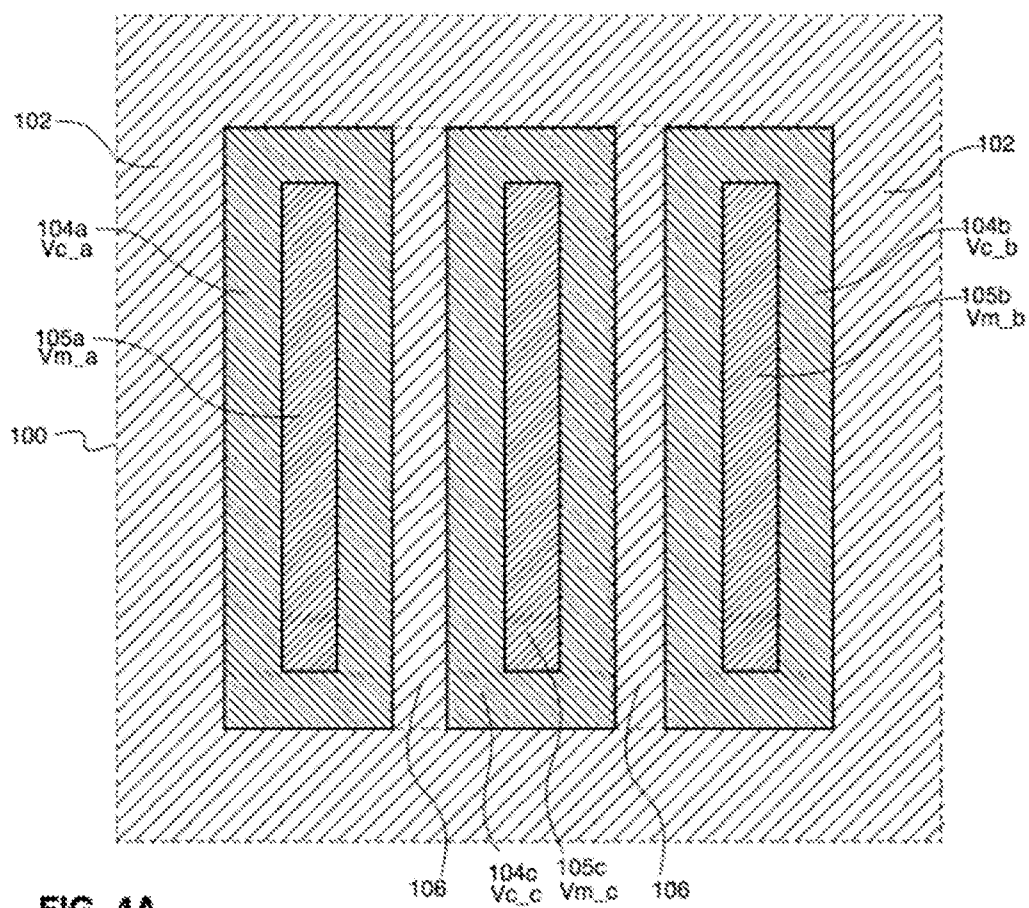
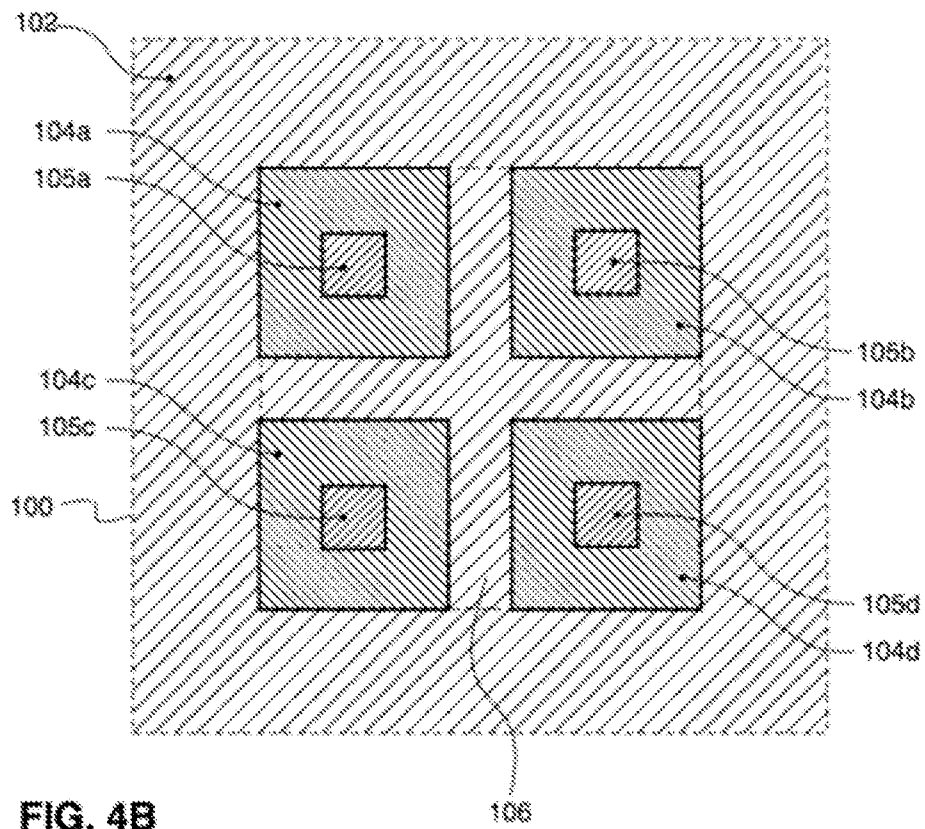


FIG. 4A



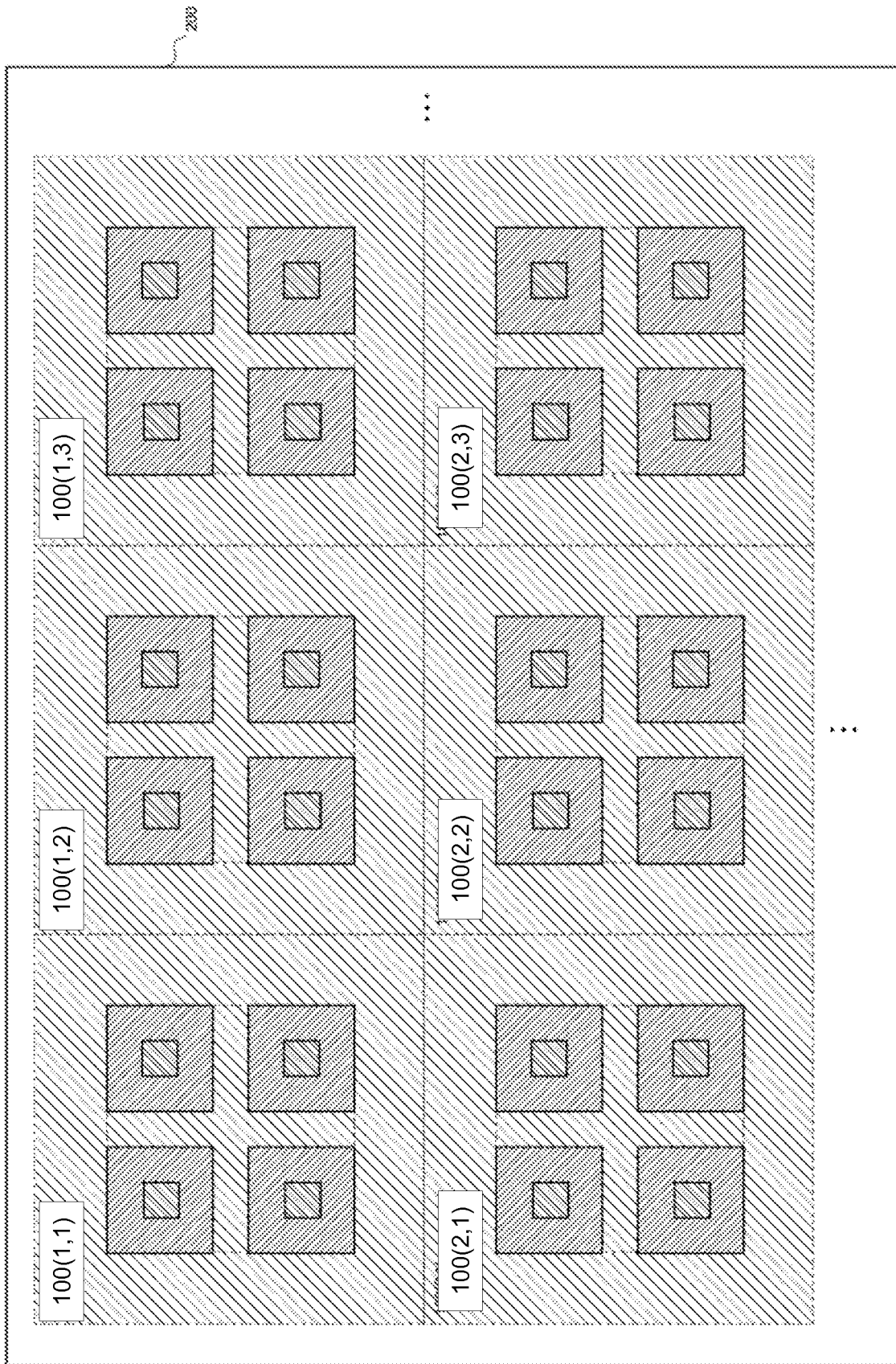


FIG. 4C

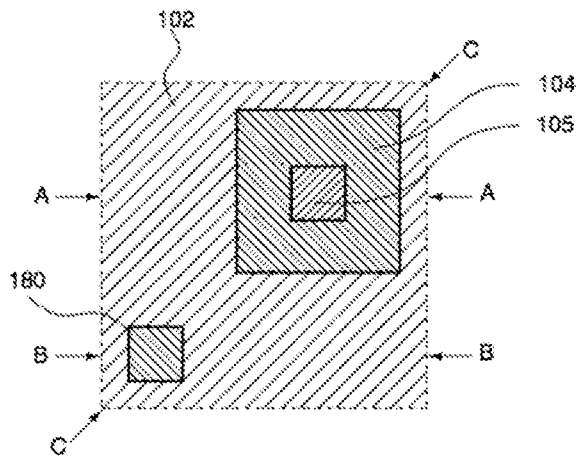


FIG. 5A

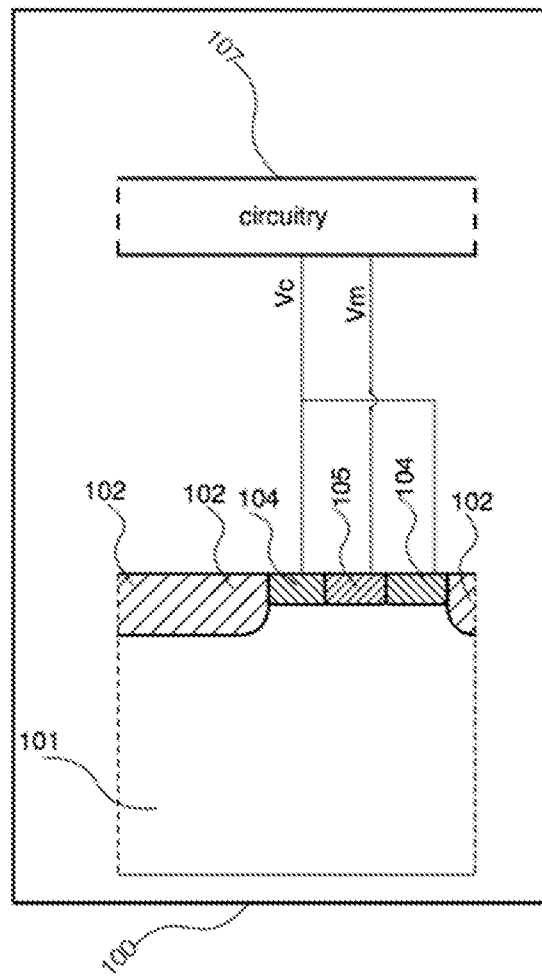


FIG. 5B

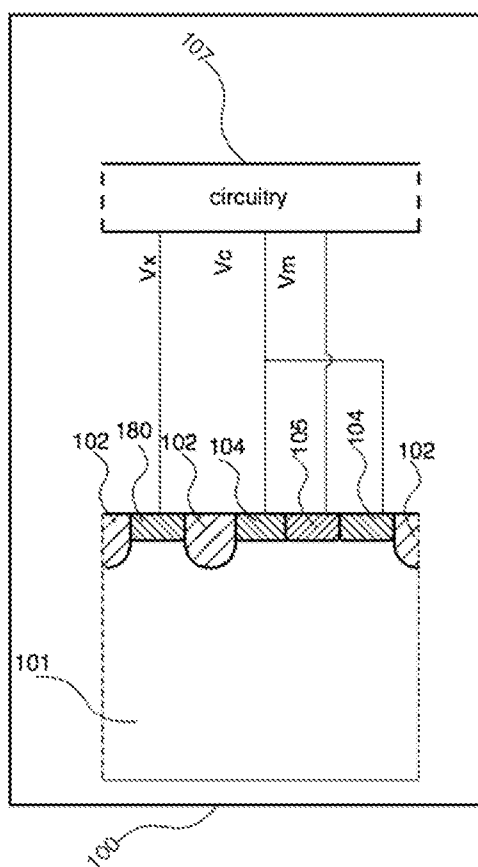


FIG. 5C

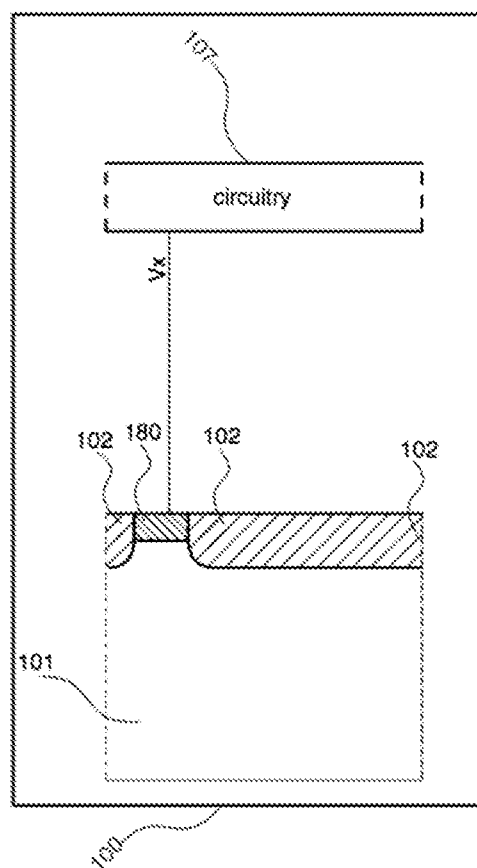


FIG. 5D

200

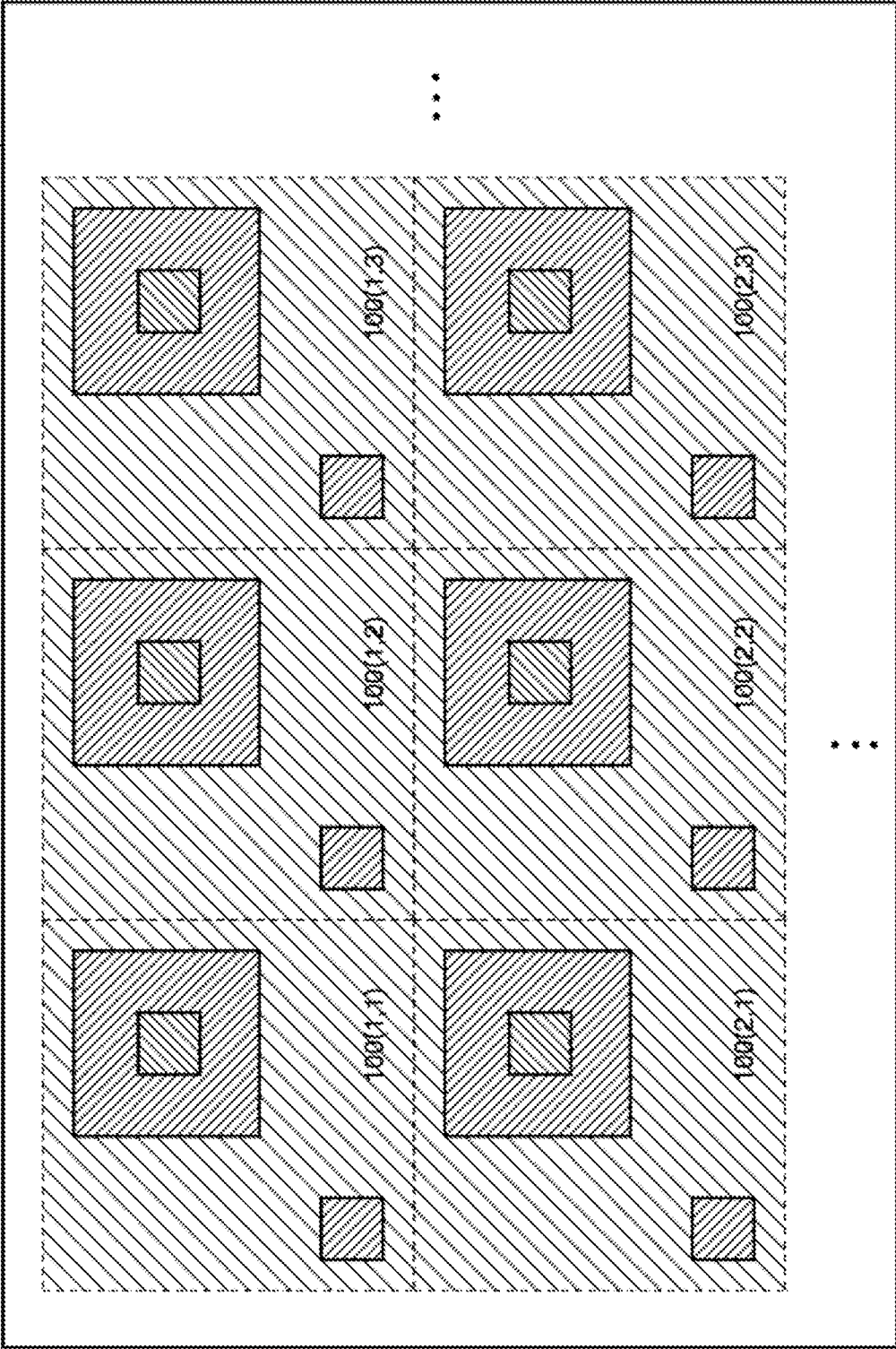


FIG. 6

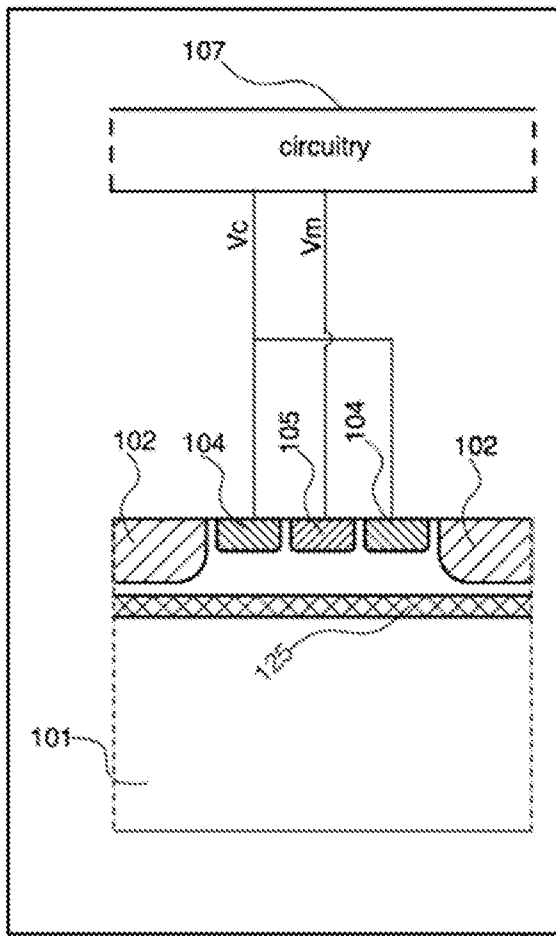


FIG. 7A

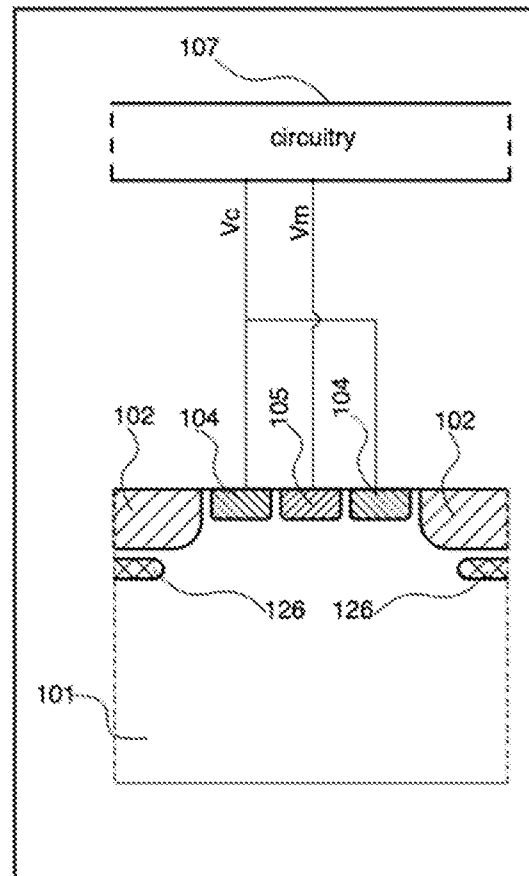


FIG. 7B

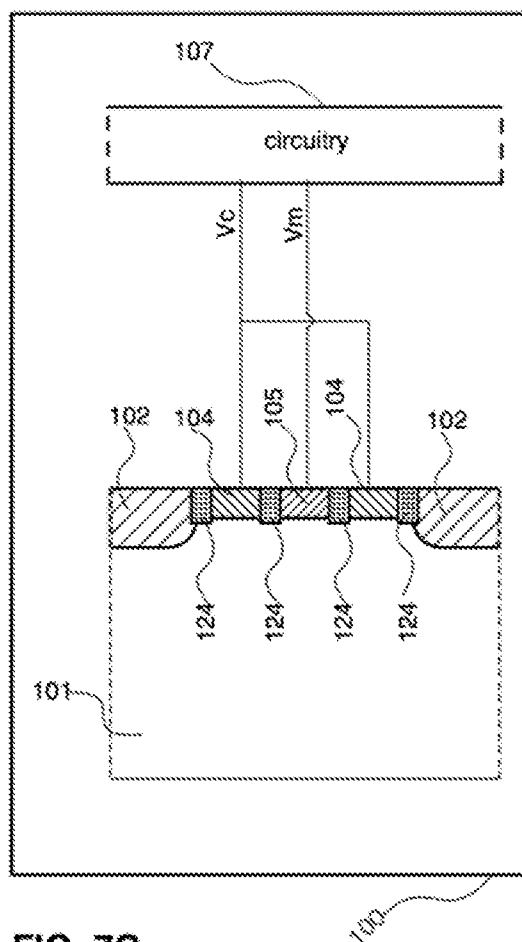


FIG. 7C

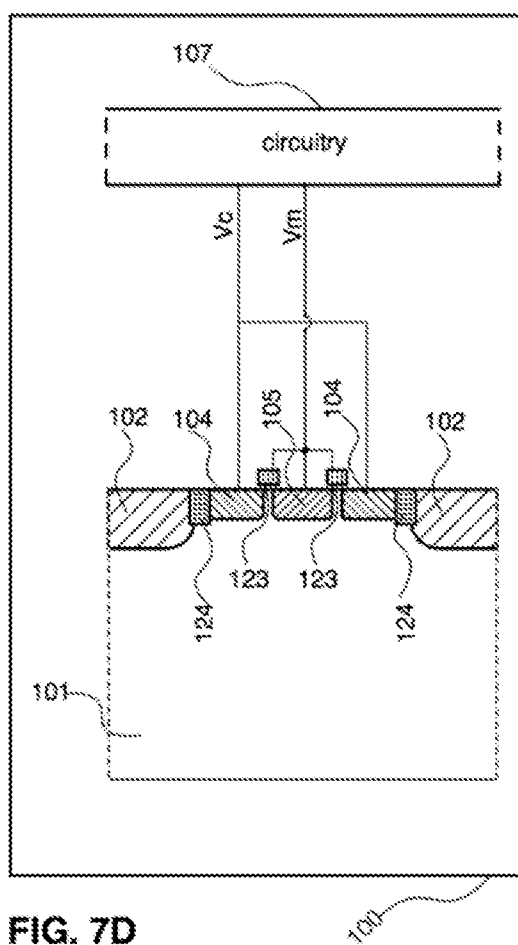
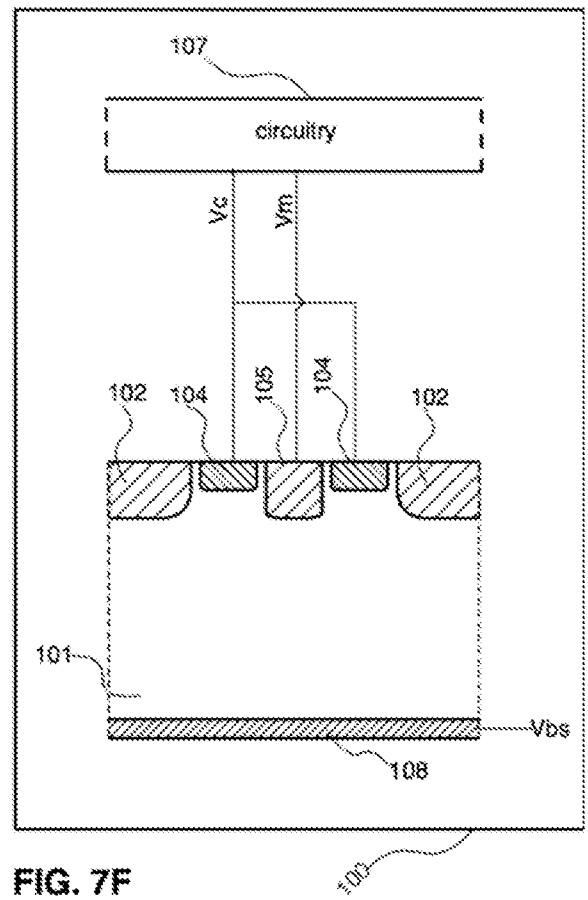
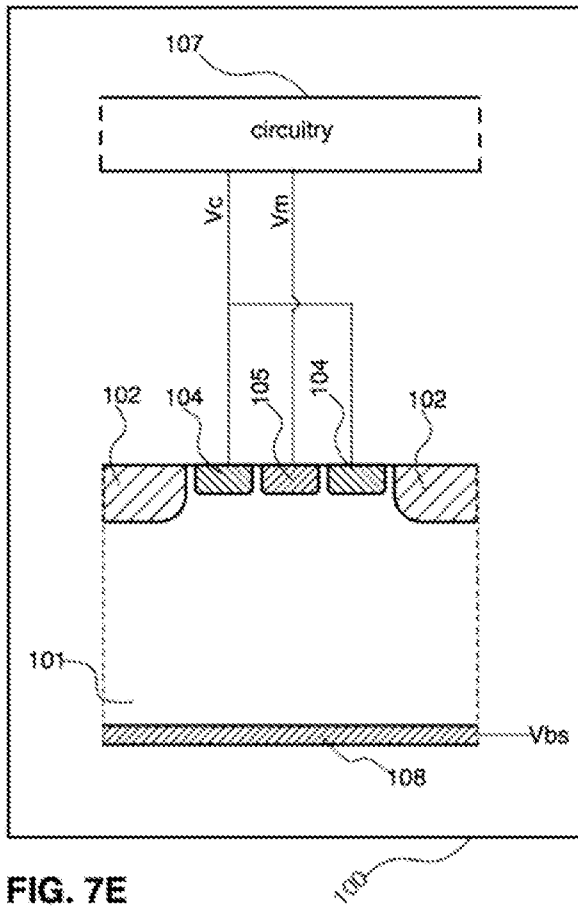


FIG. 7D



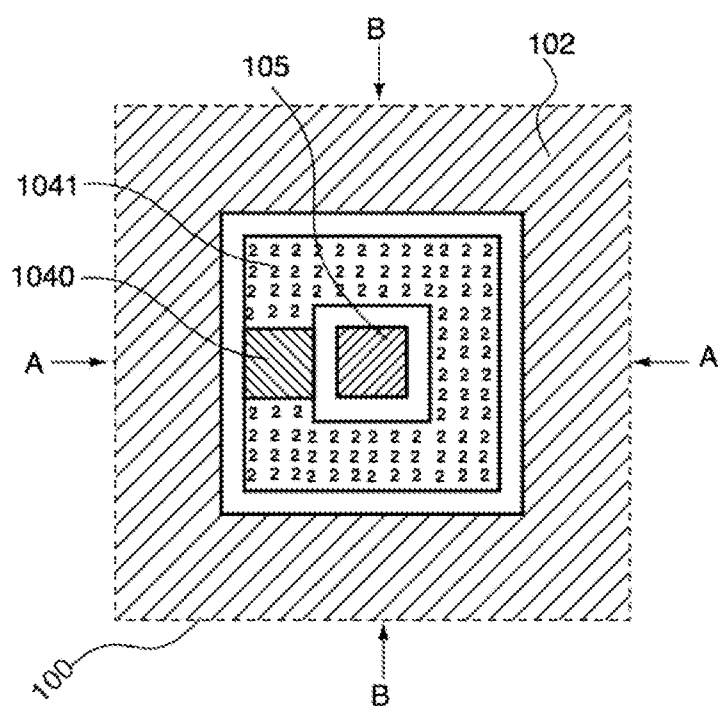


FIG. 7G

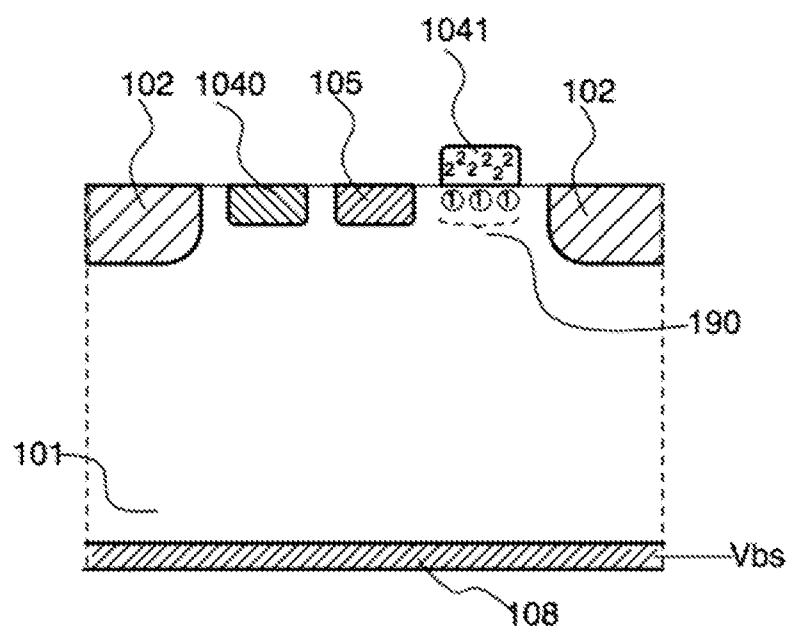


FIG. 7H

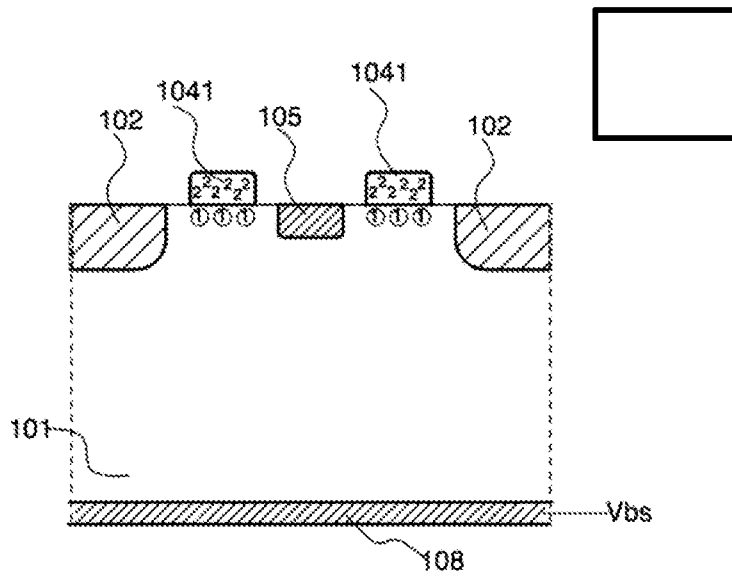


FIG. 7J

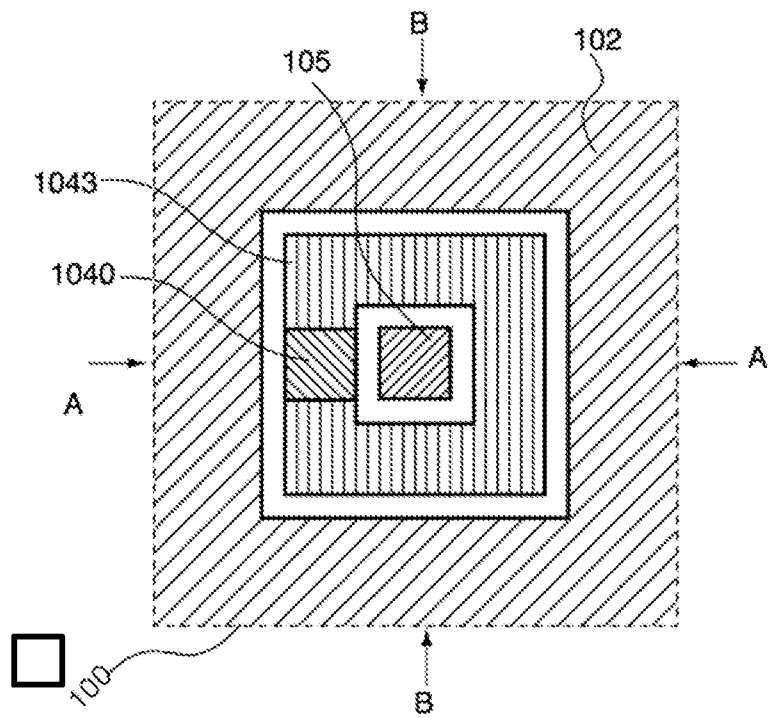


FIG. 7K

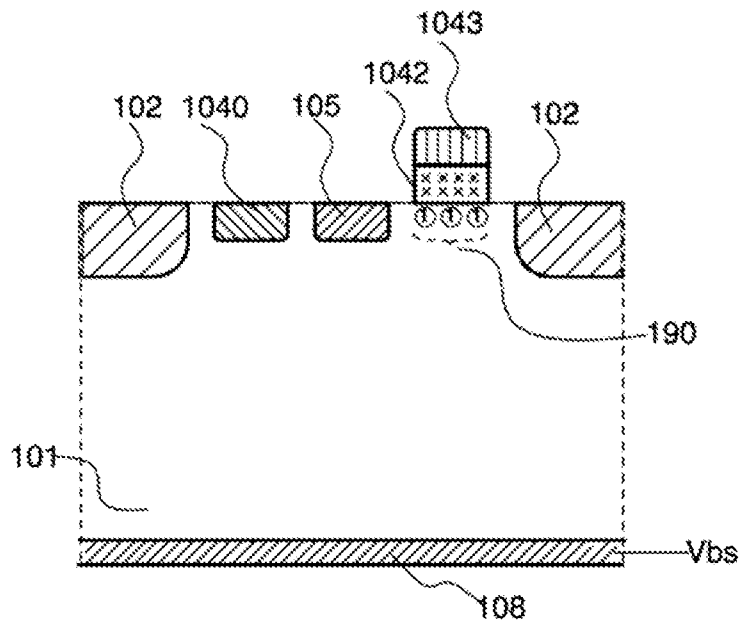


FIG. 7L

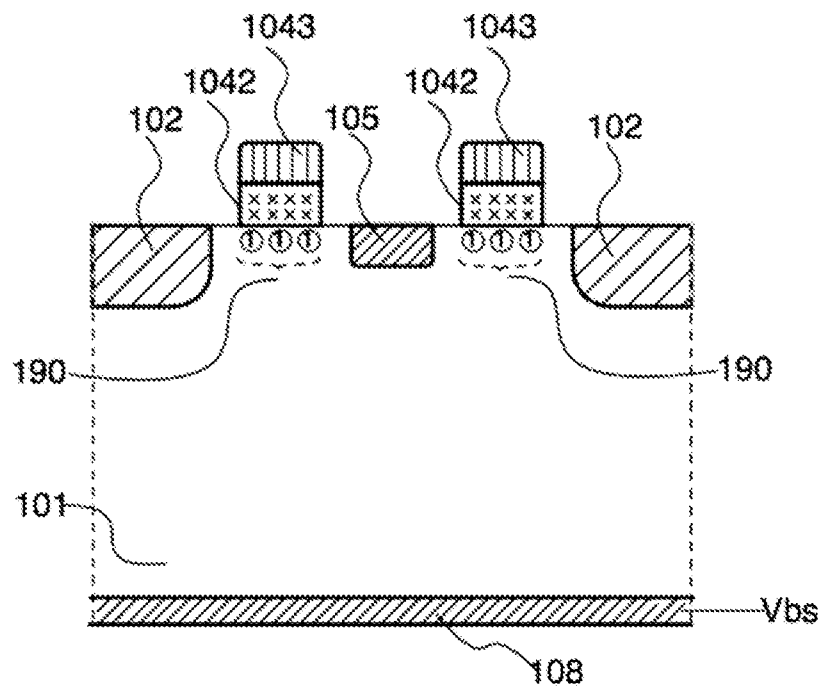


FIG. 7M

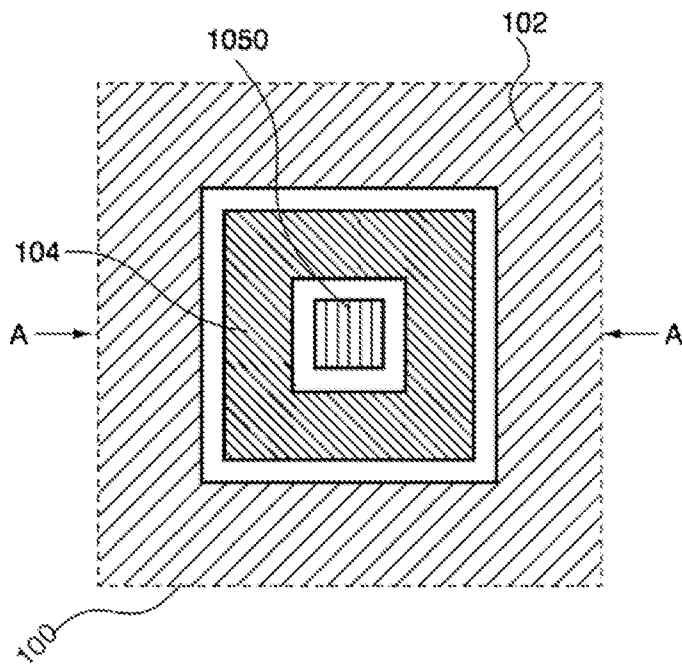


FIG. 7N

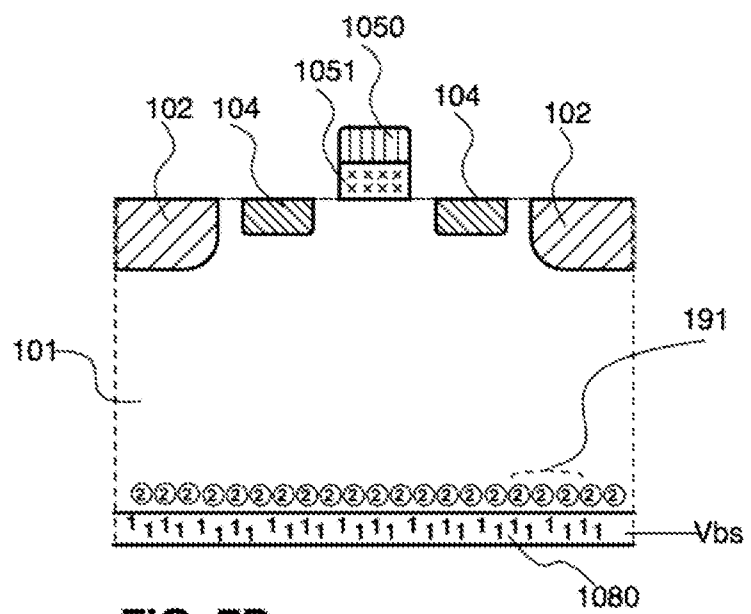


FIG. 7P

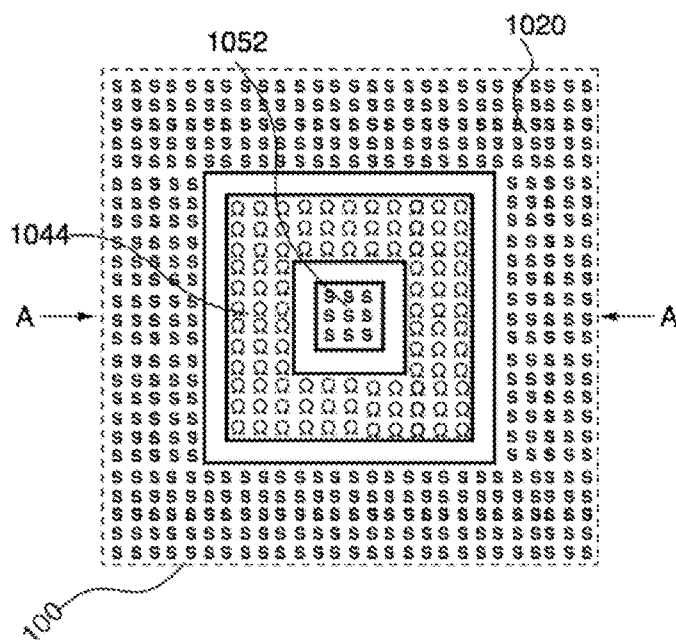


FIG. 7Q

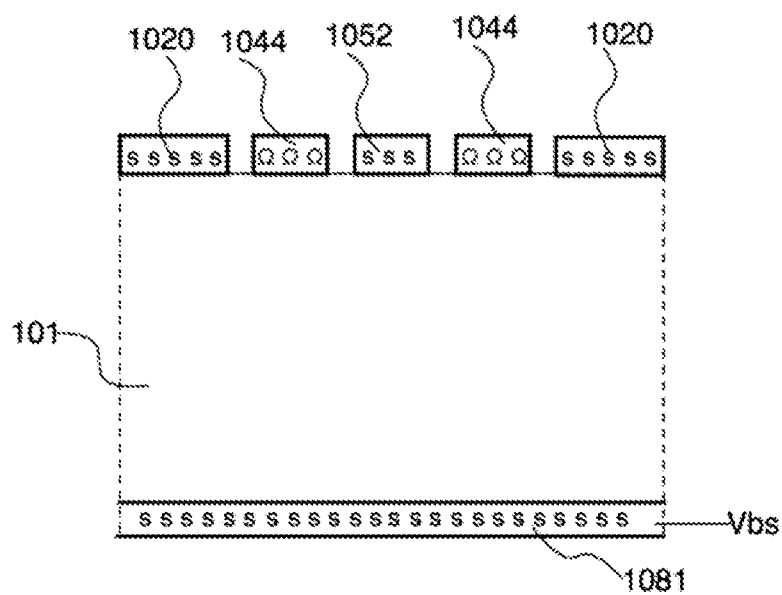


FIG. 7R

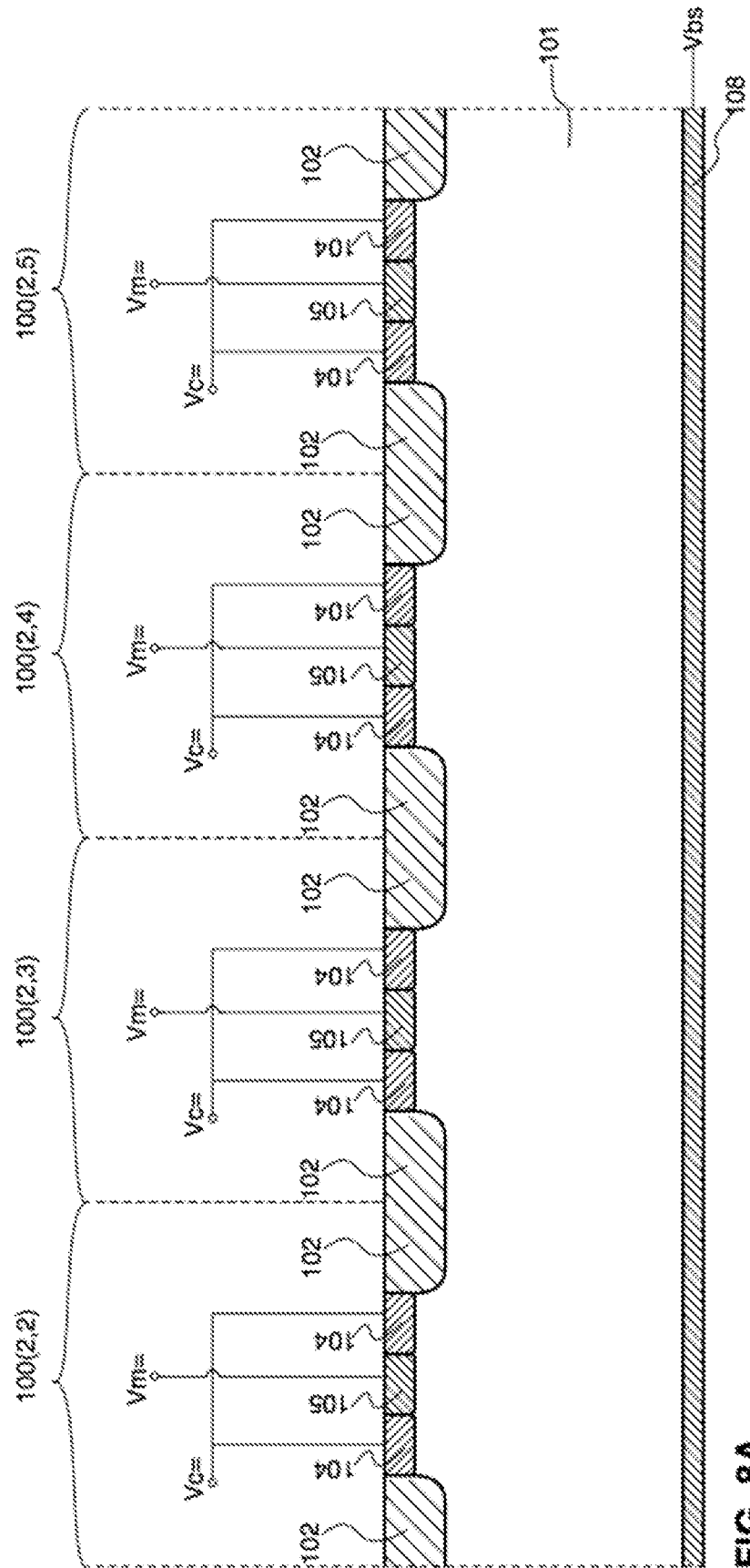


FIG. 8A

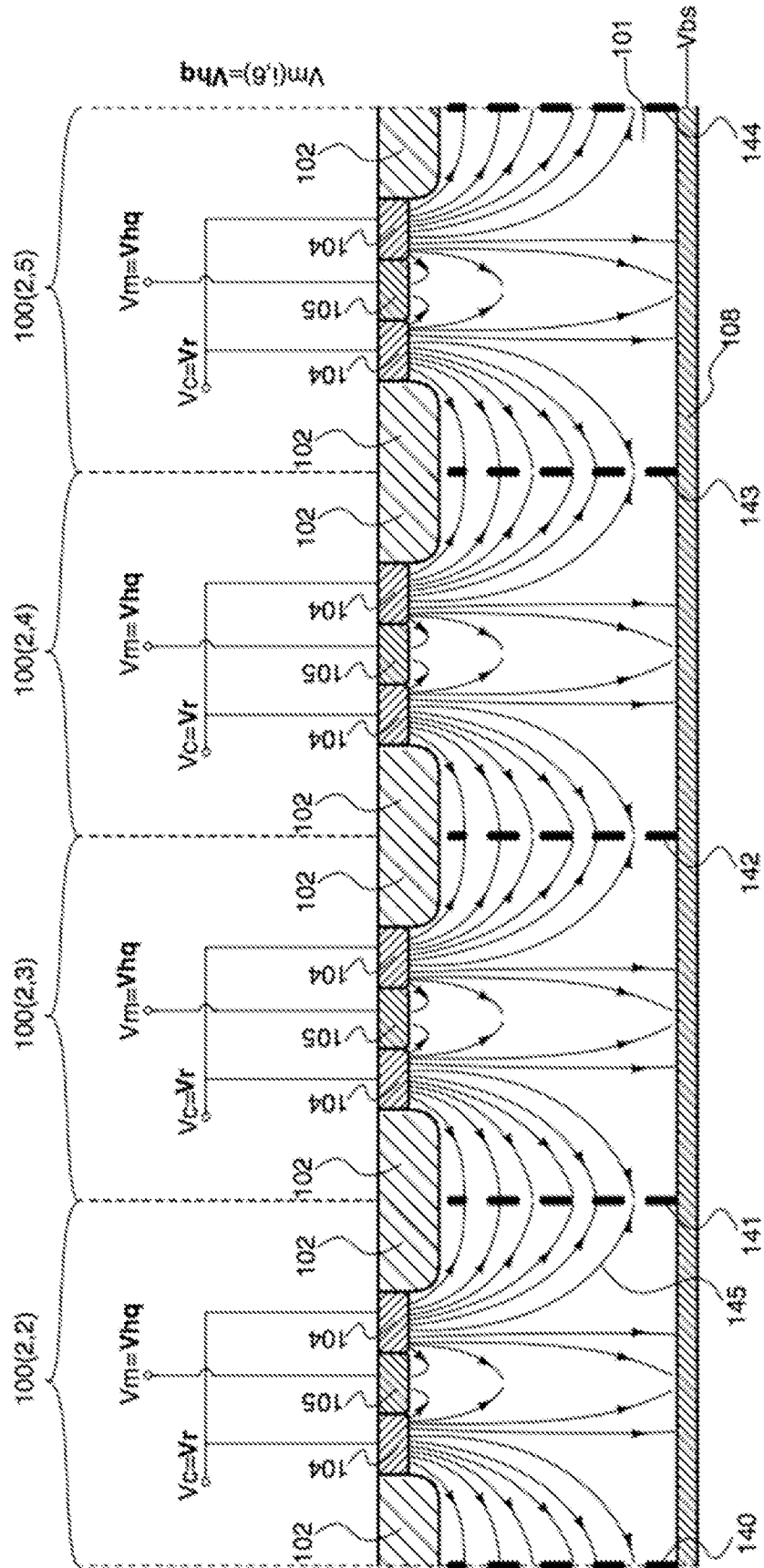


FIG. 8B



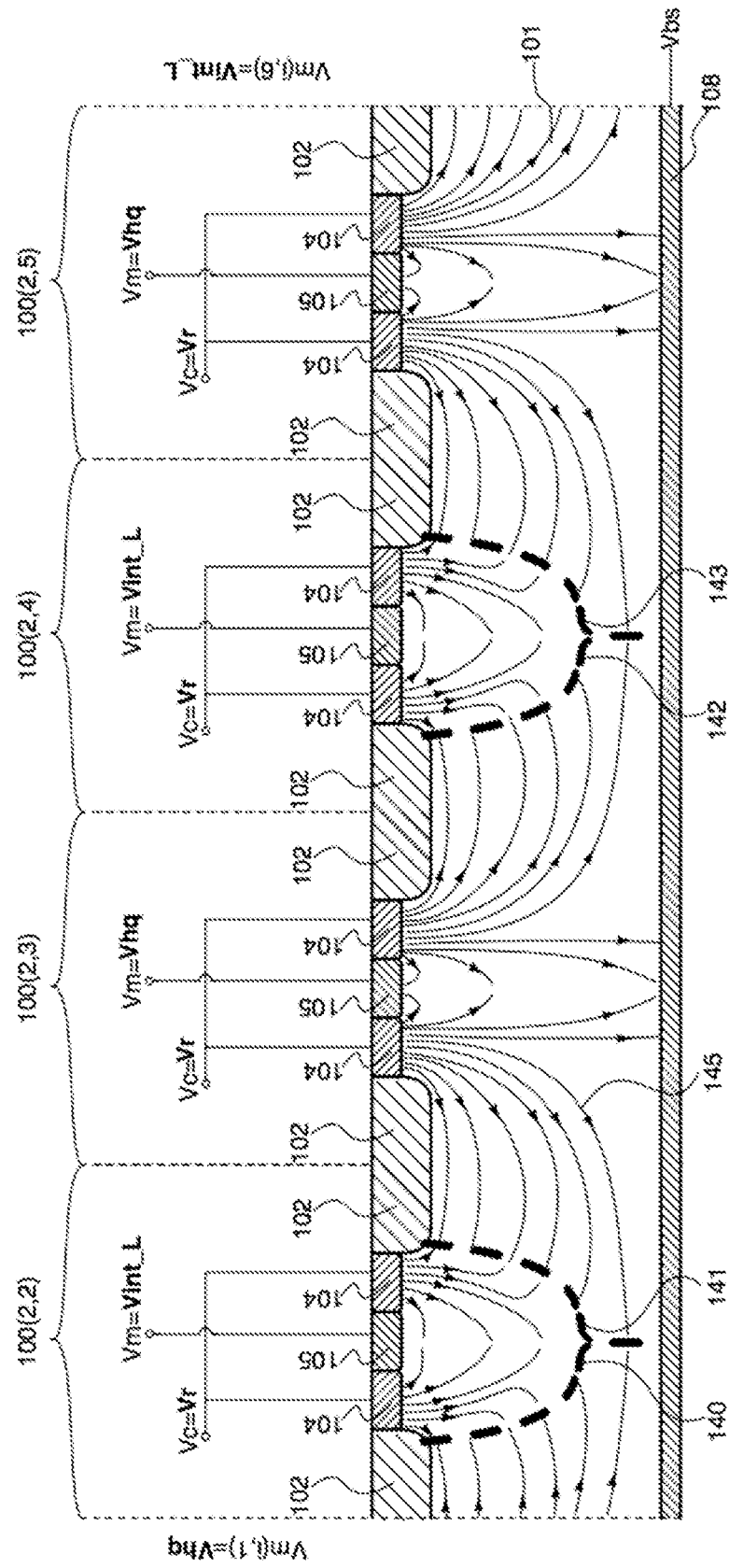
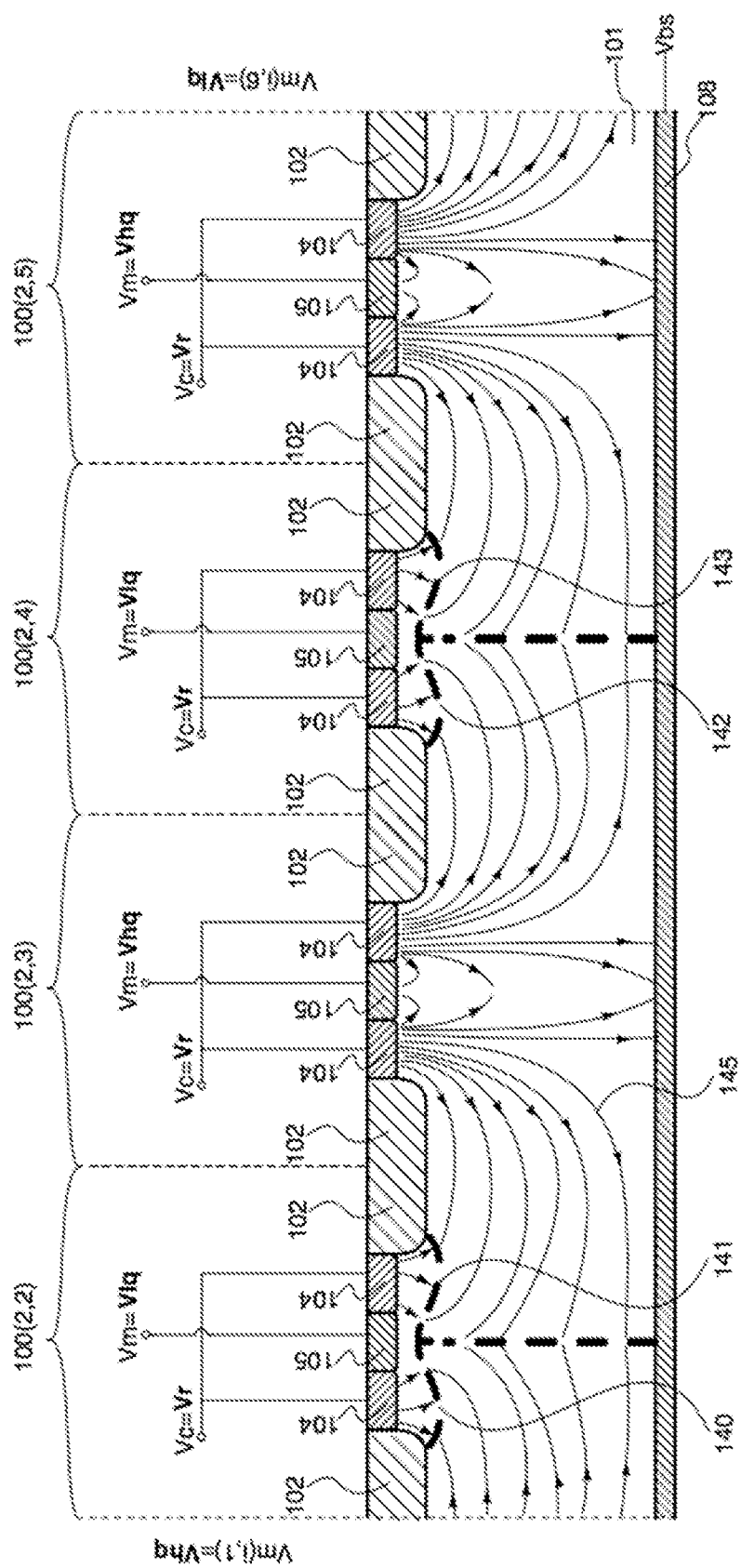
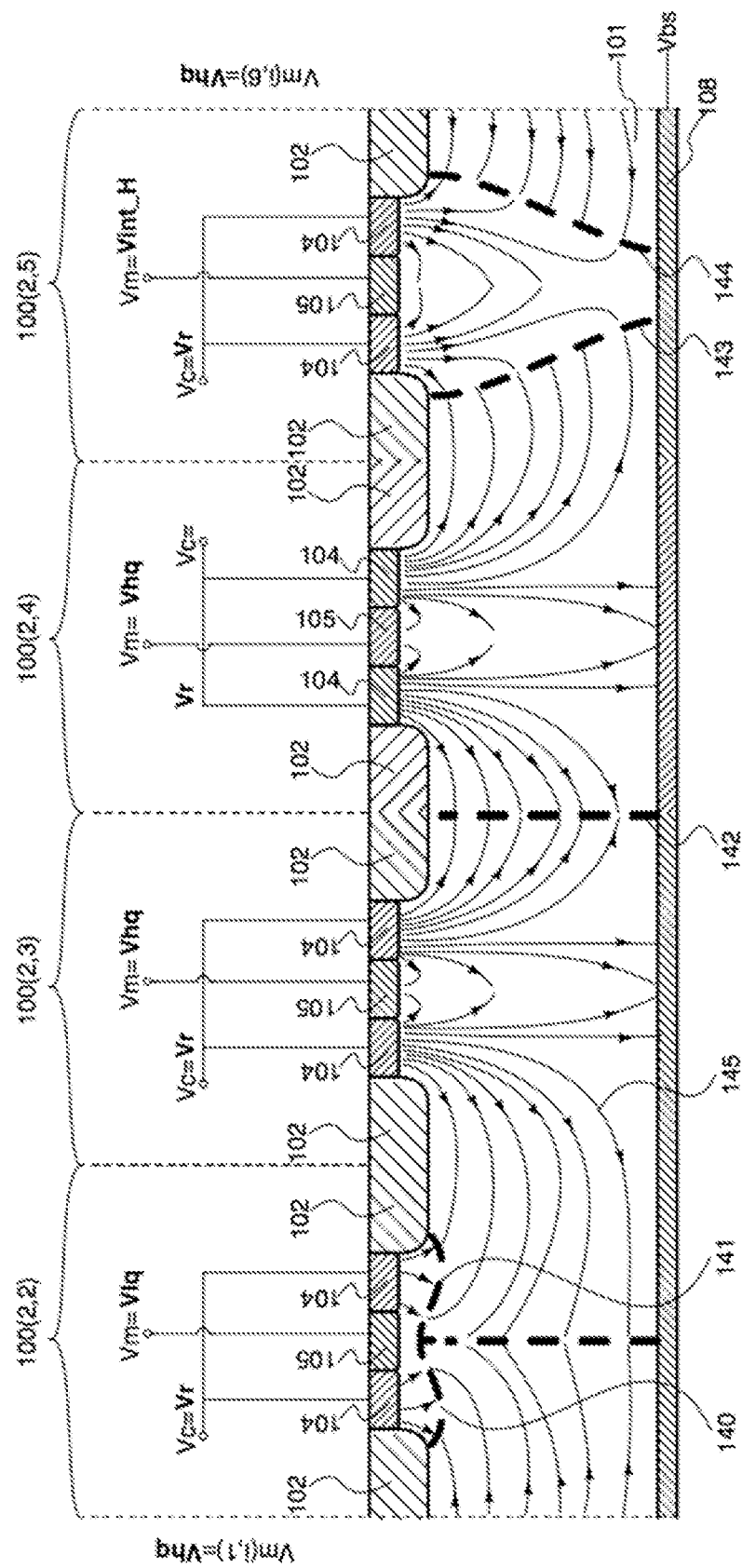


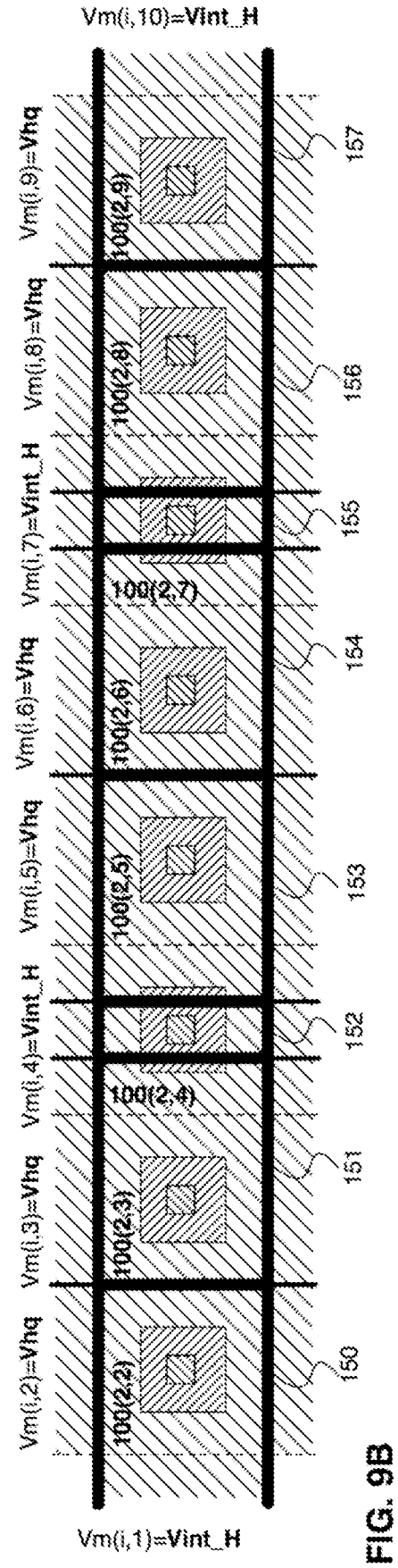
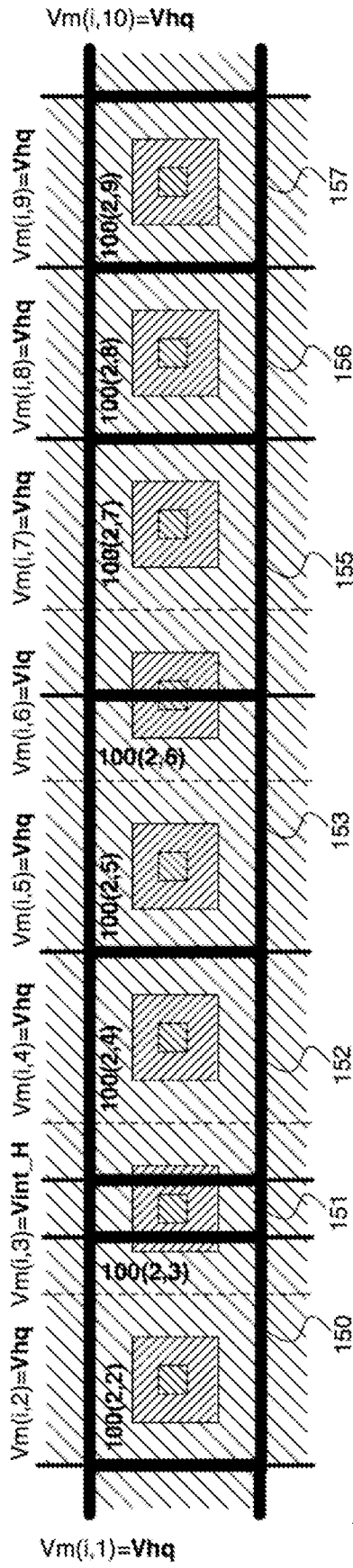
FIG. 8D



W  
O  
G<sup>\*</sup>  
L



८०८३



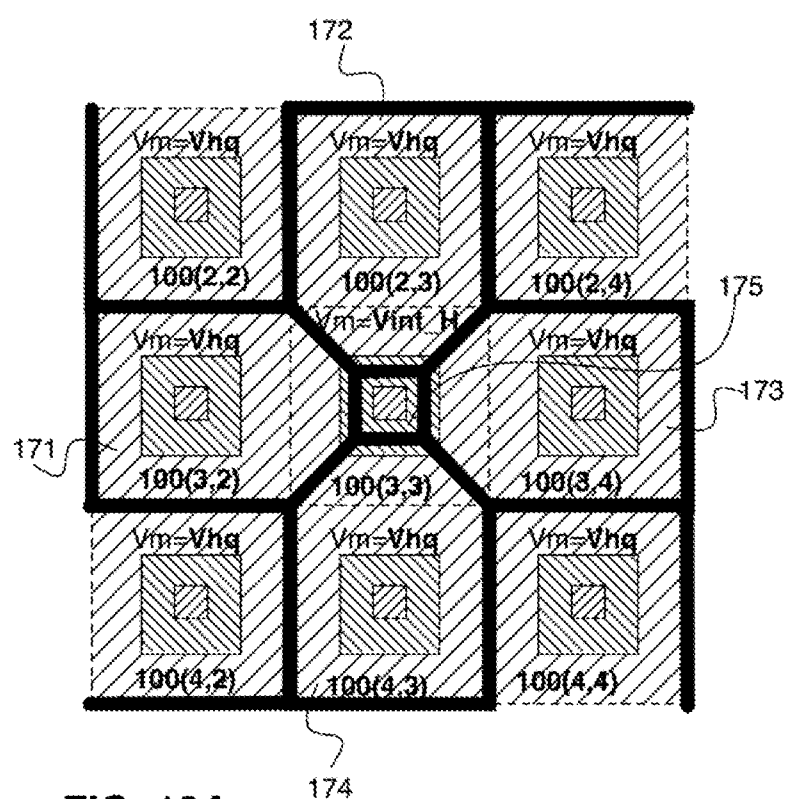


FIG. 10A

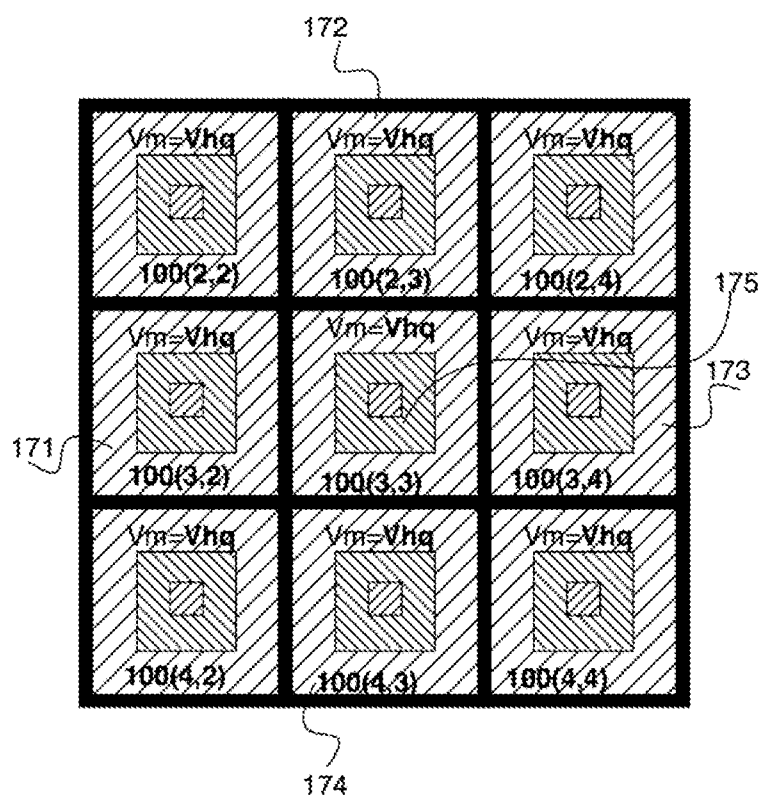


FIG. 10B

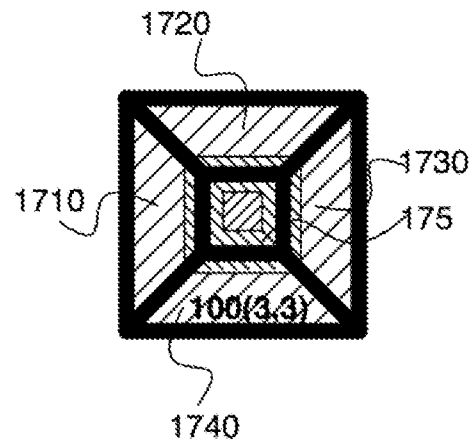


FIG. 10C

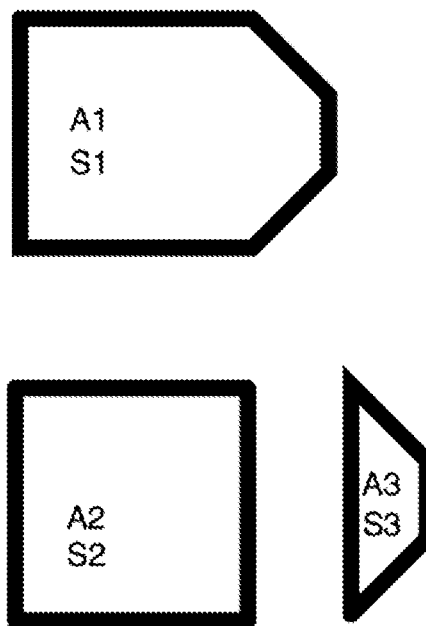


FIG. 10D

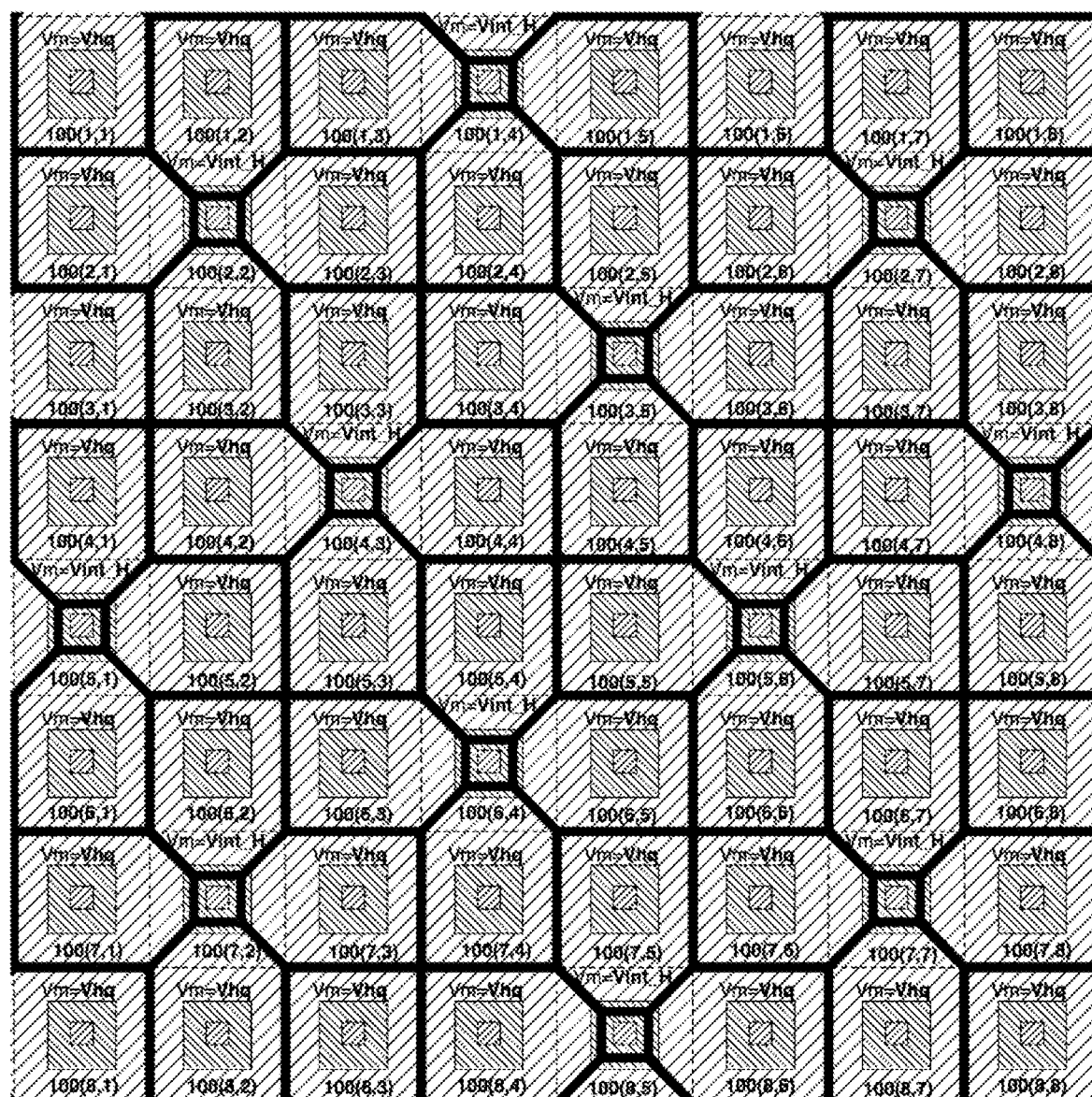


FIG. 10E

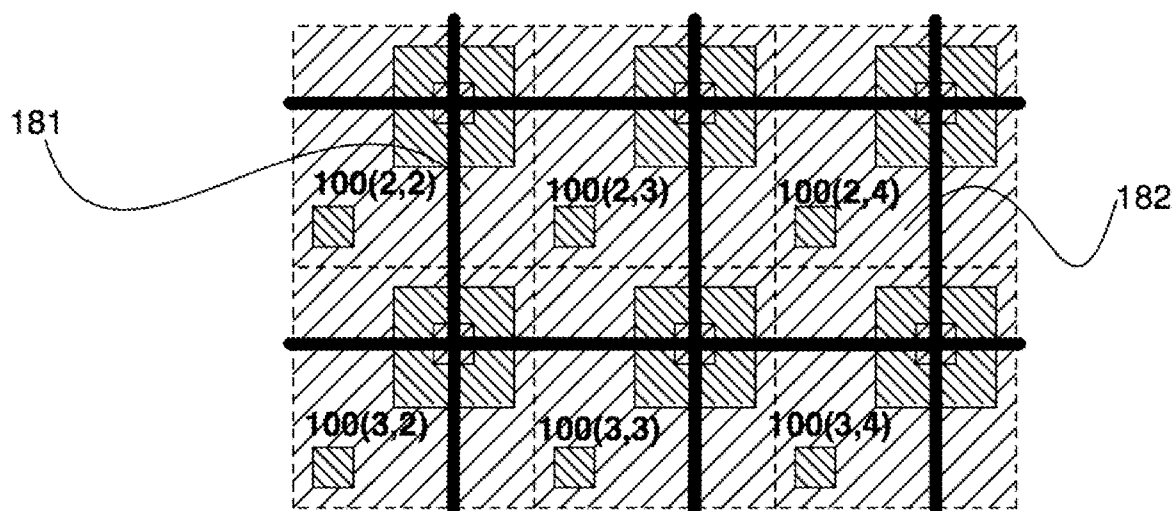


FIG. 11A

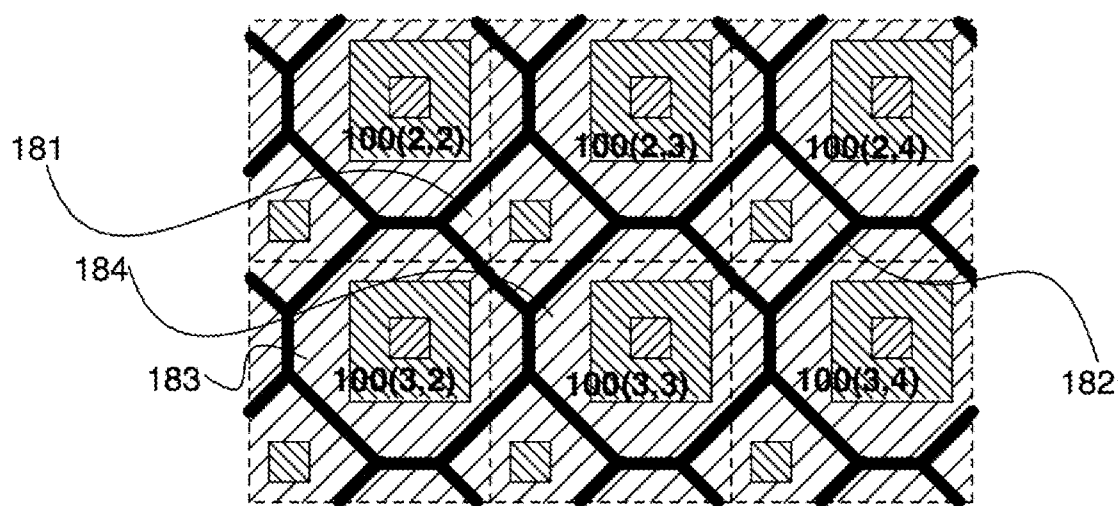


FIG. 11B

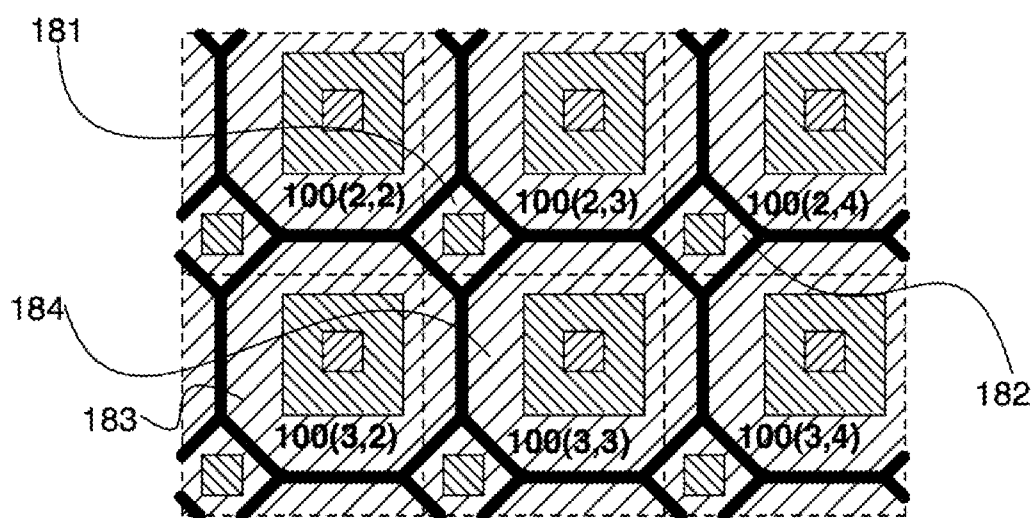


FIG. 11C

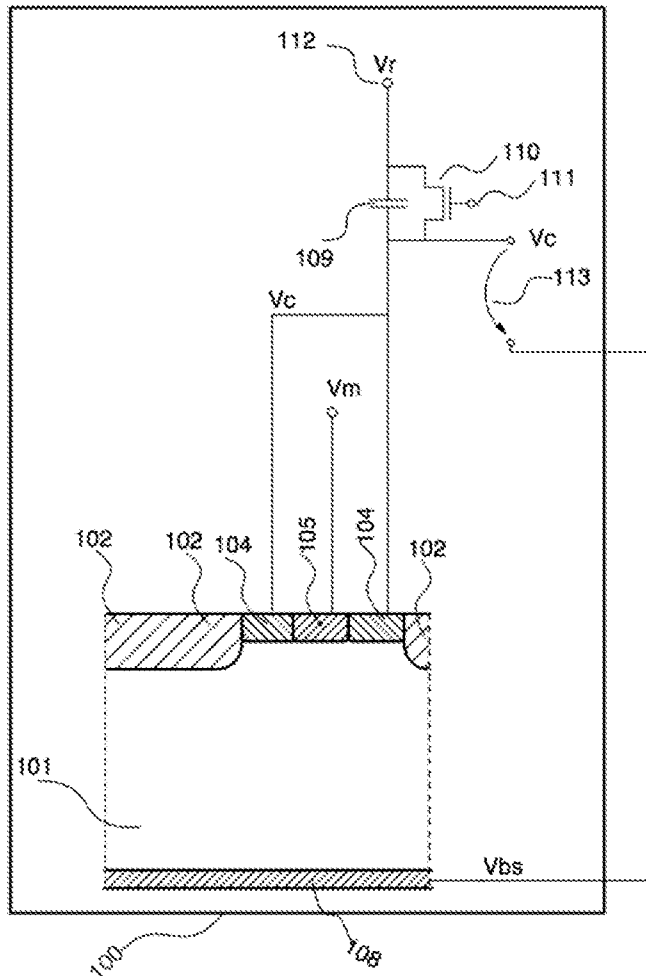


FIG. 12A

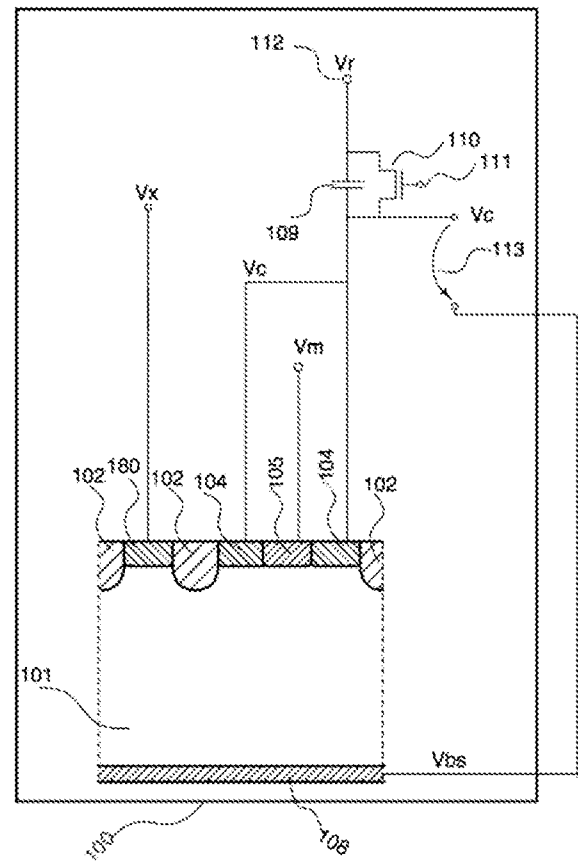


FIG. 12B

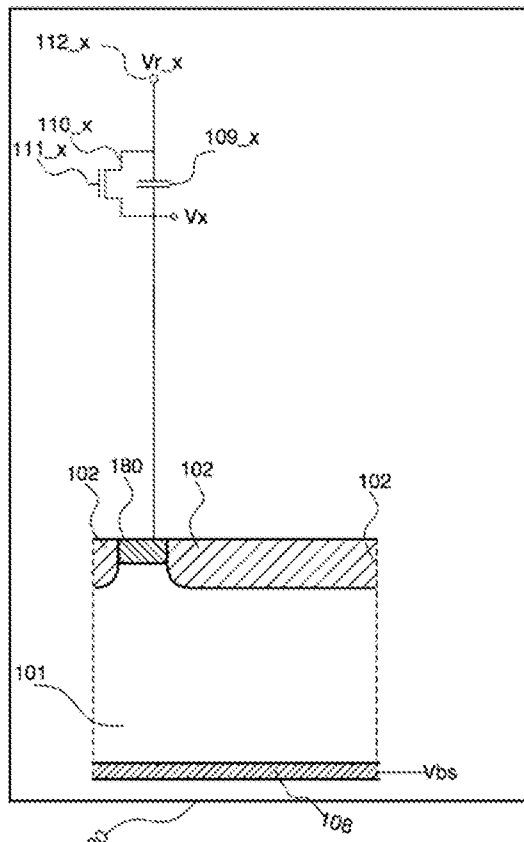


FIG. 12C

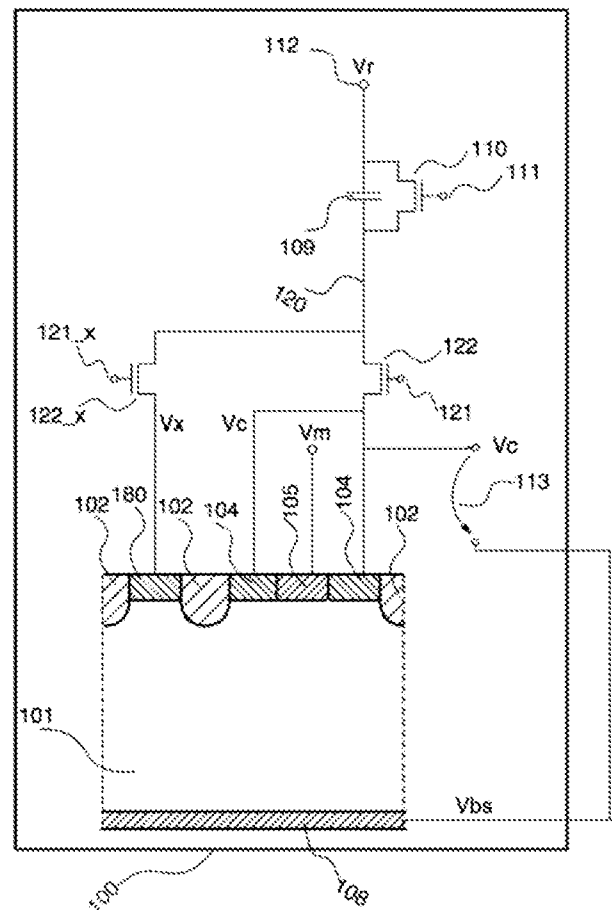


FIG. 12D

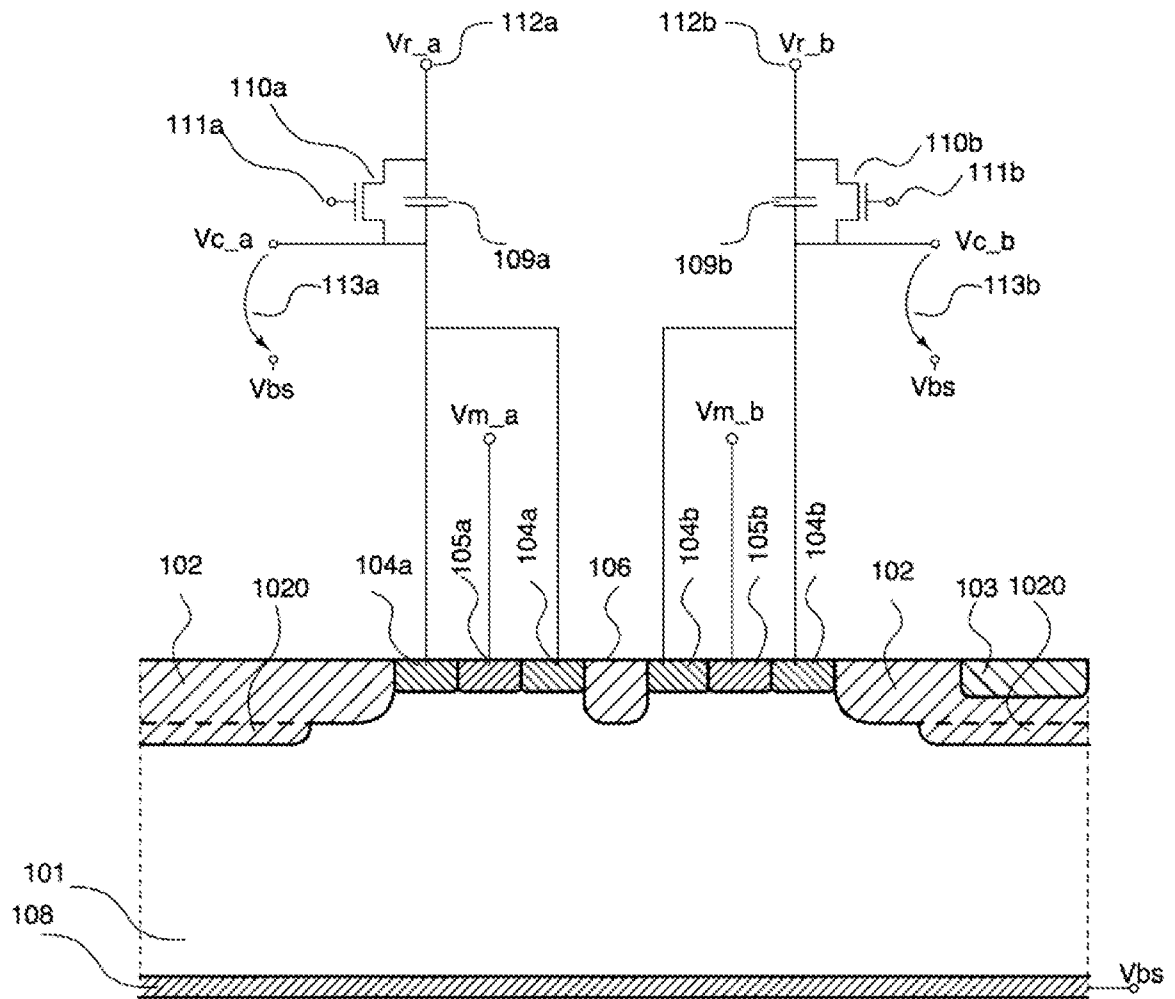


FIG. 13

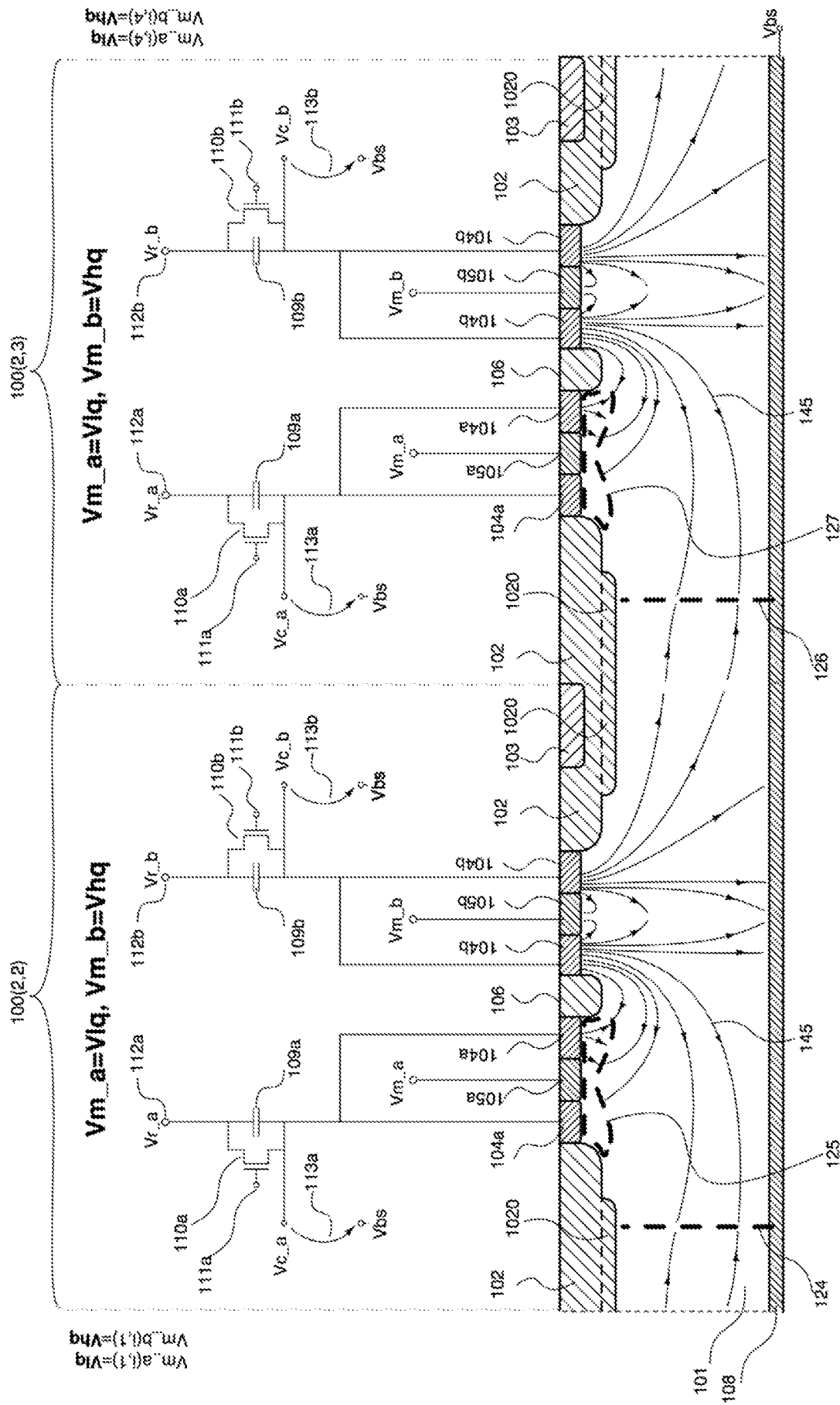


FIG. 14A



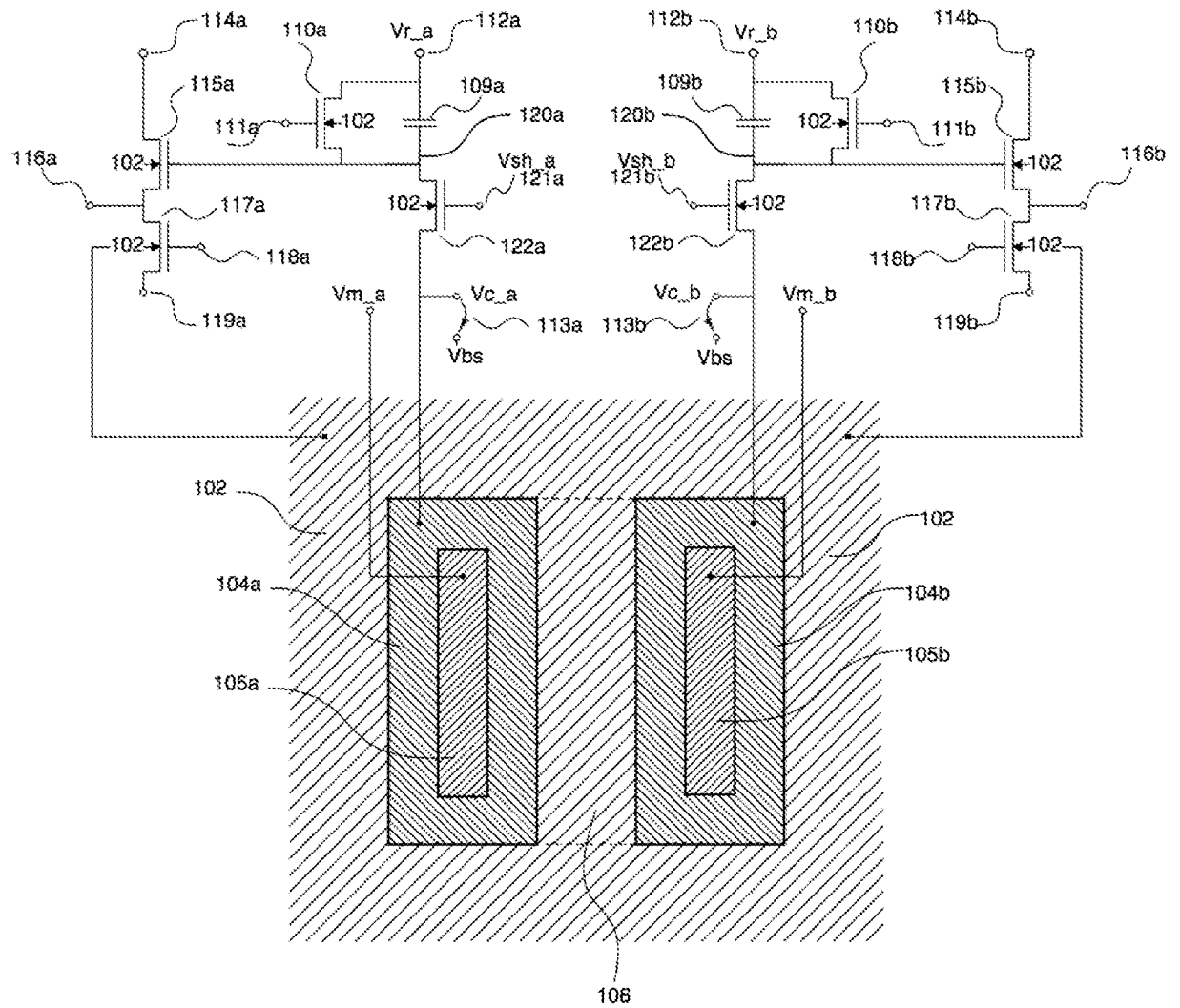
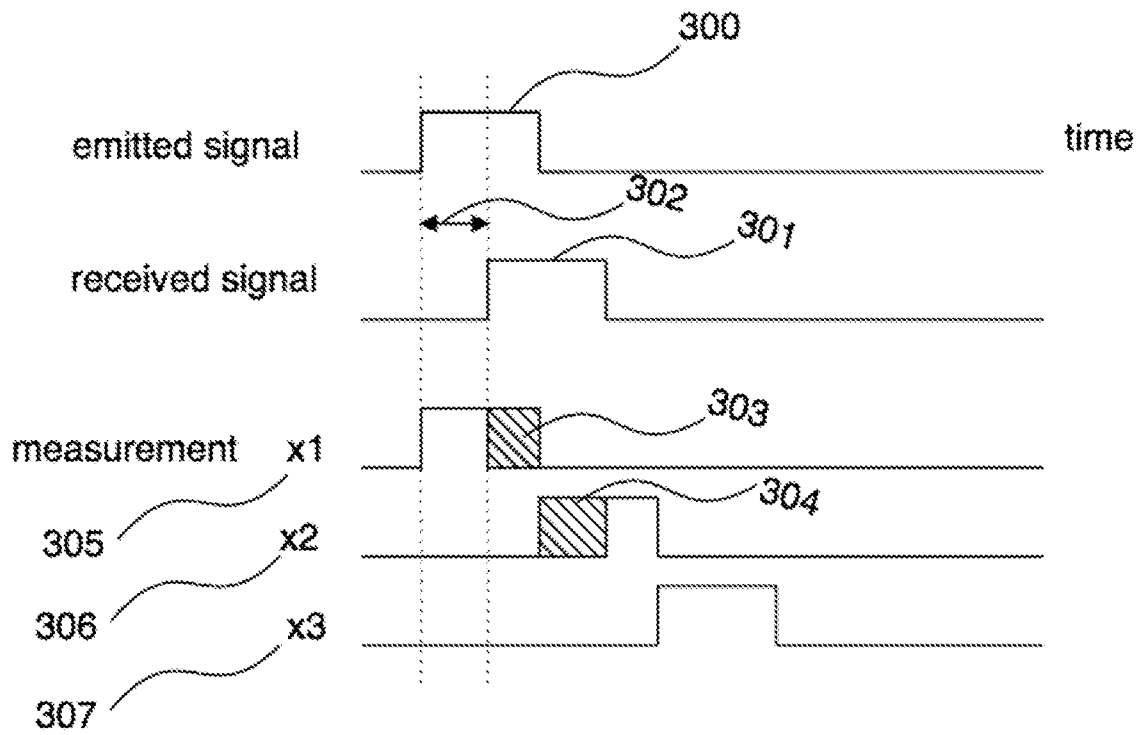


FIG. 15

**FIG. 16**

## EVENT-BASED ASYNCHRONOUS SELF-TRIGGERING READOUT

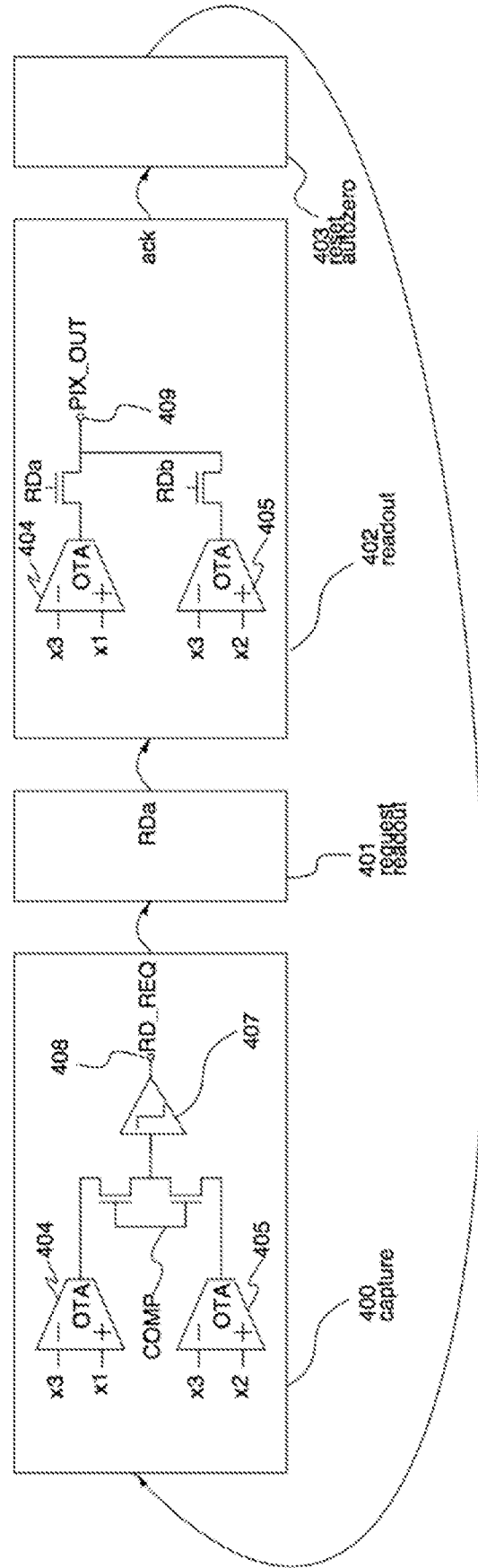


FIG. 17

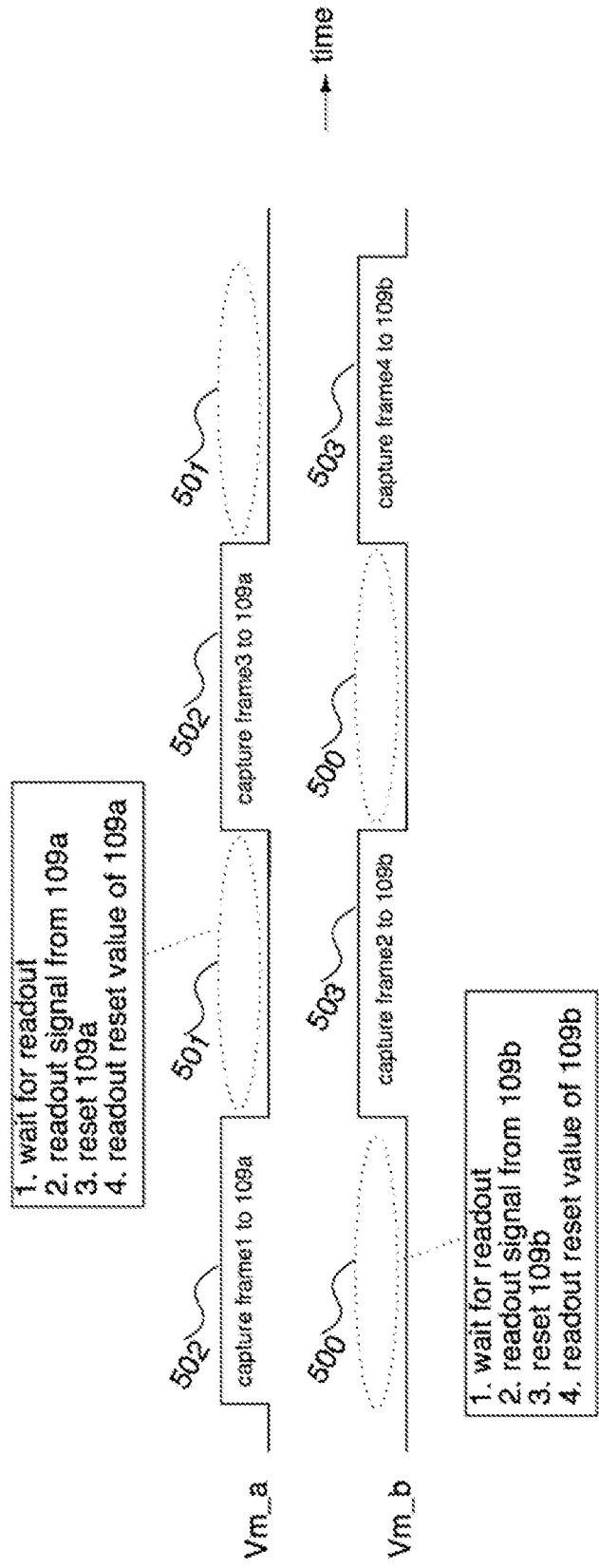


FIG. 18

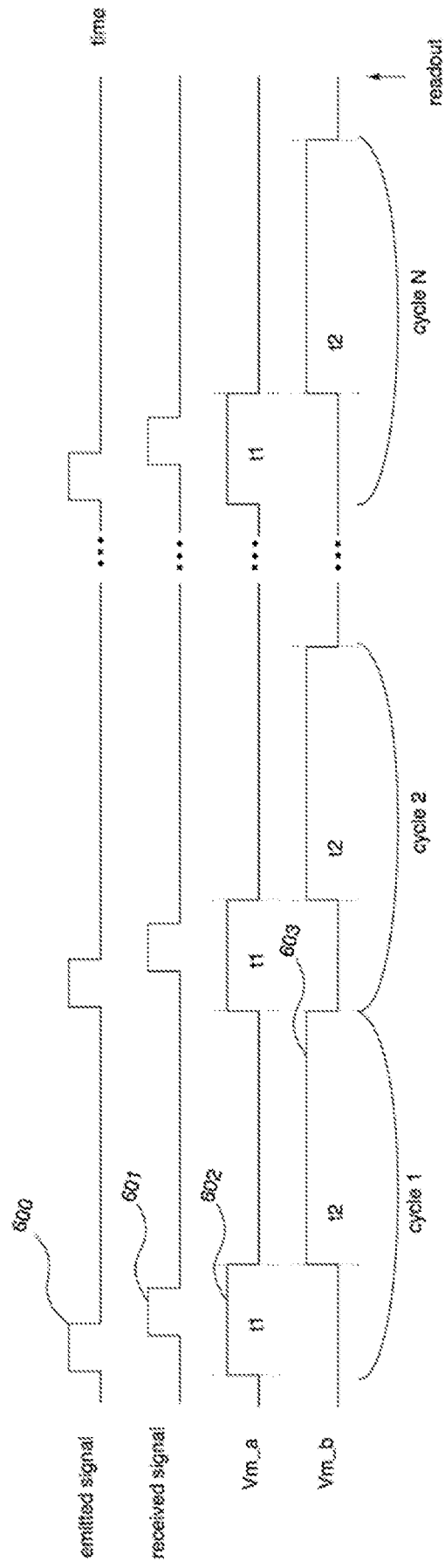


FIG. 19

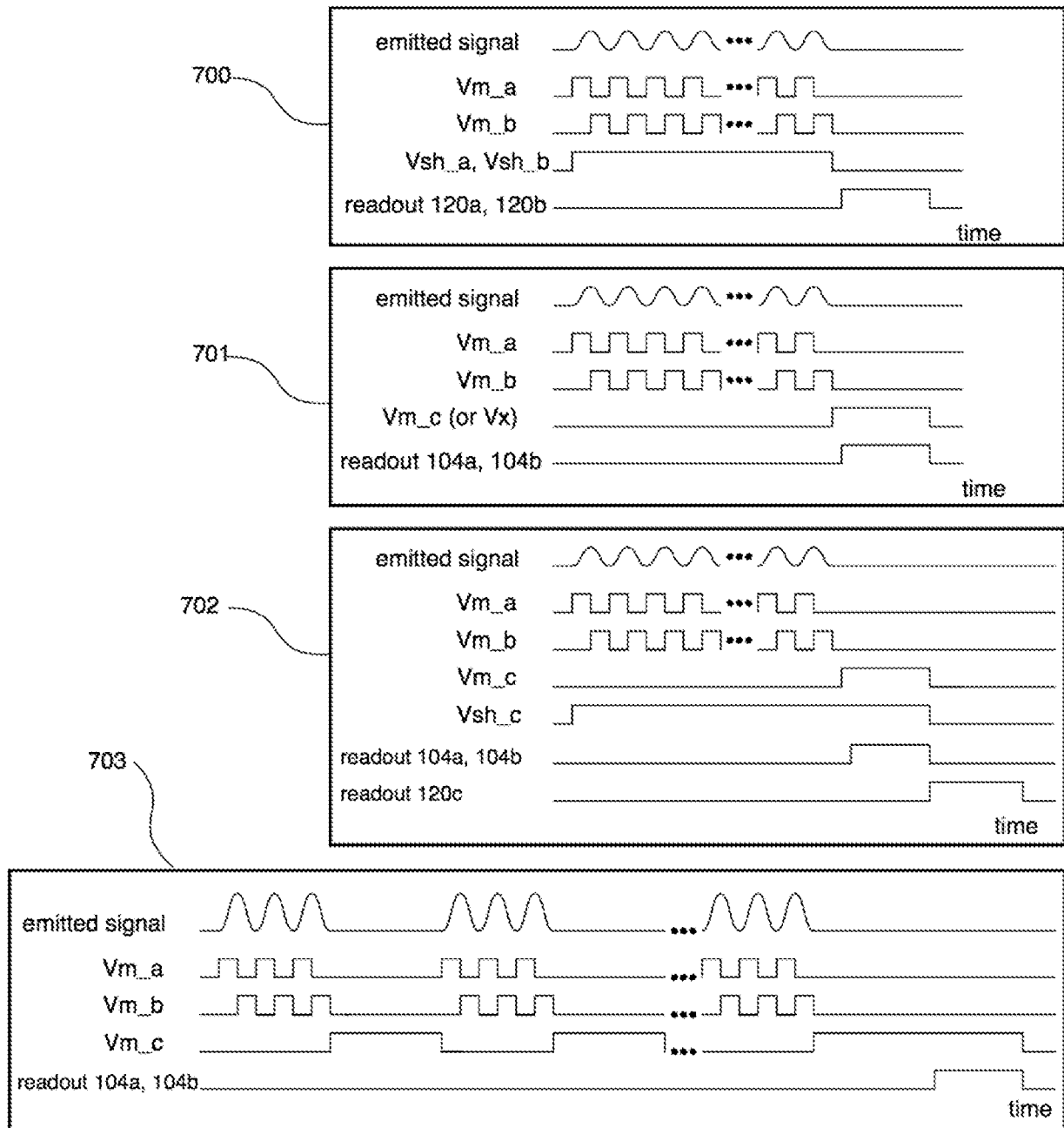
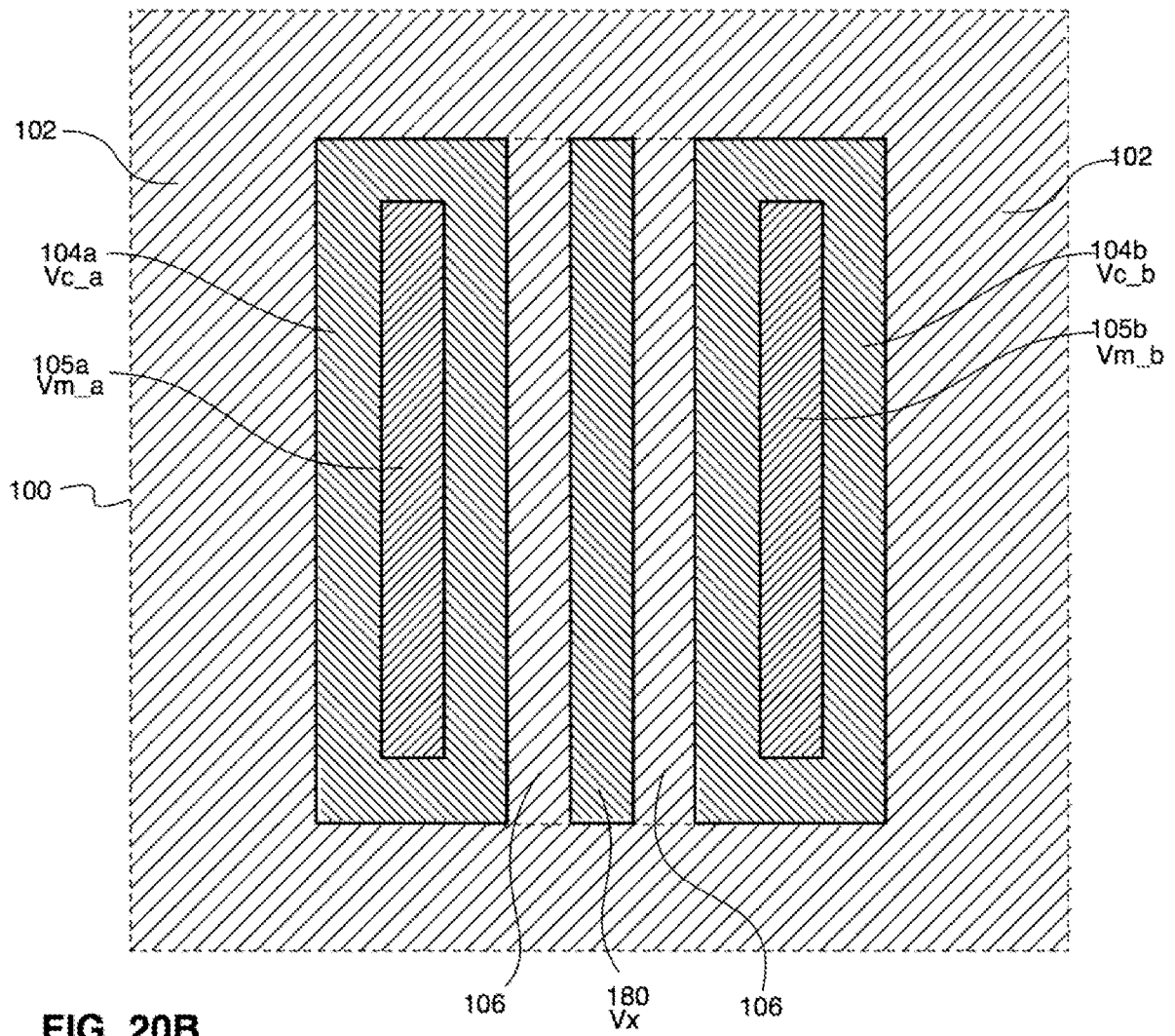
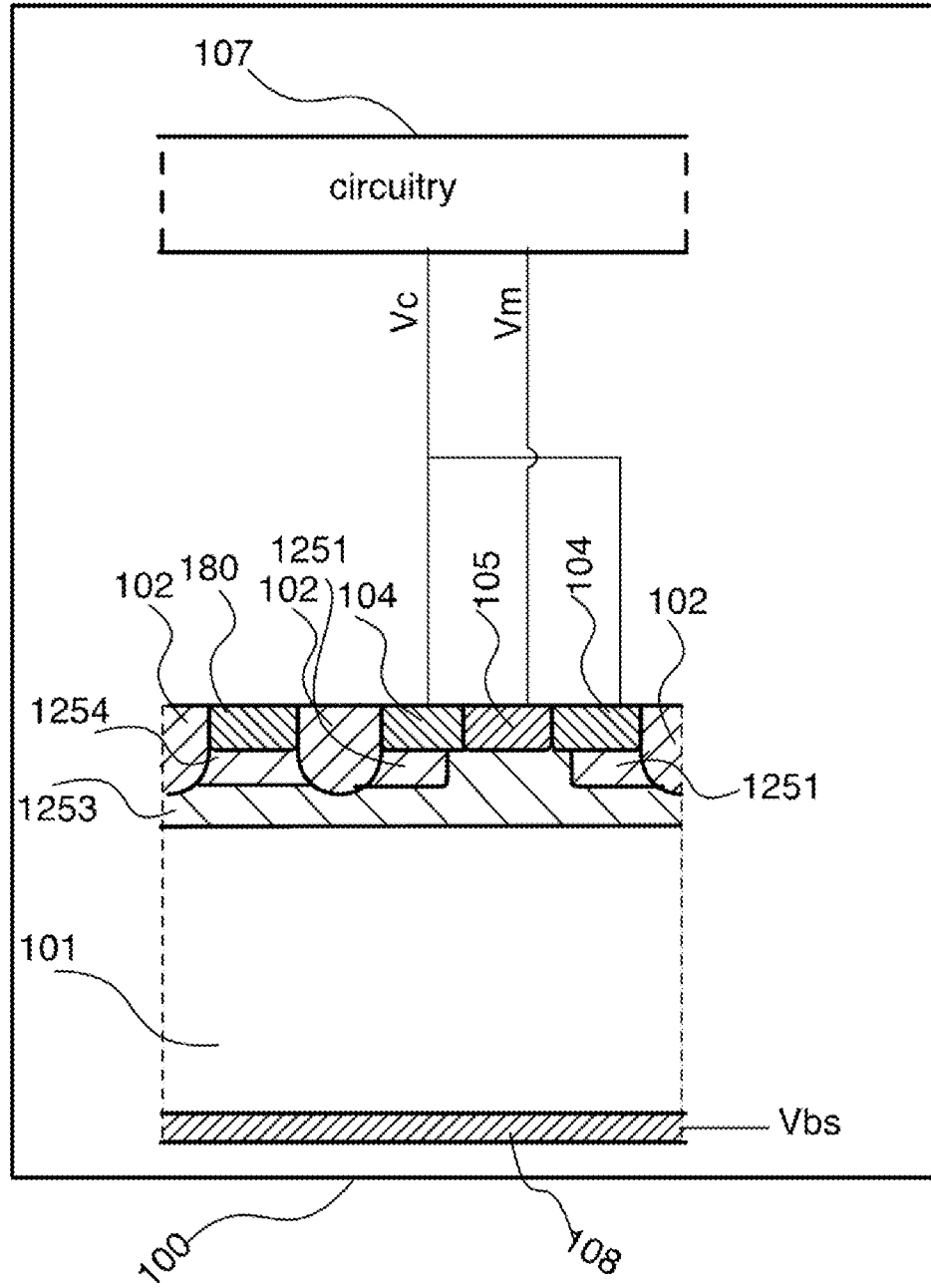


FIG. 20A







**FIG. 21B**

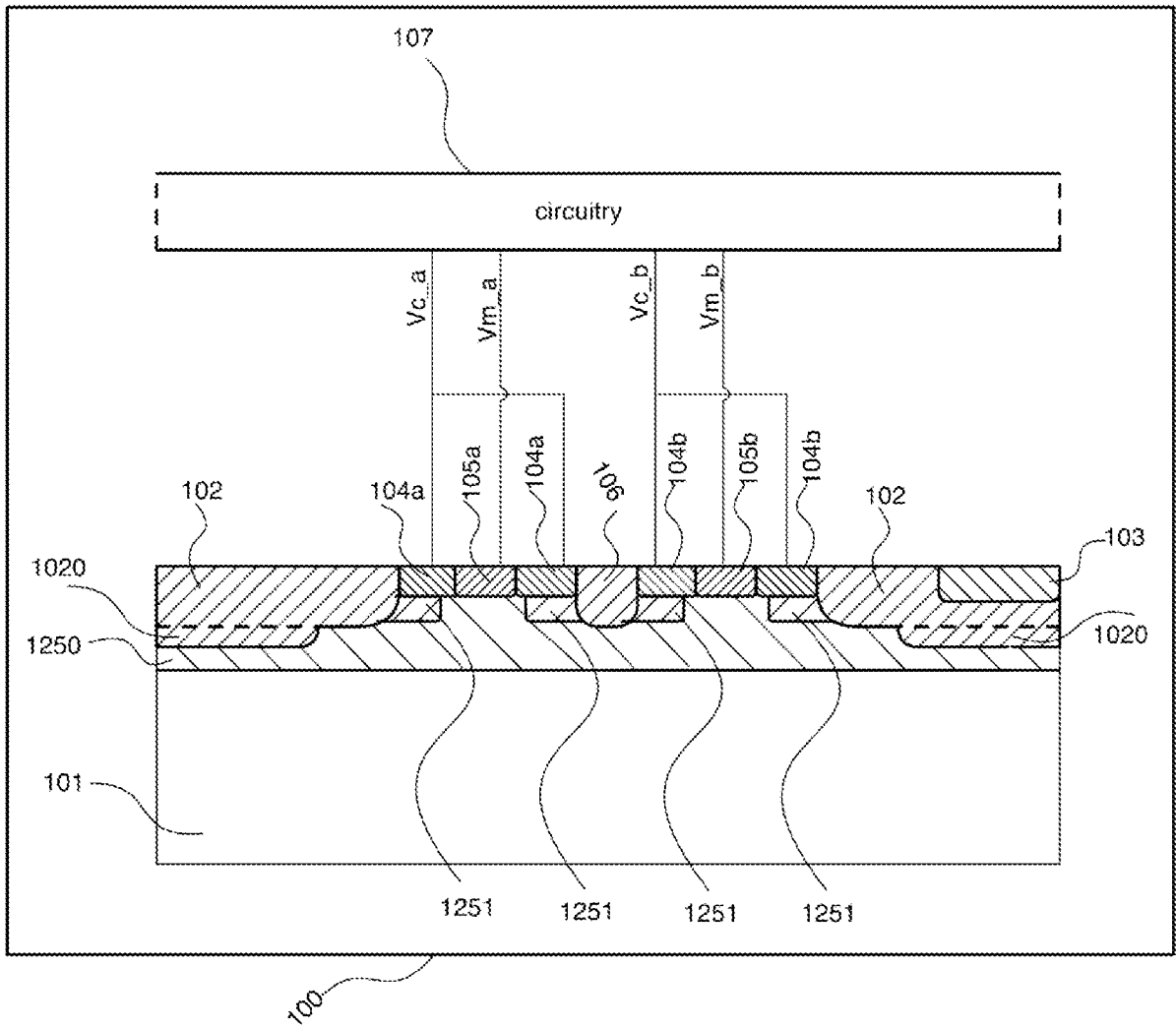


FIG. 21C

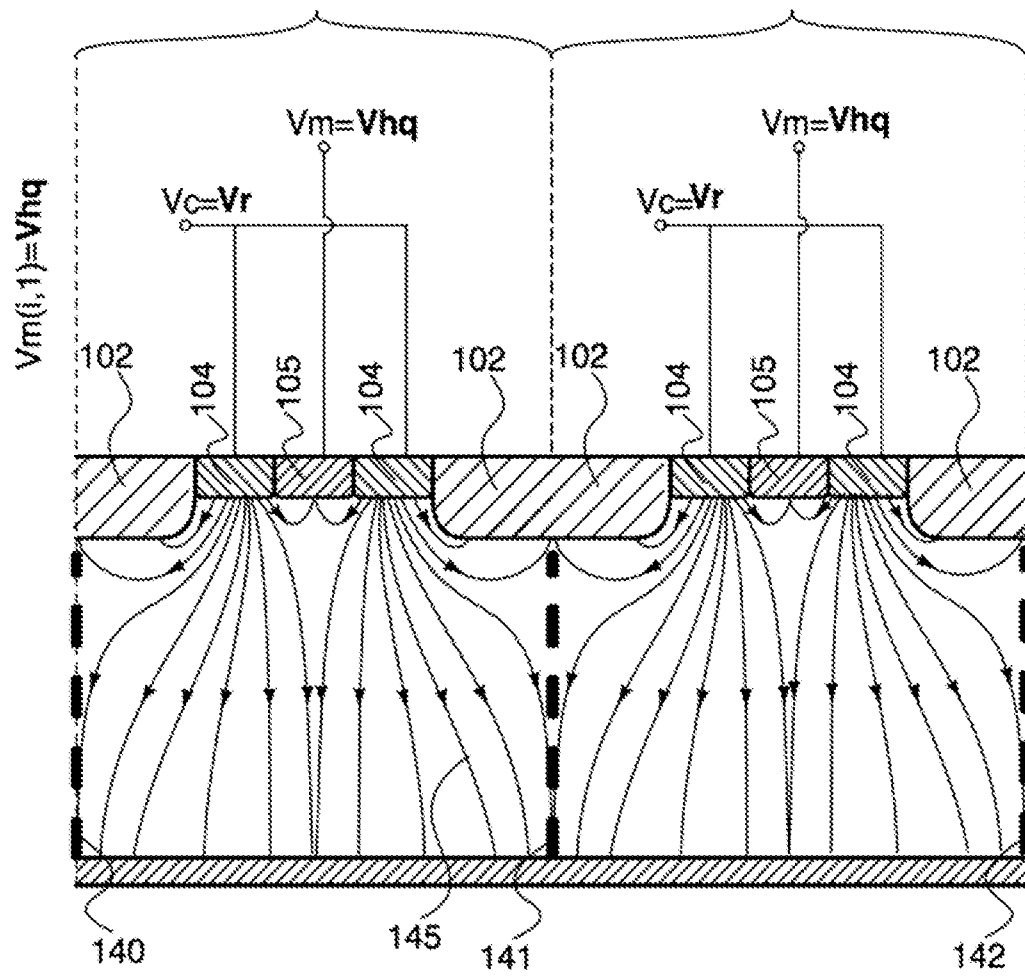


FIG. 22