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(54) Title: METHOD AND APPARATUS FOR COMPUTING INTERPOLATION FACTORS IN SAMPLE RATE CONVERSION SYSTEMS

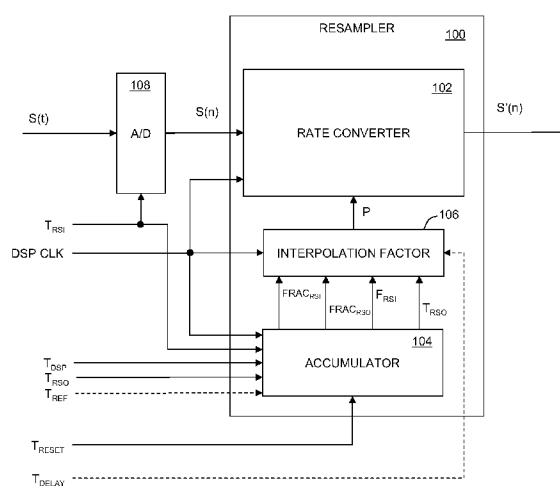


FIG. 1

(57) Abstract: The invention allows the interpolation factor, a critical parameter in sample rate conversion systems, to be computed in a real-time system where there is a complex relationship between a DSP clock and the data clocks. Typically, two or three of the clocks in such a system will have simple relationships (such as $CLOCK_1 = 2 * CLOCK_2$). This relationship leads to degenerate cases where, in fact, there are only one or two clocks to consider rather than three. Furthermore, the invention allows for input data rates that are higher than the DSP clock rate. The invention also provides for an arbitrary time delay to be applied to the output signal.

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