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09/717,998 21 November 2000 (21.11.2000) US(71) Applicant: TRANSWITCH CORPORATION
[US/US]; 3 Enterprise Drive, Shelton, CT 06484 (US).

(72) Inventors: ROY, Subhash, C.; 36 Bertwell Road, Lexington, MA 02420 (US). DAS, Santanu; 14 Hunter Ridge Road, Monroe, CT 06468 (US). UPP, Daniel, C.; 15

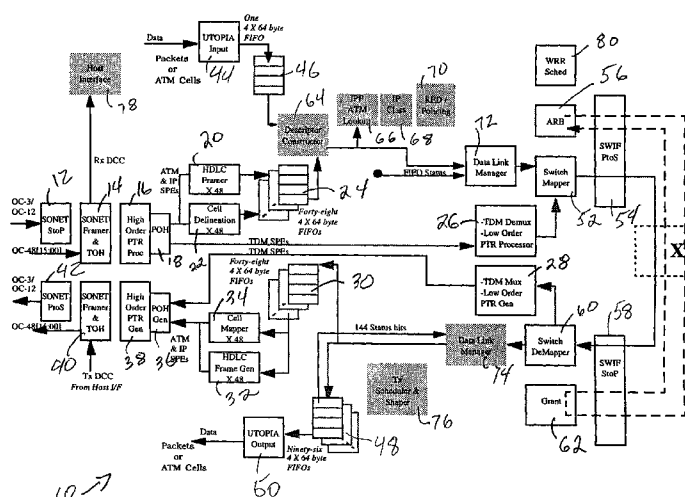
Pepper Tree Hill Lane, Southbury, CT 06488 (US). LIPP, William, B.; 317 Saint Ronan Street, New Haven, CT 06511 (US). VIJ, Jitender, K.; 353 Putting Green Road, Trumbull, CT 06611 (US). RENAULT, Michael, M.; 40 Ellis Street, Medway, MA 02053 (US). CARTER, Frederick, R.; 481 South Broadway, Apt. #11, Lawrence, MA 01843 (US). RAMCHANDANI, Rajen, S.; 290 Berlin Street, #41, Clinton, MA 01510 (US). BENOIT, Steven, E.; 12 Bramel Circle, Walpole, MA 02081 (US). TOEBES, David, K.; 4 Brundrett Avenue, Andover, MA 01810 (US).

(74) Agents: GORDON, David, P. et al.; 65 Woods End Road, Stamford, CT 06905 (US).

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(54) Title: METHODS AND APPARATUS FOR SWITCHING ATM, TDM, AND PACKET DATA THROUGH A SINGLE COMMUNICATIONS SWITCH



(57) Abstract: A network switch includes at least one port processor (10) and at least one switch element (100). The port processor (10) has an SONET OC-x interface (12), a UTOPIA interface (44), and an interface to the switch element (100). The port processor (10) has a total I/O bandwidth equivalent to an OC-48, and the switch element (100) has 12x12 ports for a total bandwidth of 30 Gbps. A data frame of 9 rows by 1700 slots is used to transport ATM, TDM, and Packet data. Each frame is transmitted in 125 microseconds. Each slot includes a 4-bit tag plus a 4-byte payload. The last twenty slots of the frame are reserved for link overhead. For ATM and packet data, a PDU of 16 slots is defined for a 64-bytes payload. The PDUs are self-routed through the switch with a 28-bit routing tag. Bandwidth is arbitrated among ATM and Packet connections while maintaining TDM timing.

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