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(54) Title: FLEXIBLE PROCESSING HARDWARE ARCHITECTURE

(57) Abstract

A flexible, reconfigurable processing system architecture allows for the implementation of a variety of processing system preferably a PCI bus add-in extension board (100) with an attached daughter card (D) attached and electrically connected thereto through a PCI Mezzanine type connector (36), and which is plugged into a personal computer PCI expansion slot. The architecture uses the PCI bus (26) which allows PCI devices to be hidden from the host CPU to allow for proper system startup.

