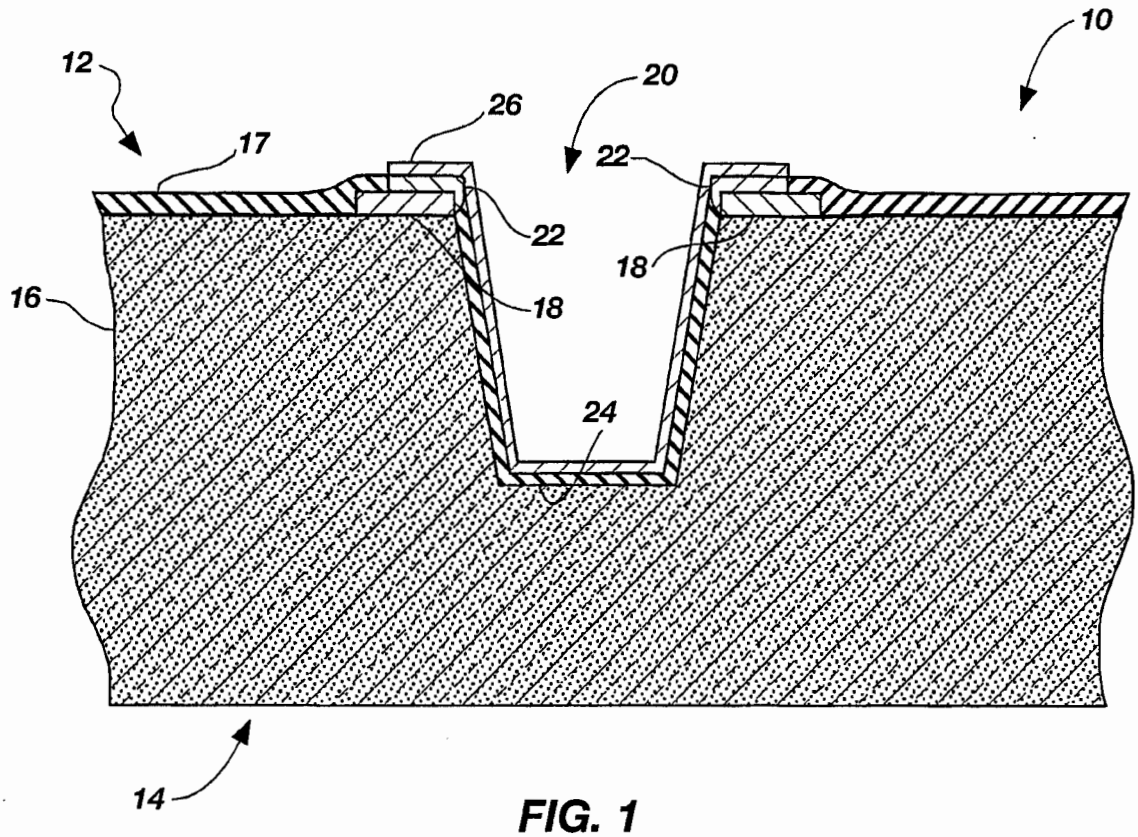


FIG. 1

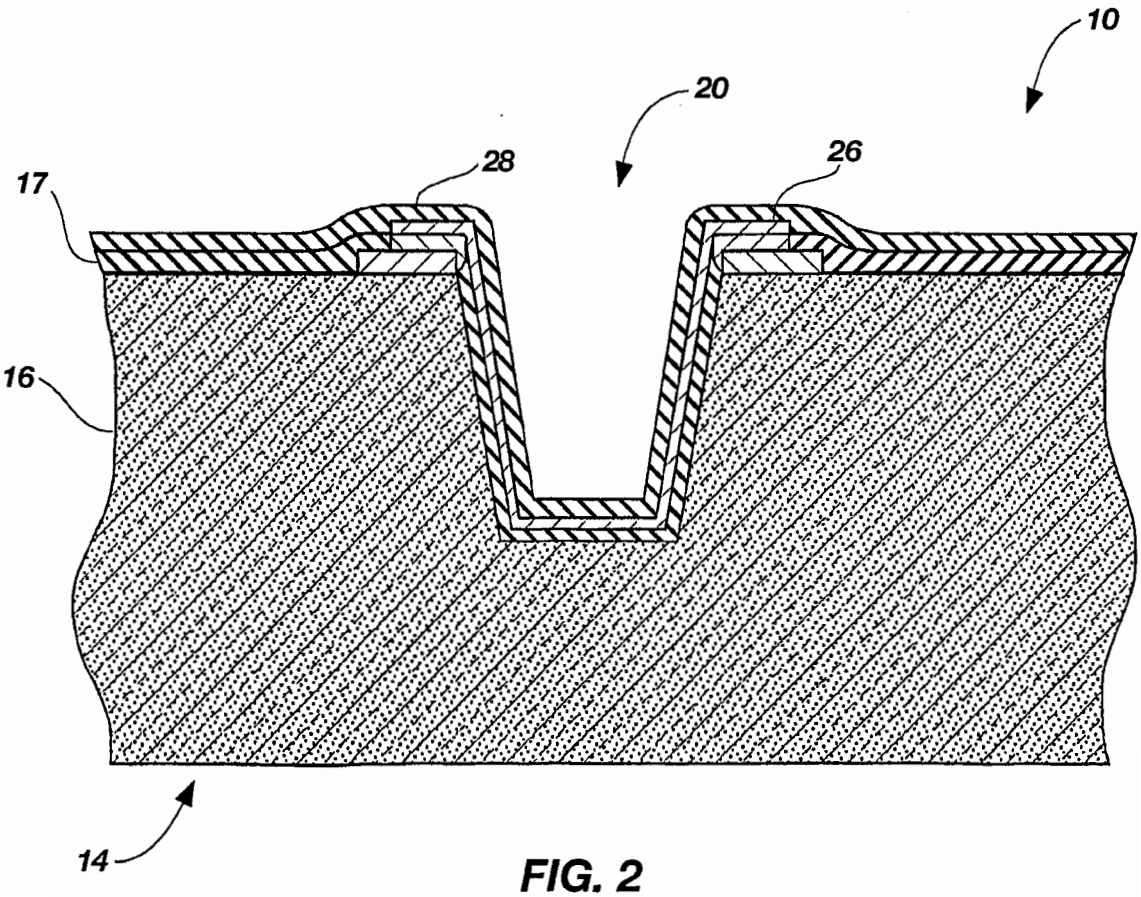


FIG. 2

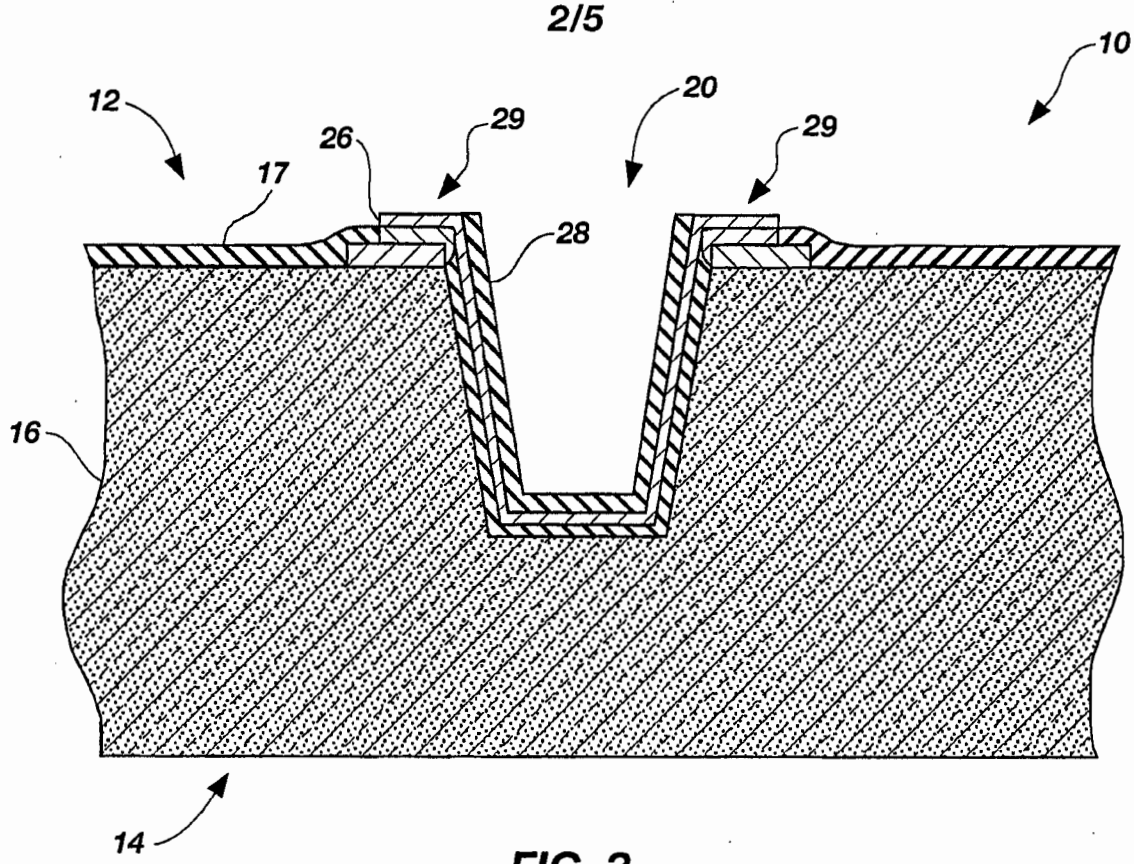


FIG. 3

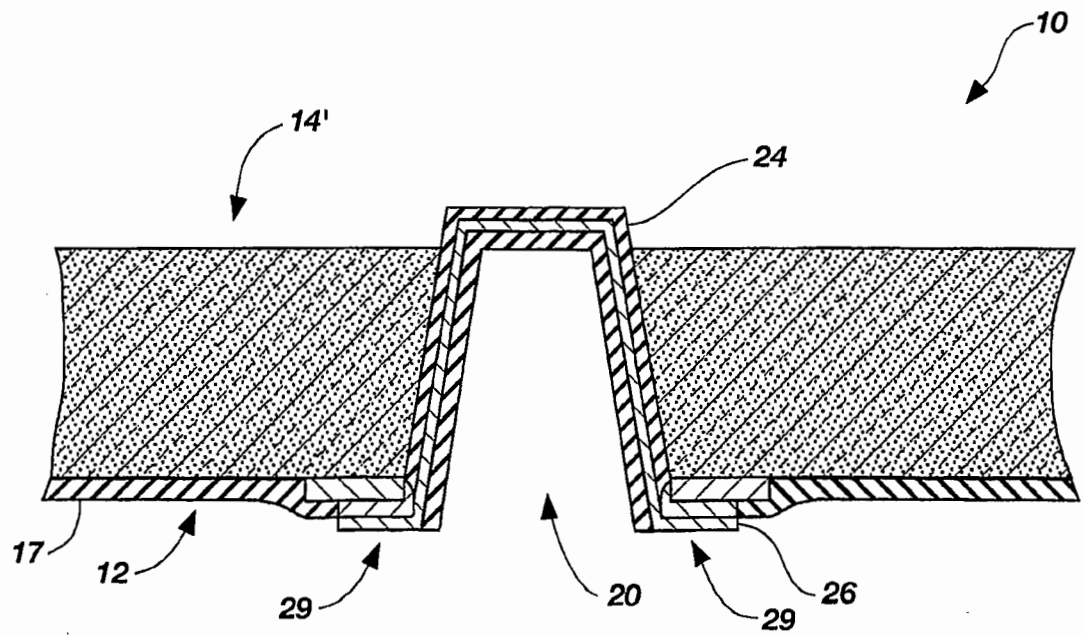


FIG. 4

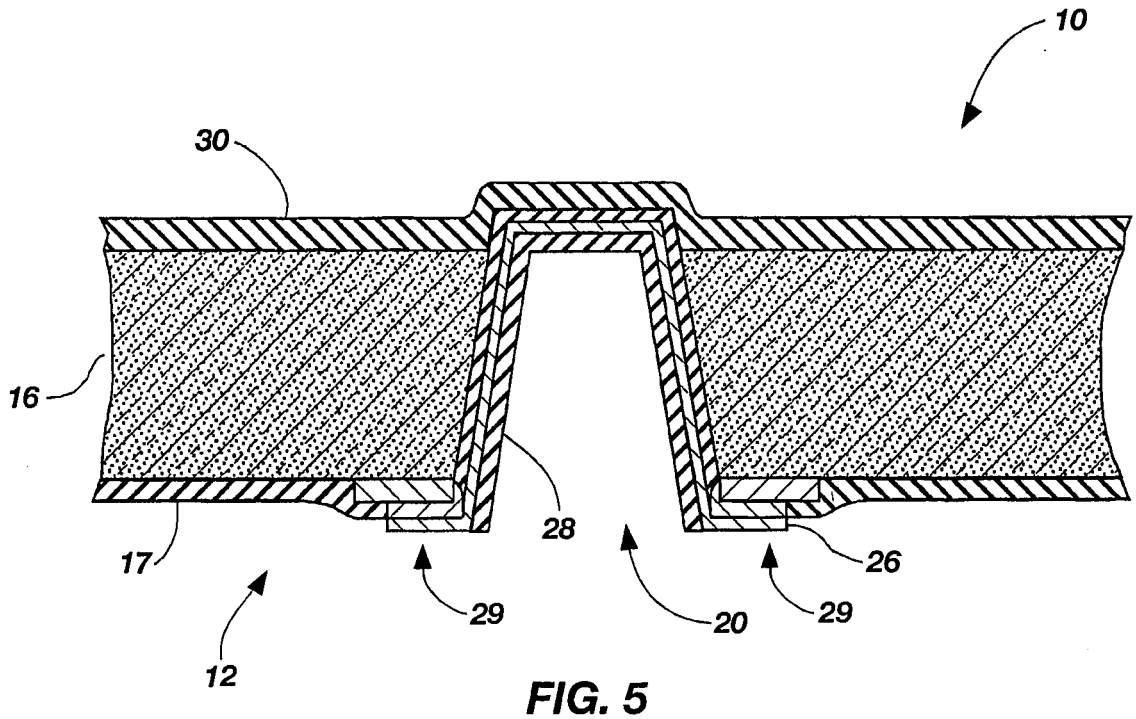


FIG. 5

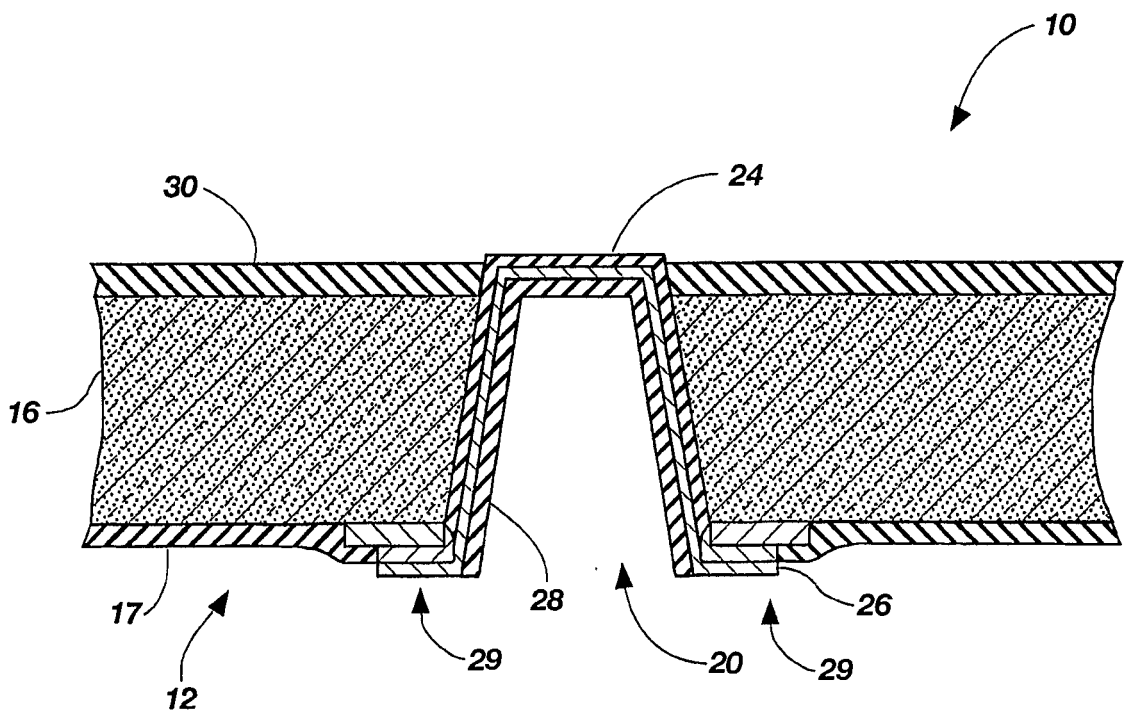


FIG. 6

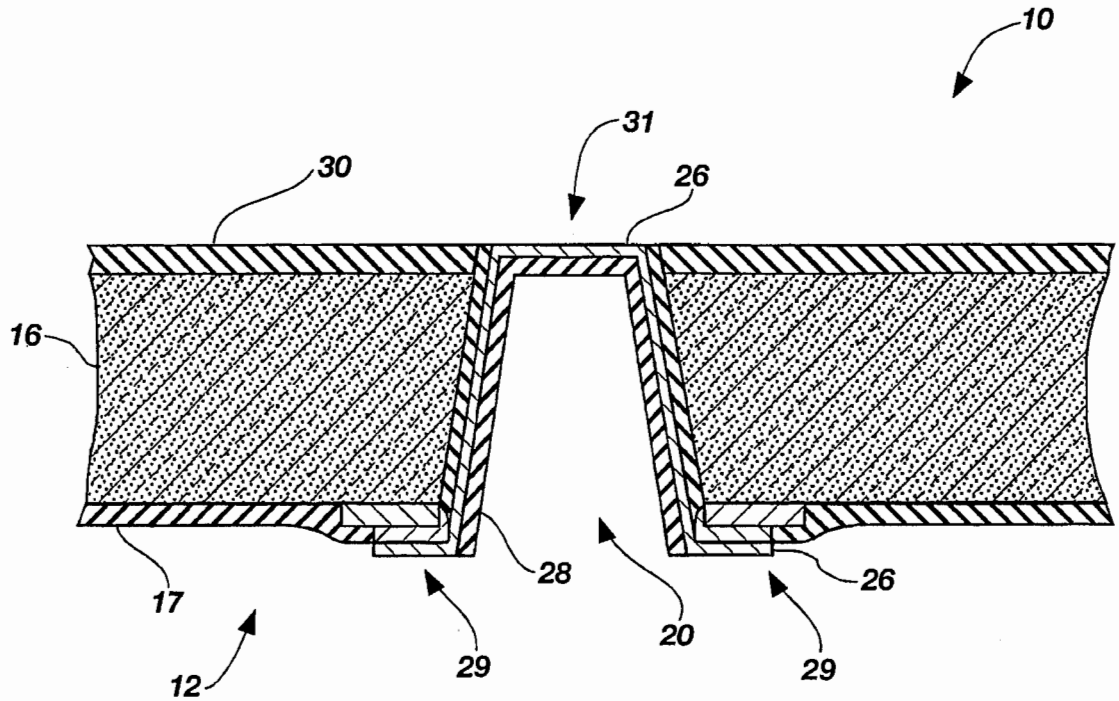


FIG. 7

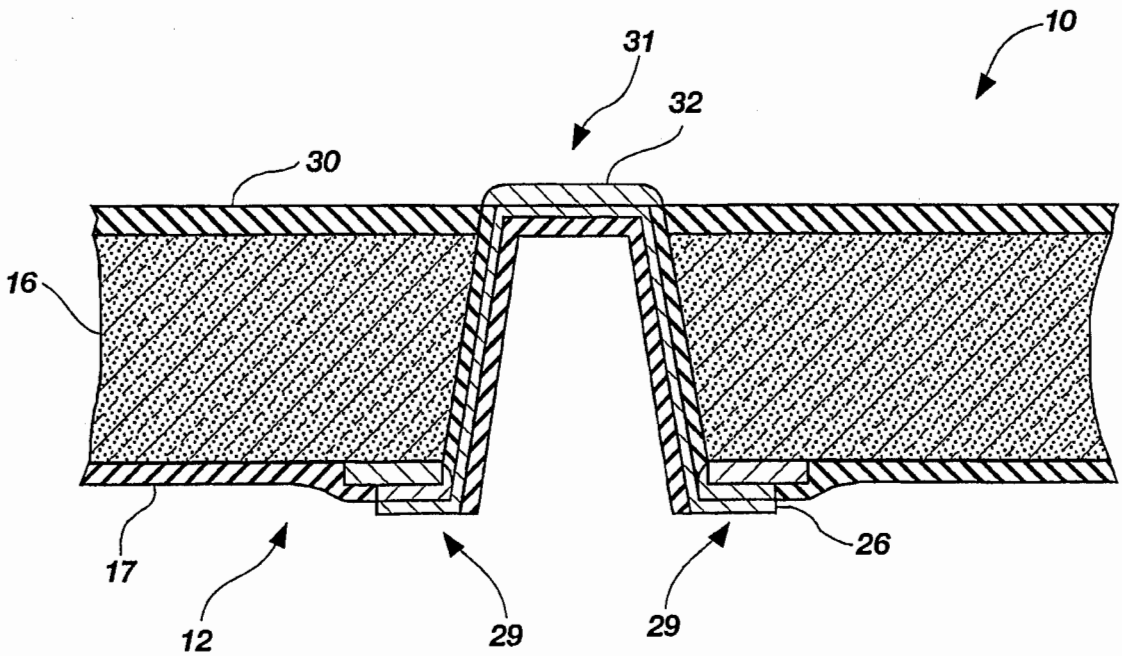


FIG. 8

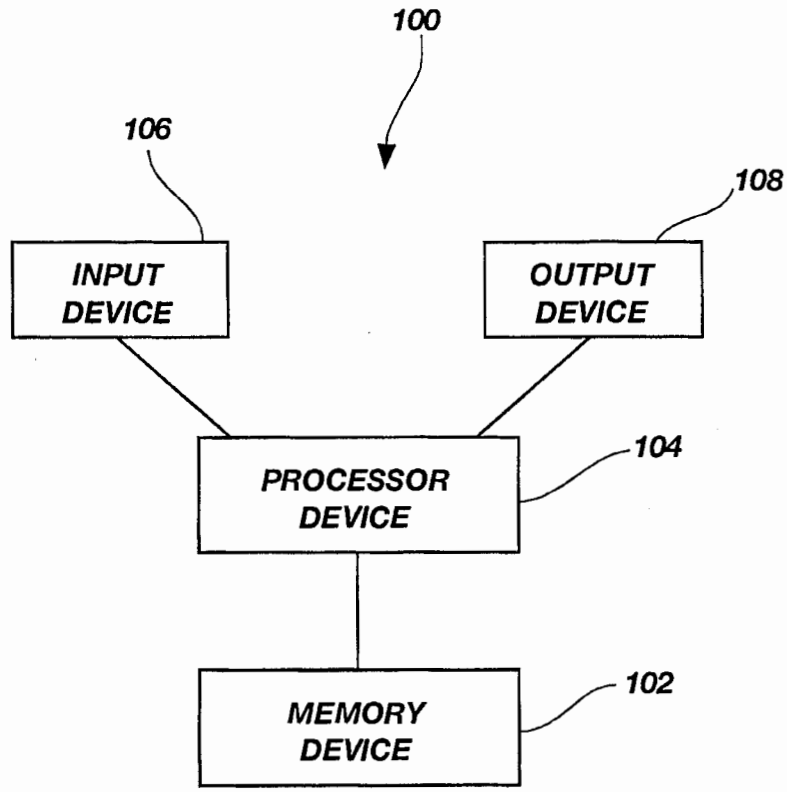

100
106
108
INPUT DEVICE
OUTPUT DEVICE
PROCESSOR DEVICE
104
MEMORY DEVICE
102

FIG. 9