

PHA 1236

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ABSTRACT:

BiCMOS output driver circuit.

A BiCMOS power driver circuit for interfacing to a bus comprises means for channelling current from a power source to the base of a bipolar device to pull the output all the way down to within a bipolar V_{SAT} voltage drop of ground, and then uses feedback to turn-off the pull-down circuit to conserve power.

- 5 A similar circuit functions to provide Incident Wave Switching and Glitch Suppression by monitoring the voltage level at the output and sinking current as necessary to maintain a low logic level.

This Sheet Has Been Replaced

