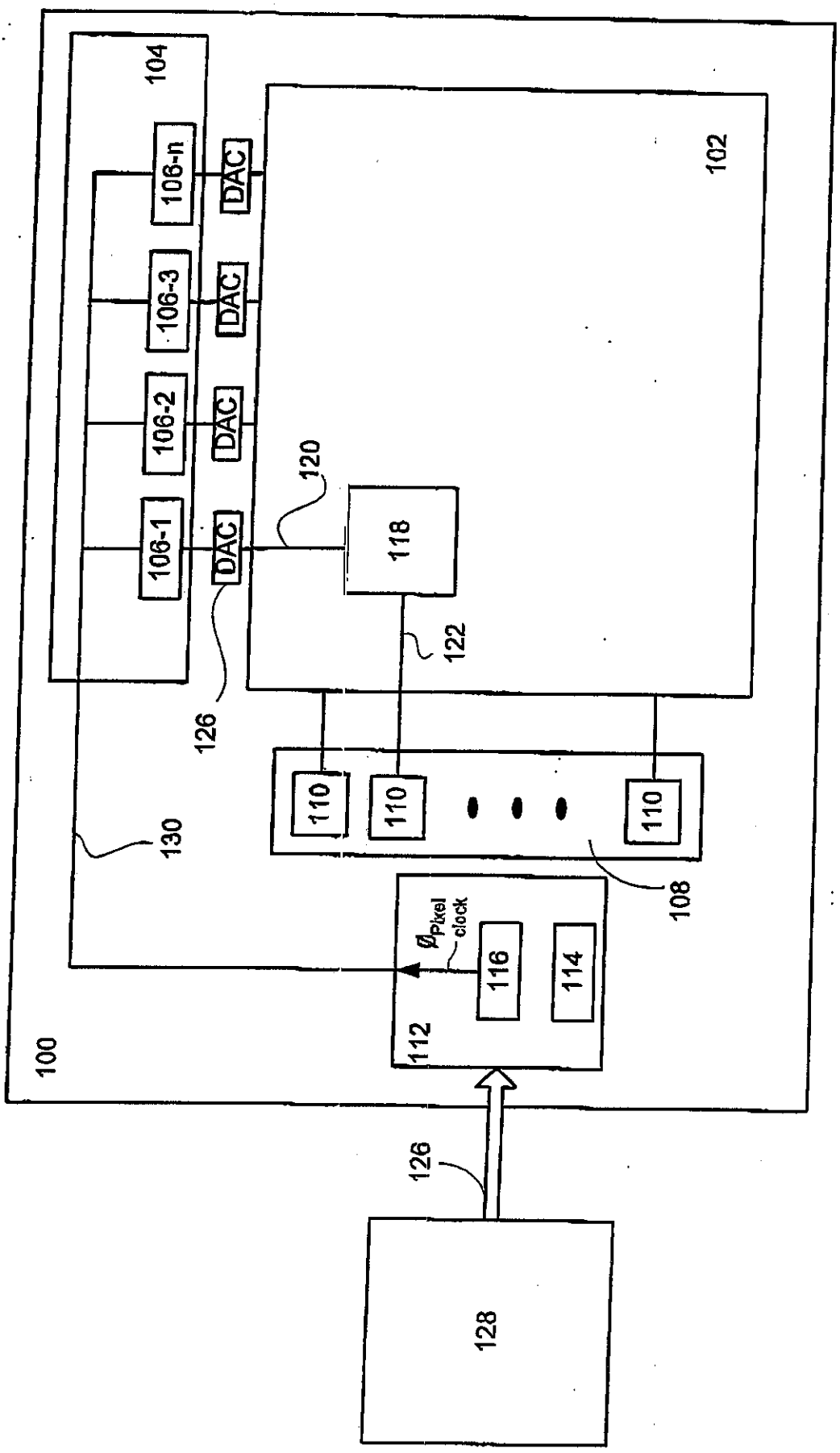


Fig. 1

Prior Art

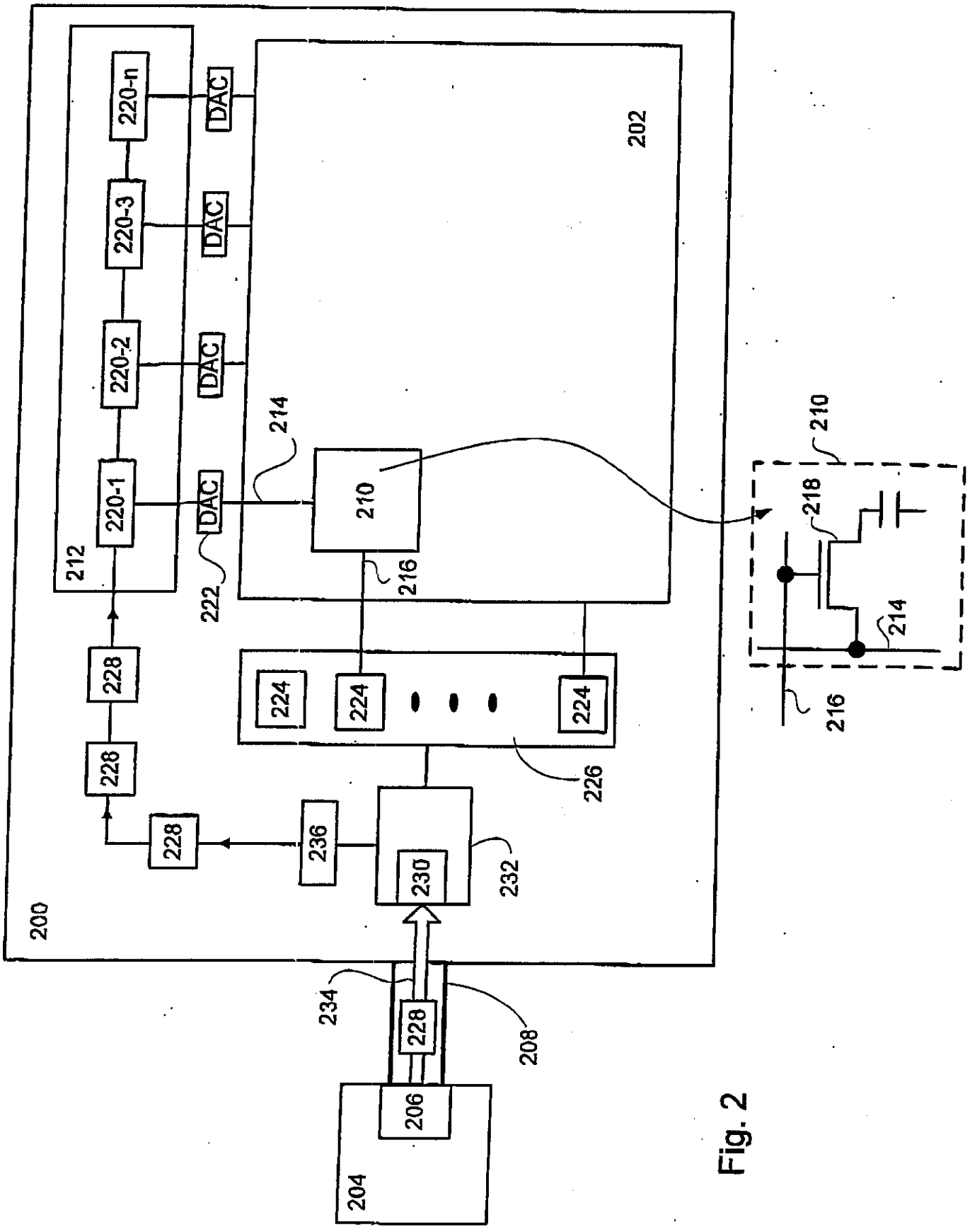

200
202
204
206
208
210
212
214
216
218
220-1
220-2
220-3
220-n
222
224
226
228
230
232
234
236
DAC

Fig. 2

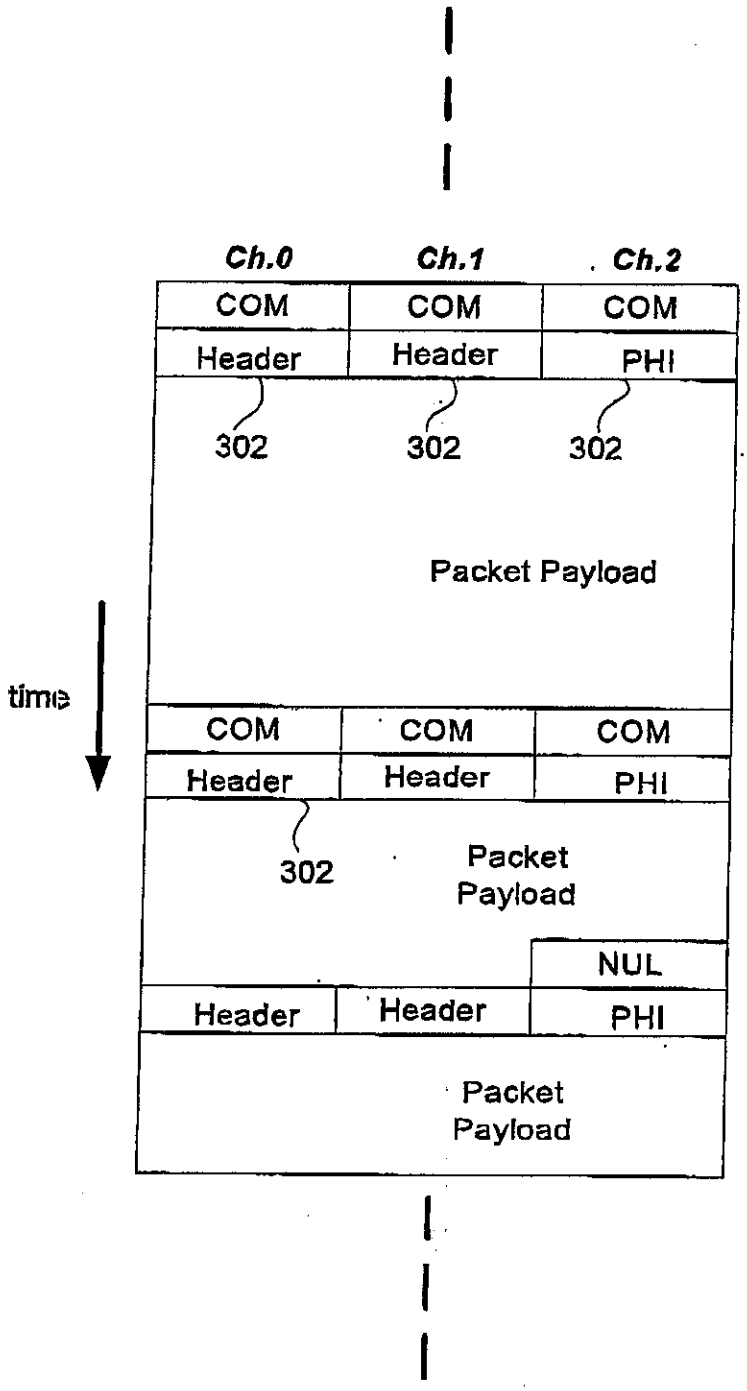


Fig. 3A

Main Link Packet Format

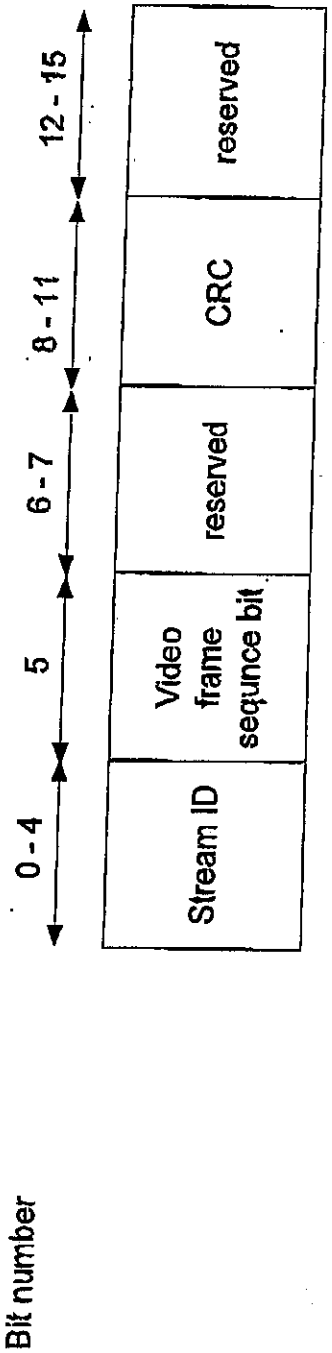

Bit number
0 - 4
5
6 - 7
8 - 11
12 - 15
Stream ID
Video frame sequnce bit
reserved
CRC
reserved

Fig. 3B

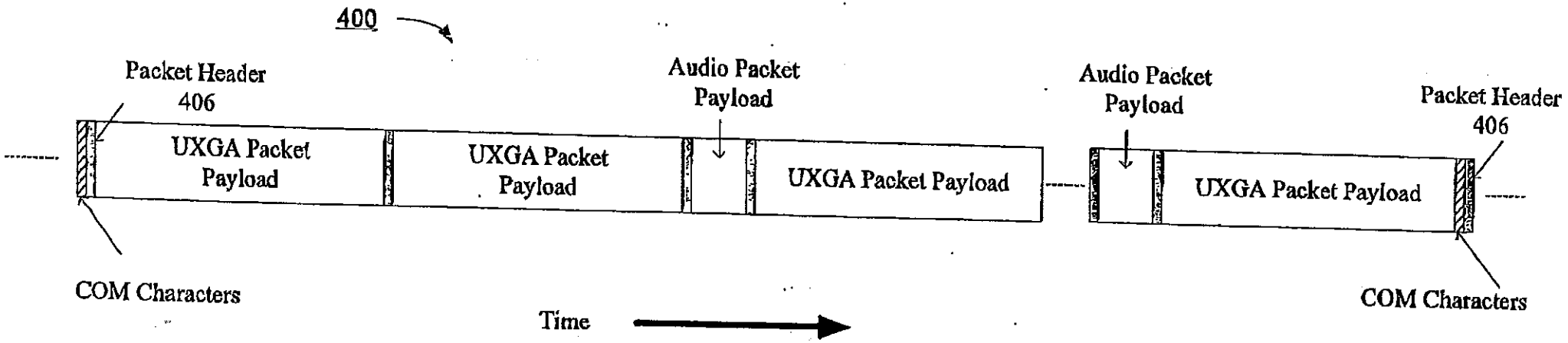


Fig. 4

High-level diagram of link traffic example

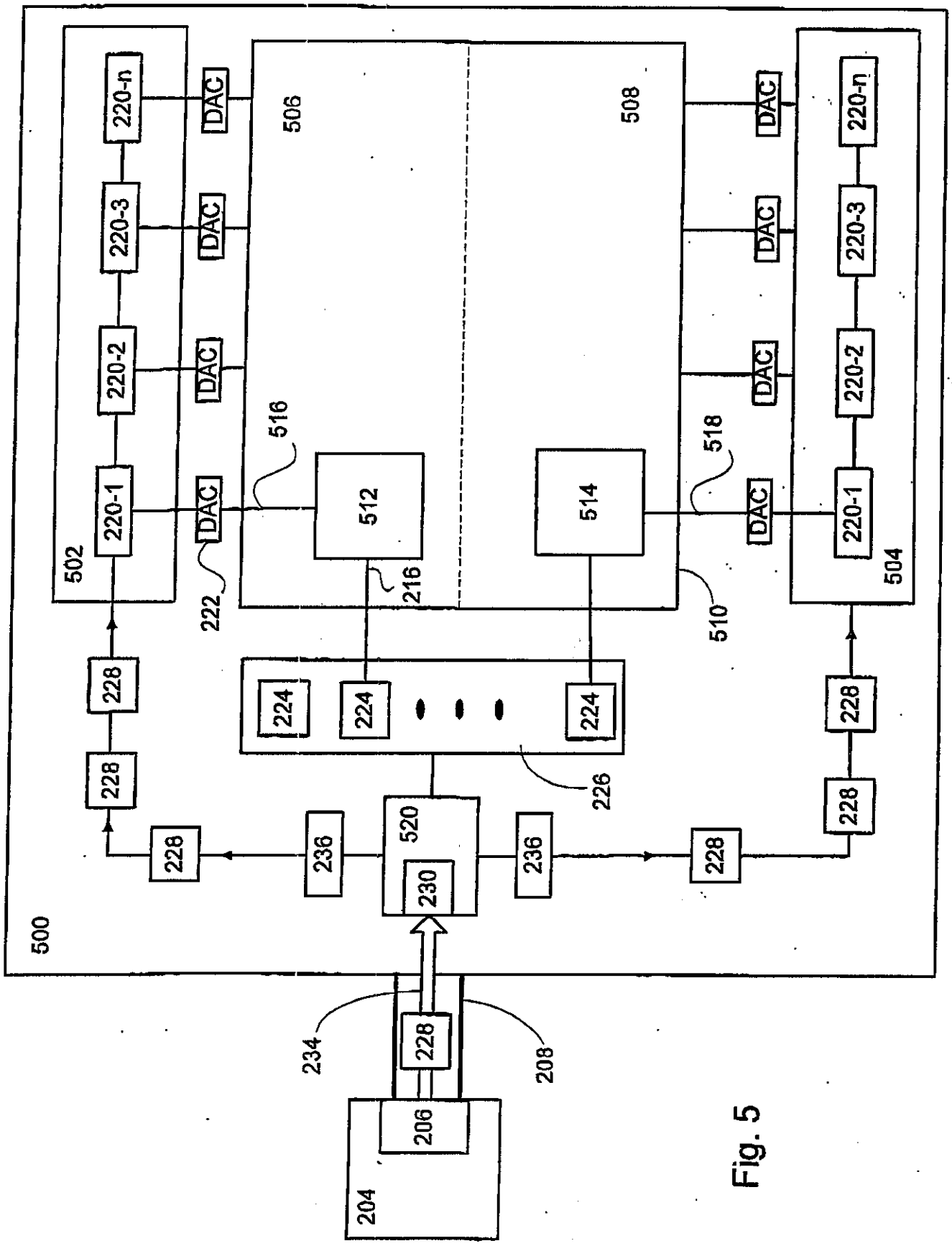

500
502
504
506
508
510
512
514
516
518
520
522
216
220-1
220-2
220-3
220-n
DAC
224
226
228
230
234
236
204
206
208

Fig. 5

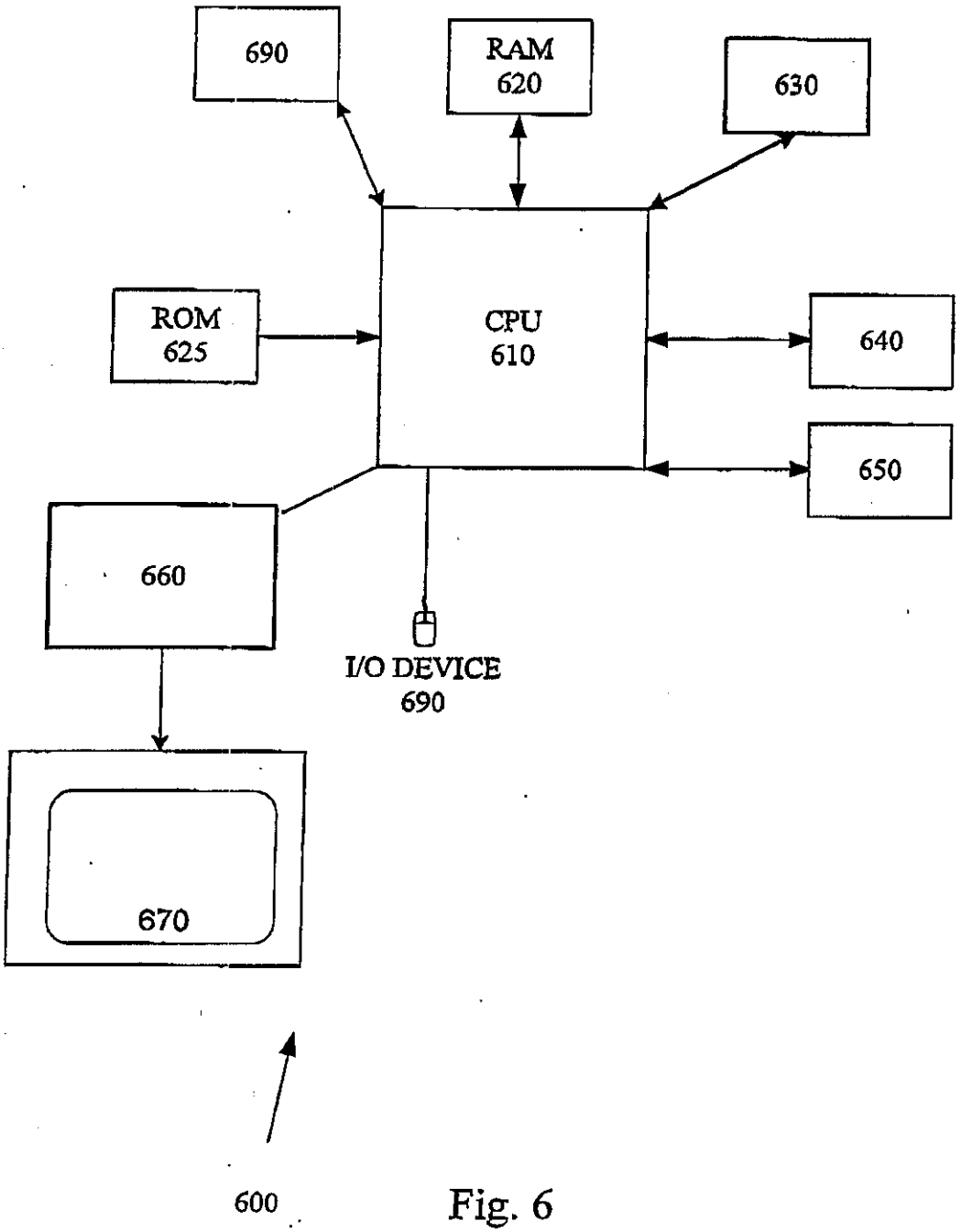

690
RAM
620
630
ROM
625
CPU
610
640
650
660
I/O DEVICE
690
670
600

Fig. 6