

ABSTRACT:

Receiver.

Receiver having an RF input for applying an RF input carrier-modulated modulation signal thereto, which receiver is coupled to a multiplier circuit and to a phase-locked loop (PLL) with a signal path incorporating a phase detector, a loop filter, a first dc decoupling circuit and a controlled oscillator having an in-phase and a quadrature output *via* which local in-phase and quadrature carriers are applied to the multiplier circuit and the phase detector, respectively, and a signal generator for generating a local auxiliary pilot and a pilot detector for detecting the local auxiliary pilot, an output of which is coupled to the controlled oscillator *via* a low-pass filter. In order to reduce the influence of oscillator radiation and other parasitic crosstalk effects on the dc reduction, an output of the signal generator is coupled to the signal path of the phase-locked loop between an output of the phase detector and a control input of the controlled oscillator, an output of the multiplier circuit is coupled to a first input and the output of the signal generator is coupled to a second input of the pilot detector.

Fig. 1.