

ABSTRACT OF THE DISCLOSURE

In a feedforward amplifier with a dual loop in which a distortion injection path of a distortion elimination circuit 50 is provided as a
5 feedforward configuration composed of a first auxiliary amplifier distortion elimination circuit 60 and a first auxiliary amplifier distortion elimination circuit, a second variable attenuator 55 and a second variable phase shifter 56 are provided preceding the distortion detection circuit 60, a second pilot signal injected between stages of a main amplifier 14 is detected by a
10 directional coupler 85, and the second variable attenuator 55 and the second variable phase shifter 56 are controlled by a second controller 97 to minimize the level of the detected second pilot signal, thereby bringing the distortion elimination circuit and the first auxiliary distortion detection circuit into balance at the same time.

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