

ABSTRACT OF THE DISCLOSURE

A pixel electrode substrate includes a glass substrate, parallel signal lines formed on the glass substrate, parallel gate lines formed perpendicularly to the signal lines on the glass substrate, and a plurality of TFTs disposed near the intersections of the signal lines and gate lines. A color filter layer and pixel electrodes are formed in that order over pixel regions defined by the signal lines and the gate lines on the glass substrate. The pixel electrodes and the color filter layer form color stripes. Each of the signal lines has offset sections formed near the intersections of the signal line and the gate lines with their center axes dislocated laterally from a center axis of the signal line such that the switching devices are disposed in dark regions outside color regions corresponding to the color layer. The pixel electrodes have projections projecting from the color regions into the dark regions, respectively, and the projections are connected to the switching devices in the dark regions.



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