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(54) Title: SELF-COMPENSATING OUTPUT BUFFER**(57) Abstract**

An integrated circuit is described comprising a signal source. Additionally, the integrated circuit includes an output buffer operatively coupled to the signal source. The output buffer generates an output signal in response to a voltage transition at the signal source. Moreover, the output buffer can be coupled to a transmission line terminated with a circuit, wherein the transmission line has a characteristic impedance which can vary over a range of A to B. The output buffer has a voltage-current (V-I) curve having a voltage that is approximately constant over the range of A to B, wherein the output signal can be transmitted to the circuit such that the output signal reaches a stable voltage after one round trip.

