

DIGITAL PHASE LOCKED LOOP SYSTEM AND METHOD

ABSTRACT OF THE DISCLOSURE

A phase locked loop control system includes a digital controlled oscillator (DCO) that is controlled by logic cells in response to comparison of the oscillator output with a reference clock related signal without use of an additional external clock. Delay cell number adjustment, delay cell load adjustment and cycle control are operative to digitally control the DCO frequency to obtain wide frequency range and limited jitter.

Figure 1