

Method of Operation of Arithmetic and Logic Unit, Storage
Medium, and Arithmetic and Logic Unit

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ABSTRACT OF THE DISCLOSURE

A method of operation of an arithmetic and logic unit, a storage medium, and an arithmetic and logic unit introducing a technique and concept of converting a
10 serial structure of decisions having an order dependency to an indeterminate code binary tree which can be processed in parallel so as to simplify the configuration and enable higher speed operation processing.

Where a serial structure of decisions having an order
15 dependency is converted to a binary tree structure using decision nodes not having dependency input/outputs as leaves and higher priority determination nodes as the nodes other than the leaves, the decision nodes having dependency input/outputs are replaced by decision nodes
20 not having dependency input/outputs provided with connotation decision nodes and indeterminate code generation nodes.

Figure 7



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