

ABSTRACT

In a region extending along a terminal region located near a substrate end and included in a frame region defined around a rectangular display region (D), a peripheral
5 circuit section (4) is provided between the display region (D) and a mount region (M) defined in part of the terminal region. The peripheral circuit section (4) includes unit circuit sections (4ua, 4ub) that are monolithically provided and are aligned along one side of the display region (D). The arrangement pitch of outer ones of the unit circuit sections (4ua, 4ub) is larger than that of inner ones of the unit circuit sections (4ua, 4ub).