

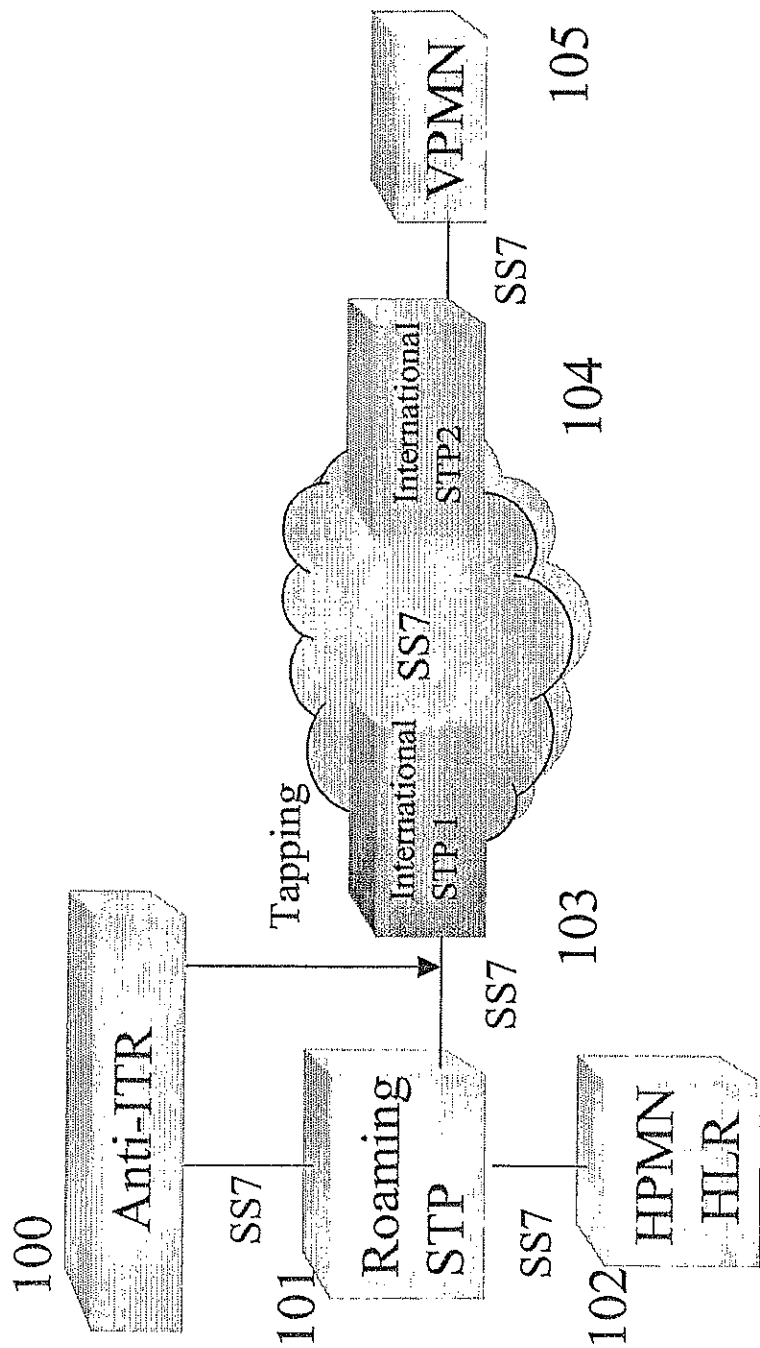


Figure 1: In-signaling path anti-ITR architecture with a monitoring option

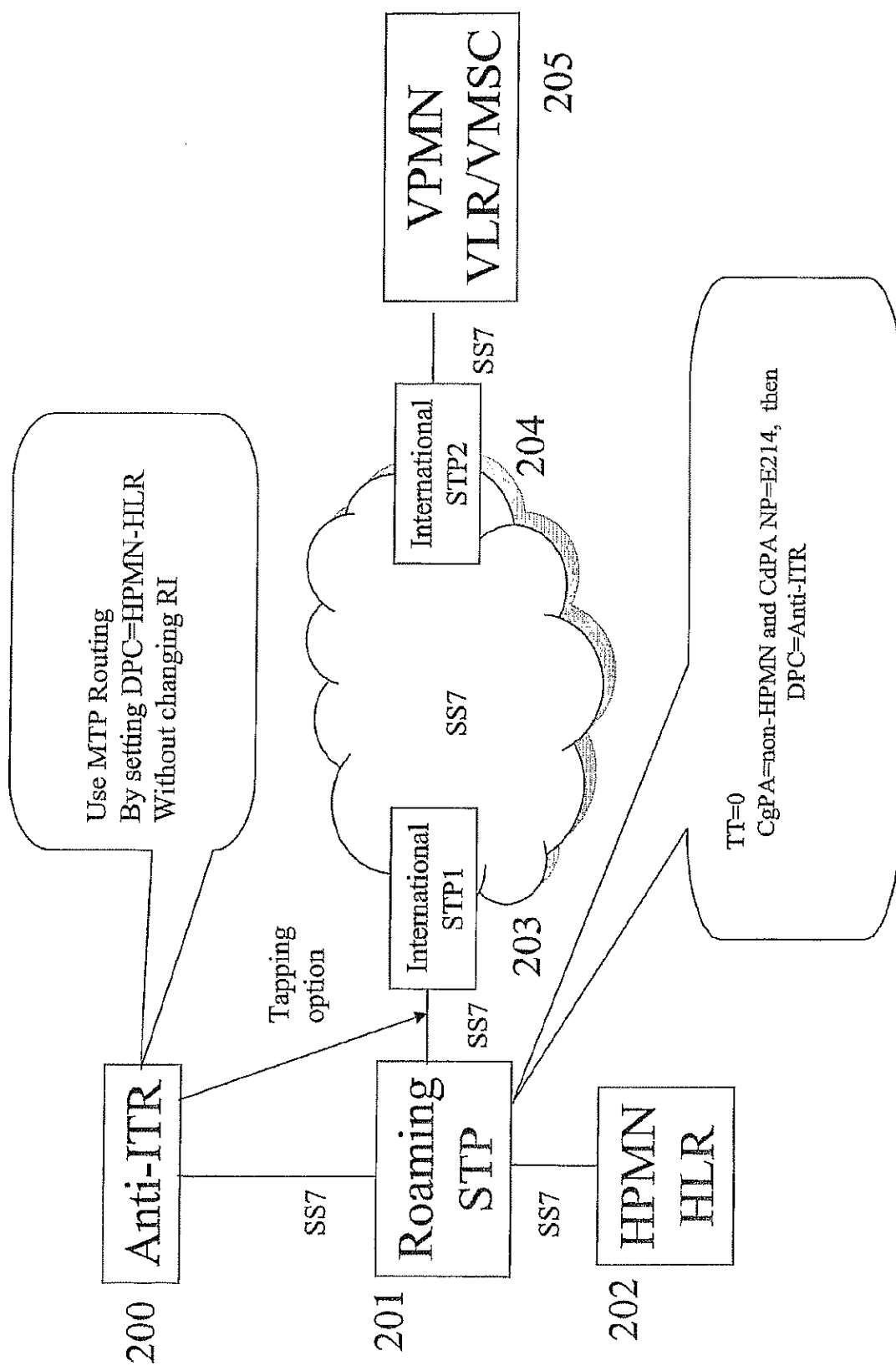


Figure 2: Not using Translation Type

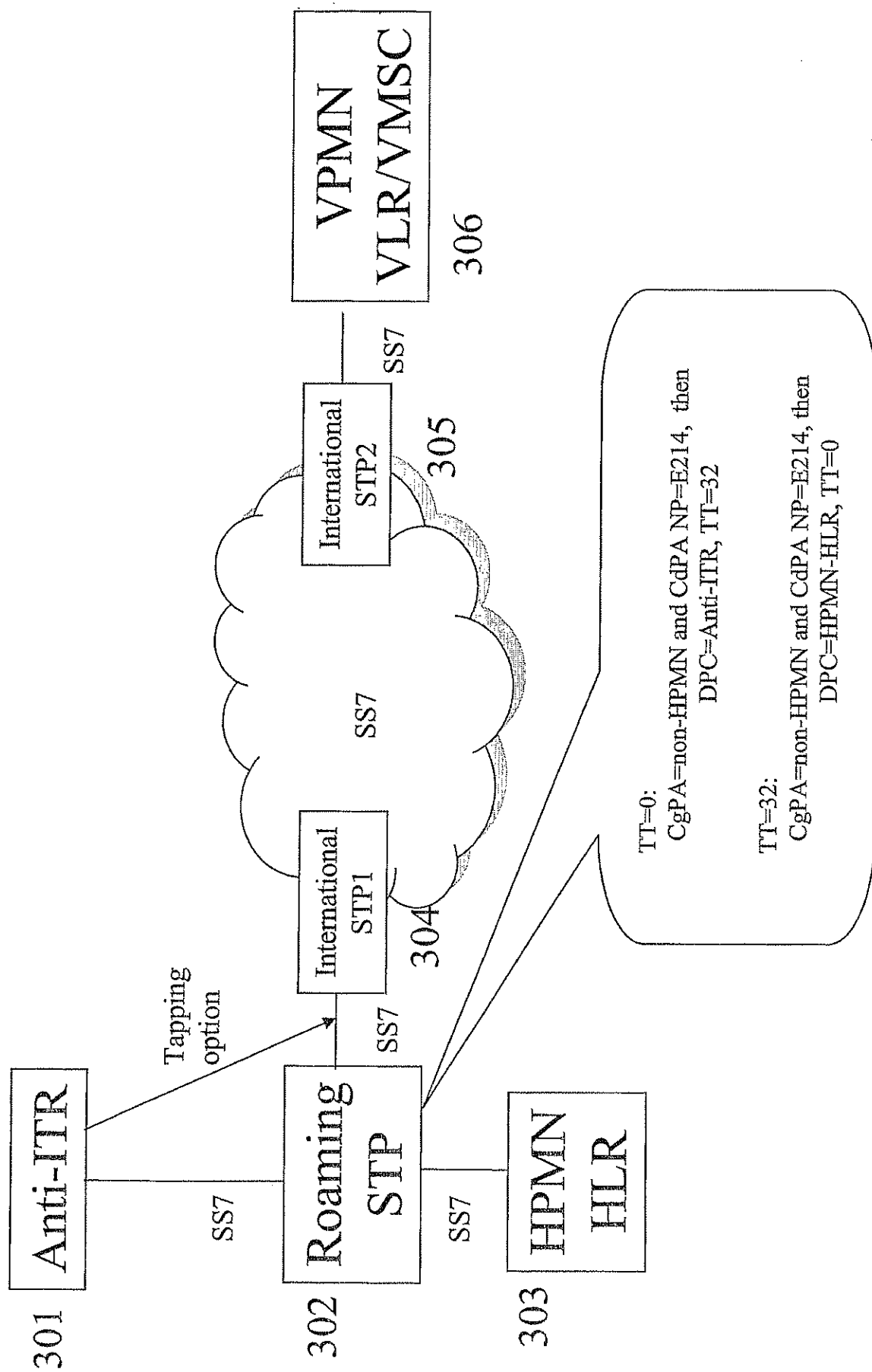


Figure 3: Using Translation Type

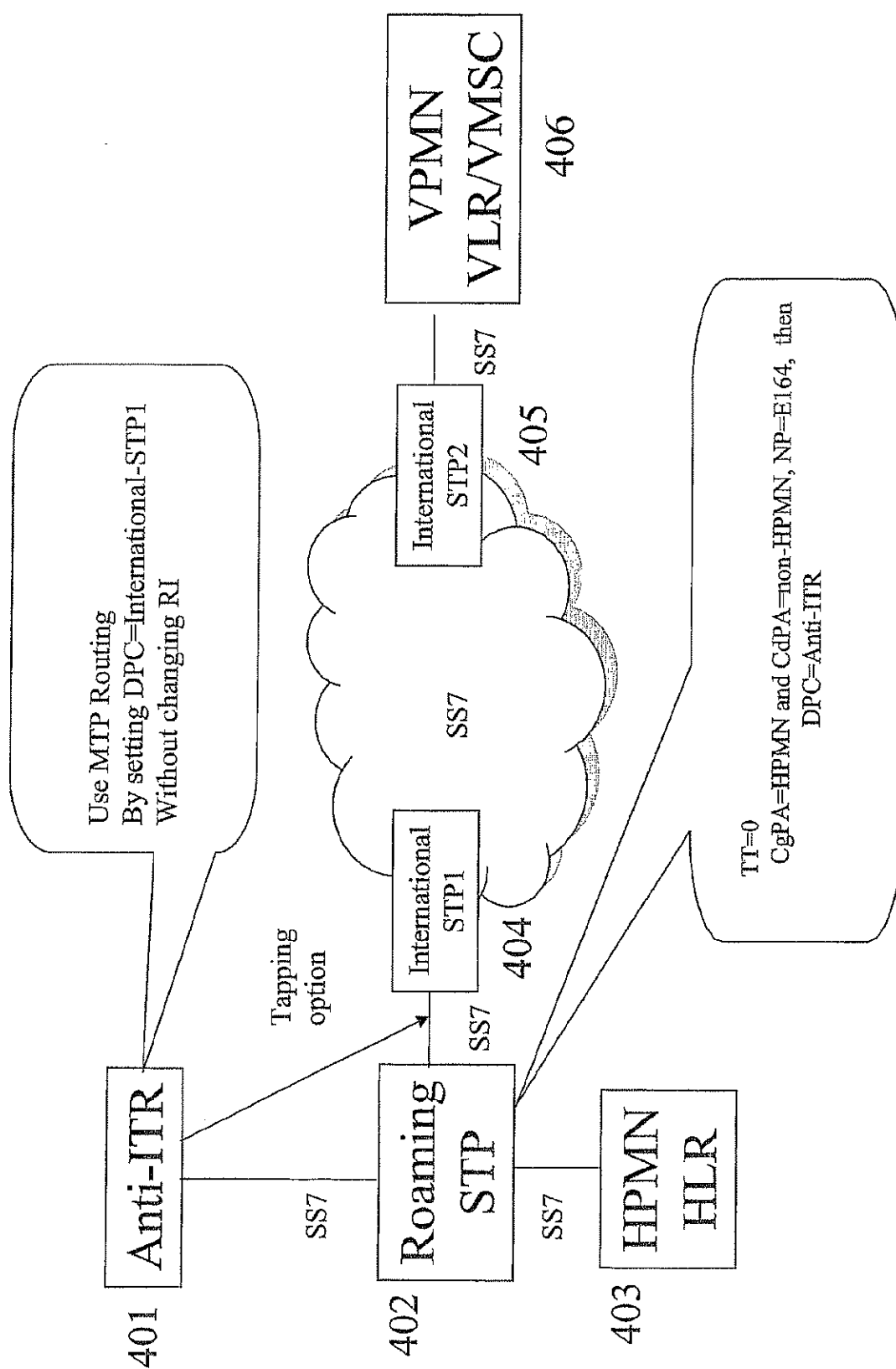


Figure 4: Not using Translation Type

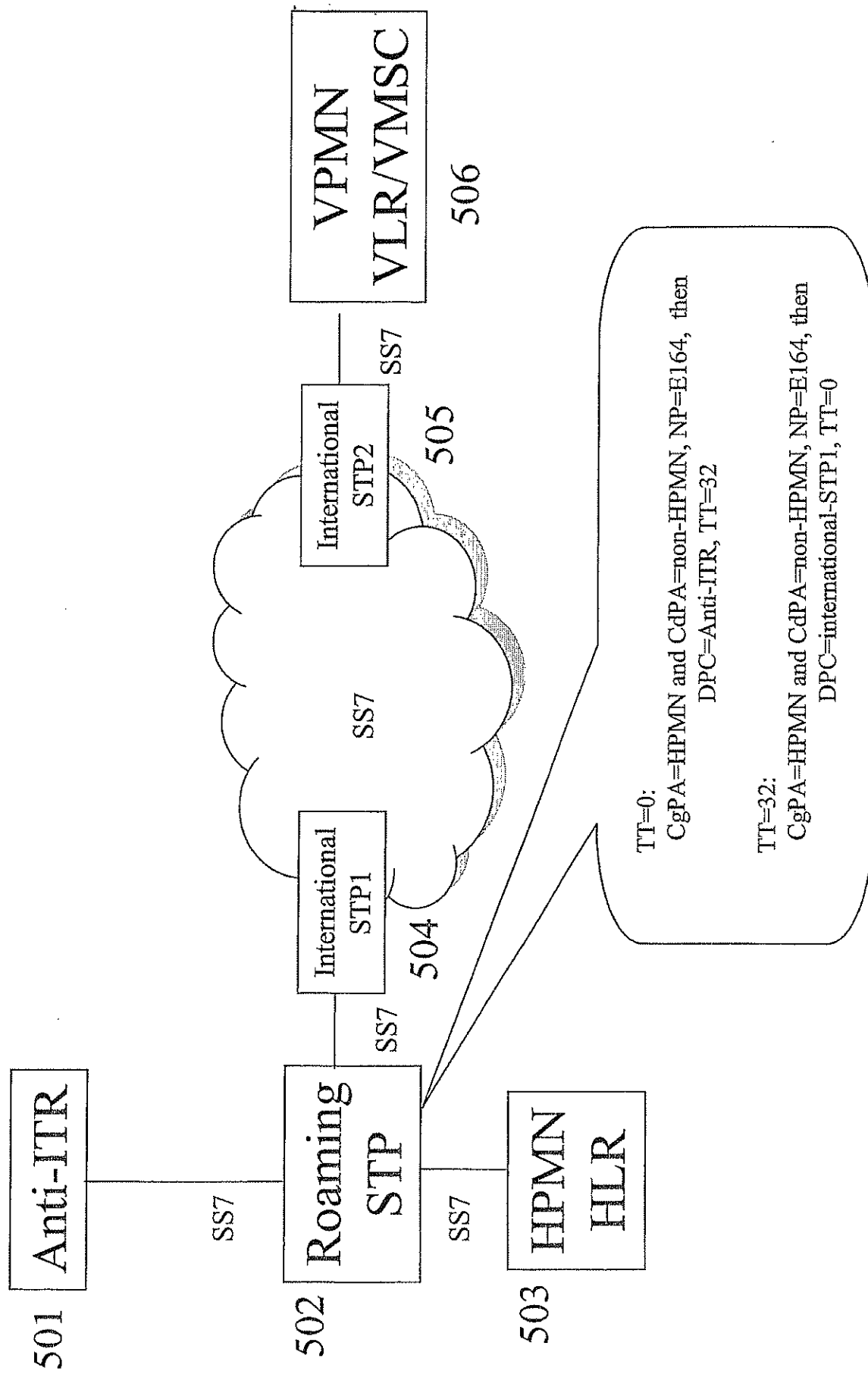


Figure 5: Using Translation Type

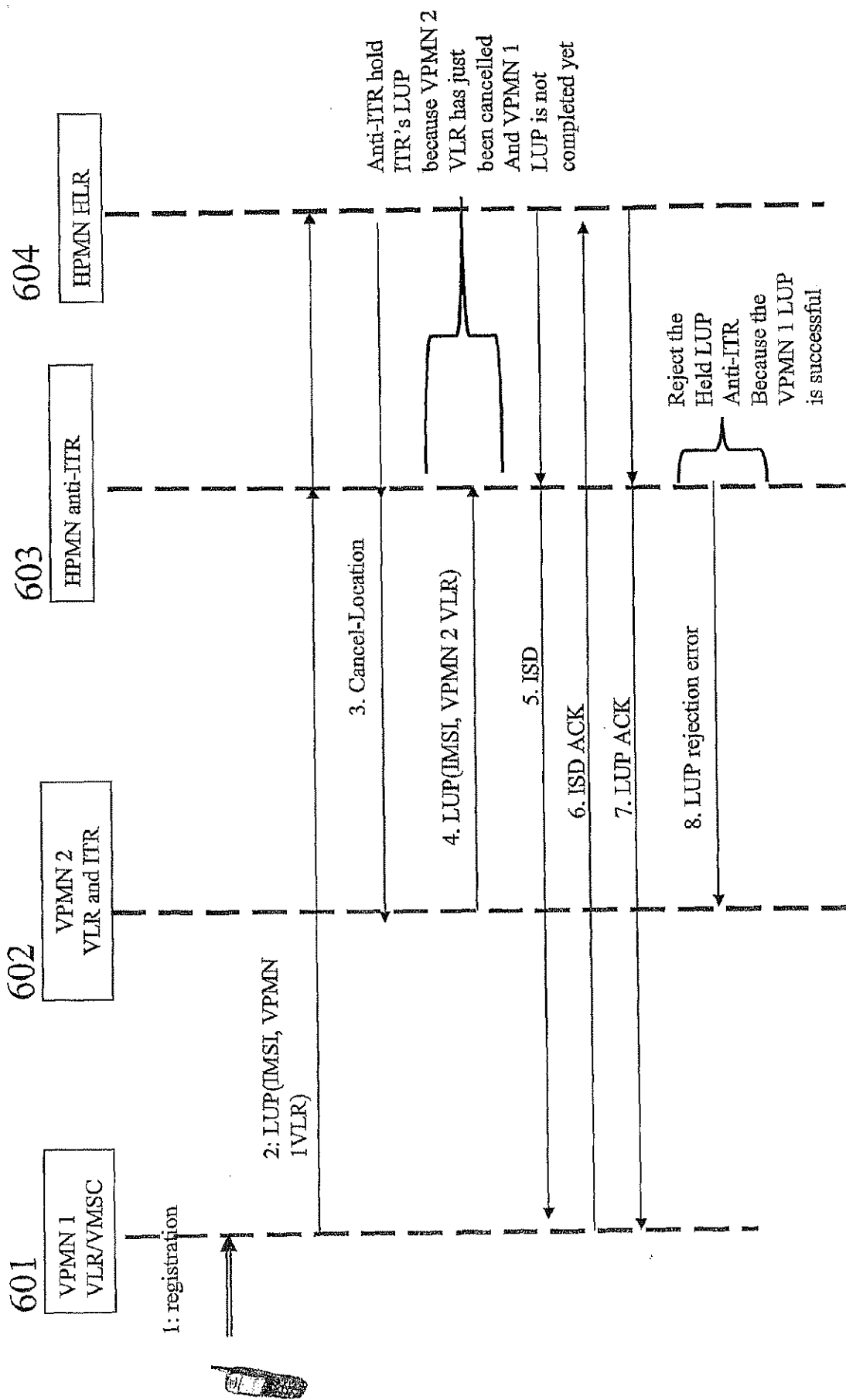


Figure 6: Anti-ITR signal flows

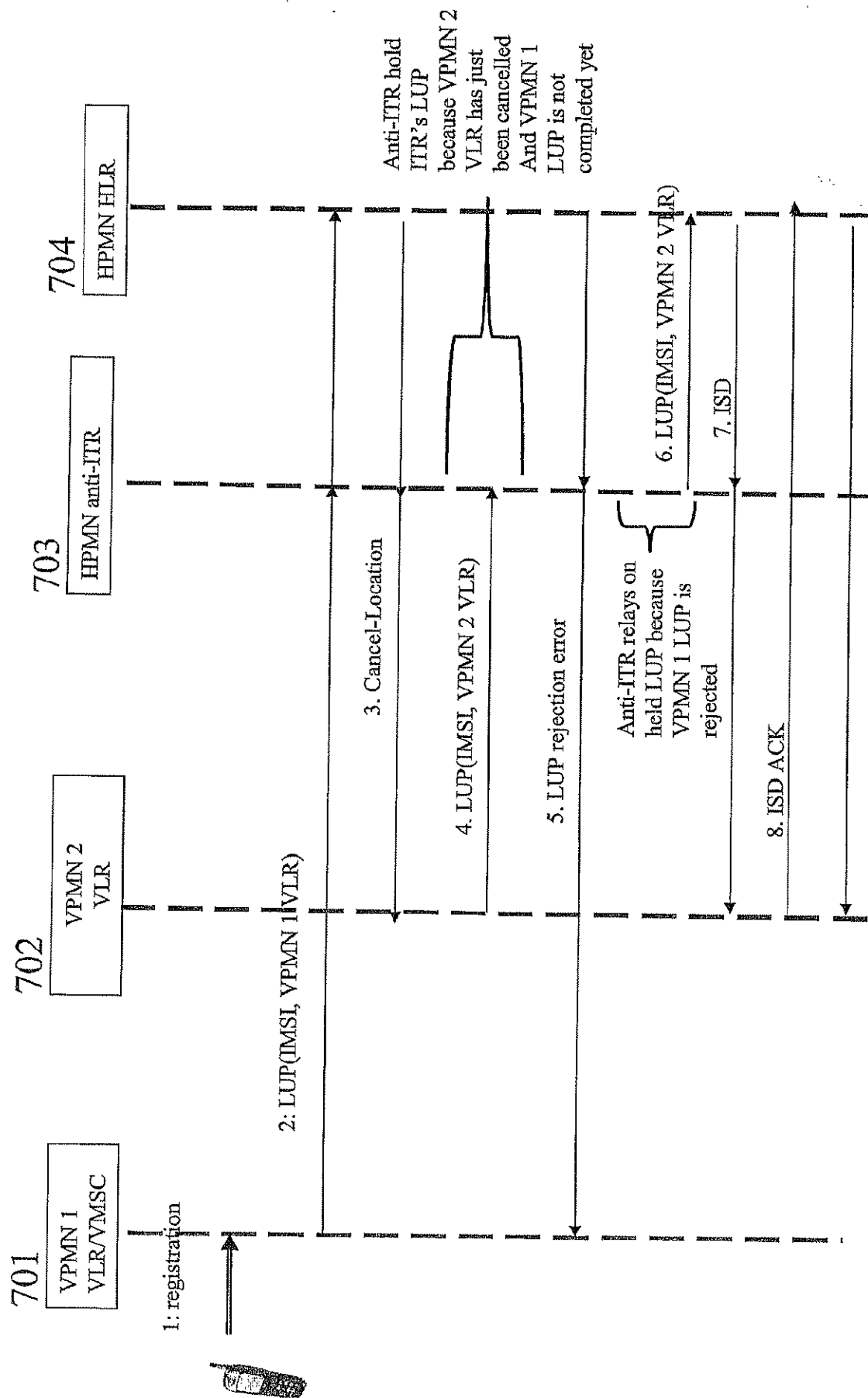


Figure 7: Genuine concurrent registration