

ABSTRACT

Embodiments of the present application provide an information flow recognition method, a network chip, and a network device. In the embodiment of the present application, a combination of an on-chip memory and an off-chip memory is used for recognizing an information flow. Firstly, a potential flow is recognized by taking advantage of a large access bandwidth of the on-chip memory, which can increase the speed of recognizing the potential flow. Secondly, only the potential flow is further recognized by taking advantage of a large storage space of the off-chip memory, rather than further recognizing all information flows by the off-chip memory, which can reduce the number of accesses to the off-chip memory and help improve the efficiency and accuracy of recognizing a target flow.

FIG. 1A